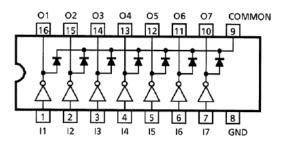


## MDC\_TBD62003AFWG\_LT

# LTspice Model ARRAY TOSHIBA TBD62003AFWG

## **Model Information**

## (top view)



 Model
 An original macro model

 Call Name
 MDC\_TBD62003AFWG\_LT

 Pin Assign
 1:11 2:12 3:13 4:14 5:15 6:16 7:17 8:GND 9:COM 10:O7 11:O6 12:O5 13:O4 14:O3 15:O2 16:O1

 File List
 Model Library
 MDC\_TBD62003AFWG\_LTO1.lib

 Model Report
 MDC\_TBD62003AFWG\_LT.pdf (this file)

Verified Simulator Version Note

LTspice version XVII

#### References

The information which was used for modeling is as follow:

[Data Sheet]	
Date/Version	2015-07-24
Product name	TBD62003AFWG
Company name	Toshiba Corporation
Characteristics	<pre>lleakVout[Temp],Ronlout[Temp],linVin[Temp],linVin[Temp]2,</pre>
	IrVr[Temp],VfIf[Temp],SwVout[Tname]

#### Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range		Unit	
	Min.		Max.	
Temperature	-55	to	150	deg C



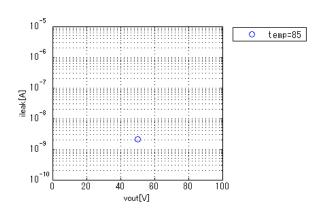
ARRAY		O : Implemented × : Not Implemented — : Not applicable	
Model Functions Table	RANK=1		
Functions	RANK	Implemented	
Ileak-Vout	1	0	
Ron-Iout	1	0	
Iin-Vin	1	0	
Ir-Vr	1	0	
Vf-If	1	0	
Sw-Vout	1	0	



# Simulation results are following. Explanatory notes -: simulated

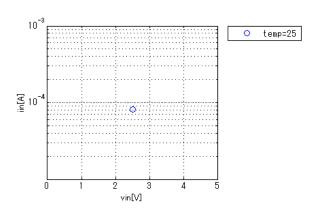
## lleakVout[Temp]

vin = 0V, icom = 0A



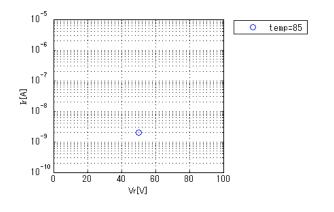
linVin[Temp]

iout = 0A, icom = 0A





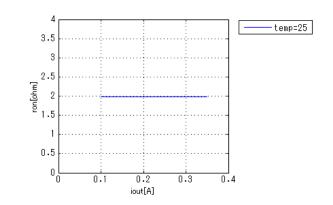
iin = 0A, ignd = 0A



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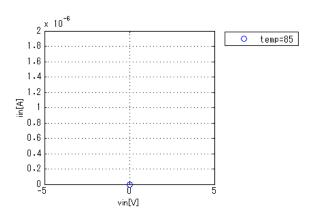
## Ronlout[Temp]

vin = 5V, icom = 0A

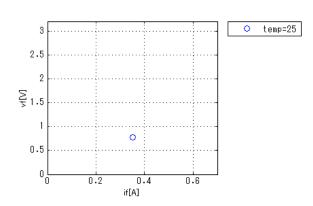


## linVin[Temp]2

iout = 0A, icom = 0A



**Vflf[Temp]** iin = 0A, ignd = 0A

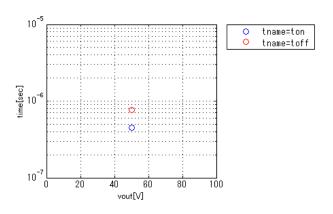




Simulation results are following. Explanatory notes -: simulated

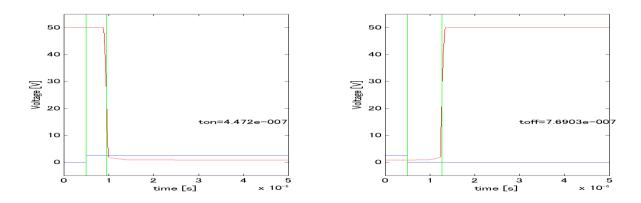
#### SwVout[Tname]

vin = 2.5V, RG = 50hm, RL = 1250hm, icom = 0A, temp = 25degC



#### Switching Waveform ( Blue : INPUT Red : OUTPUT )

vin = 2.5V, vout = 50V, RG = 50hm, RL = 1250hm, icom = 0A, temp = 25degC





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