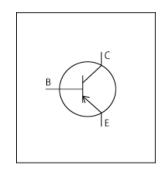


PSpice Model PNP ON NSS20300MR6T1G



Model Information

Model Gummel-Poon model

Call Name MDC NSS20300MR6T1G PS

Pin Assign 1:C 2:B 3:E

File List Model Library MDC_NSS20300MR6T1G_PS02.lib

Model Report MDC_NSS20300MR6T1G_PS.pdf (this file)

Verified Simulator Version

Note

PSpice version 16.6

References

The information which was used for modeling is as follow:

[Data Sheet]

Date/Version Rev.2

Product nameCompany nameNSS20300MR6T1GON Semiconductor.

● Characteristics Vce(sat)lc[Temp],hFElc[Temp],Vbe(sat)lc[Temp],lcVbe[Tem

p],Vce(sat)Ib[Ic],Cib,Cob

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

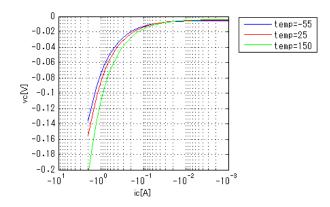
Item	Range			Unit
	Min.		Max.	
Collector-emitter voltage (DC)	0	to	-20	V
Base current (DC)	0	to	-0.30	Α
Temperature	-55	to	150	deg C



Simulation results are following. Explanatory notes — : simulated

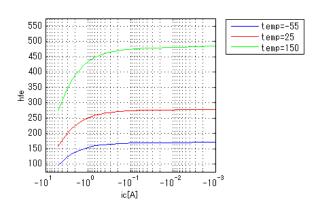
Vce(sat)lc[Temp]

IC/IB = 10



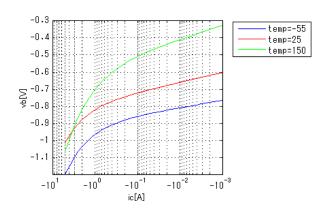
hFEIc[Temp]

Vce = -2V



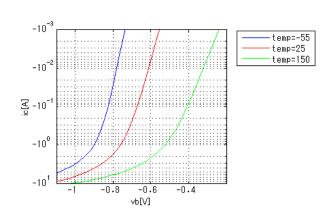
Vbe(sat)Ic[Temp]

IC/IB = 10



IcVbe[Temp]

Vce = -1V

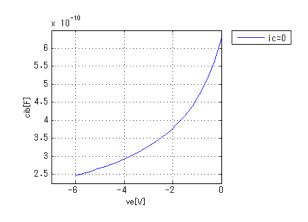


Vce(sat)lb[lc]

0 ic=-0.1 -0.1 ic=-0.3 -0.2 ic=-0.5 -0.3 -0.4 -0.5 -0.6 -0.7 -0.8 -0.9 -10-2 -10-4 -10⁻³ -10 ib[A]

Cib

Freq. = 1MHz

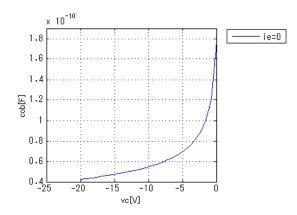




Simulation results are following. Explanatory notes — : simulated

Cob

Freq. = 1MHz





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