

# LTspice Model Low Dropout Voltage Regulator with Reset New Japan Radio NJM2801U1-3328

## **Model Information**

Model A macro model

Call Name MDC\_NJM2801U1-3328\_LT Pin Assign 1:VOUT 2:GND 3:Cd 4:VOR 5:VIN

File List Model Library MDC\_NJM2801U1-3328\_LT01.lib

Model Report MDC\_NJM2801U1-3328\_LT.pdf(this file)

Verified Simulator Version LTspice XVII

Note

#### References

The information which was used for modeling is as follow:

[Data Sheet]

Date/VersionProduct nameVer.2006-07-05NJM2801U1-3328

Company name
New Japan Radio Co.,Ltd.

[Characteristics listed]

Characteristics IQ

Vo, Io, dVo/dVin,dVi\_o

VRT, VRTH, RORL, td, VOPL

#### **Simulation Condition**

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

ltem	Condition			Lloit
	Min	Тур	Max	Unit
Input voltage			14	V
Temperature		25		deg C



## **Model Functions Table**

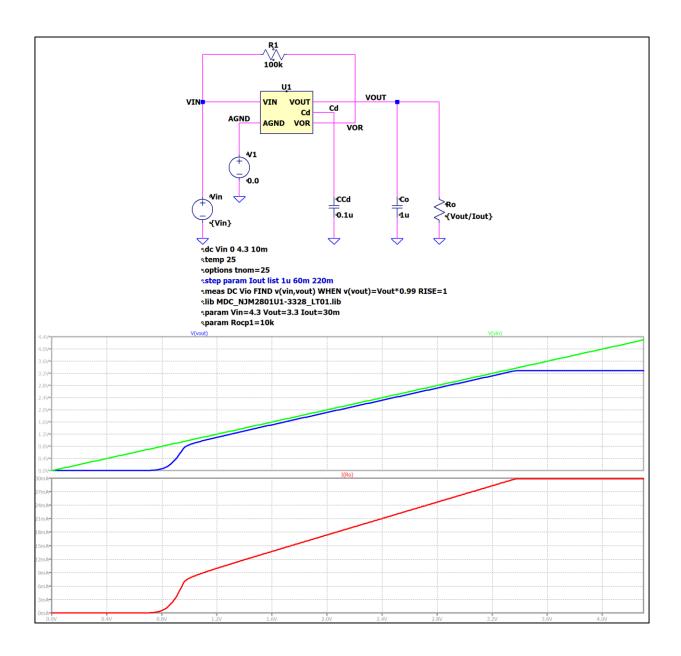
Functions	Implemented
Adjust reset delay time with external capacitor.	0
Internal Short Circuit Current Limit	0

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#### Testbench and simulated results for Vout-Vin

## **Referred to Data Sheet**



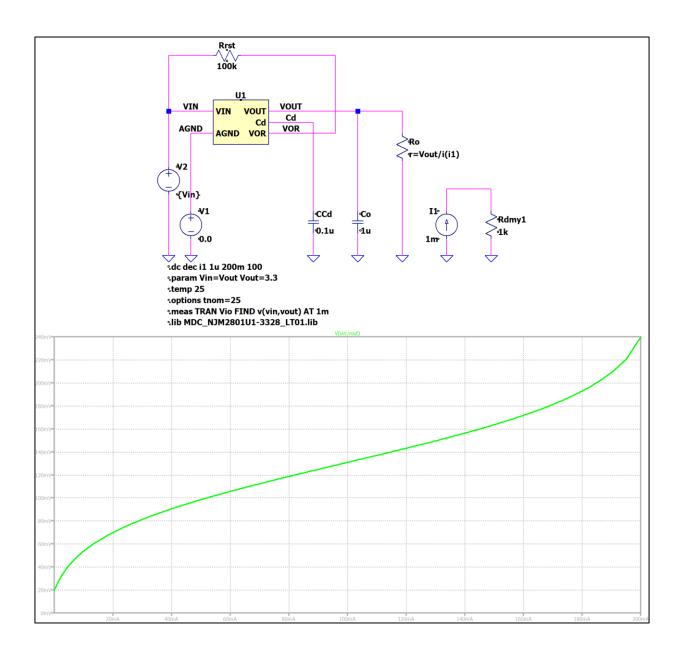
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#### Testbench and simulated results for Vio

# **Referred to Data Sheet**



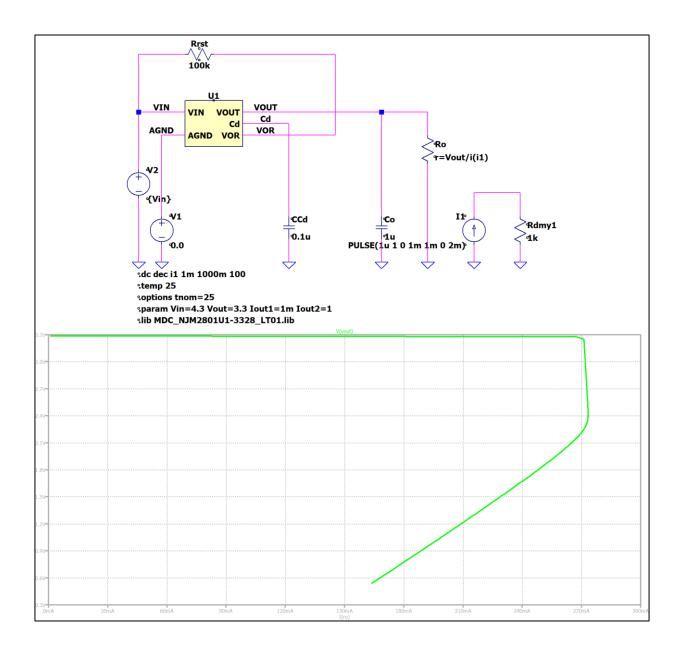
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### Testbench and simulated results for current limit function

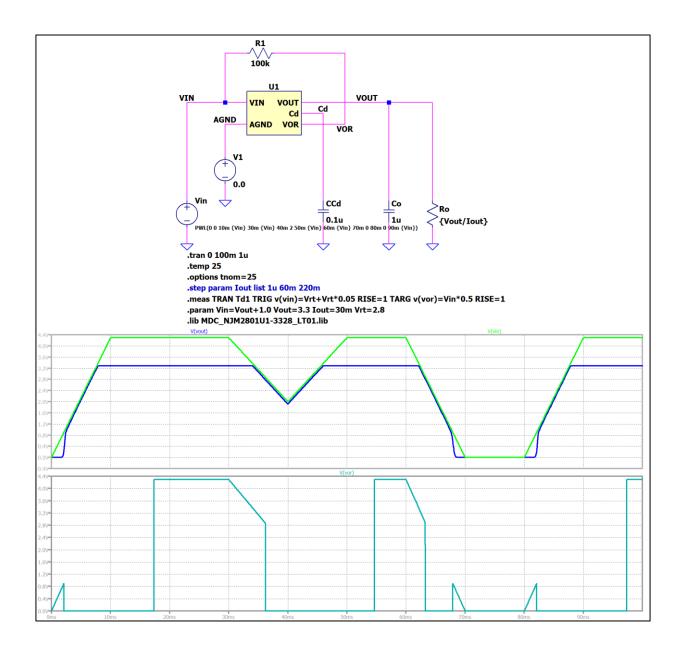
# **Referred to Data Sheet**





Testbench and simulated results for input and output transient response

# **Referred to Data Sheet**





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