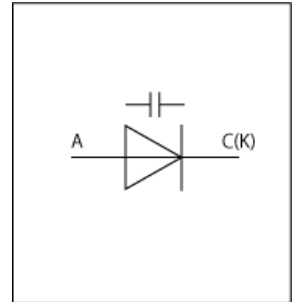


PSpice Model

Variable Capacitance Diode

ON

SVC371



Model Information

Model A macro model based on general SPICE diode model
Call Name MDC_SVC371_PS
Pin Assign 1:A 2:C
File List Model Library MDC_SVC371_PS01.lib
 Model Report MDC_SVC371_PS.pdf (this file)

Verified Simulator Version PSpice version 16.6
Note

References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version None
- Product name SVC371
- Company name ON Semiconductor.
- Characteristics CjVr

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

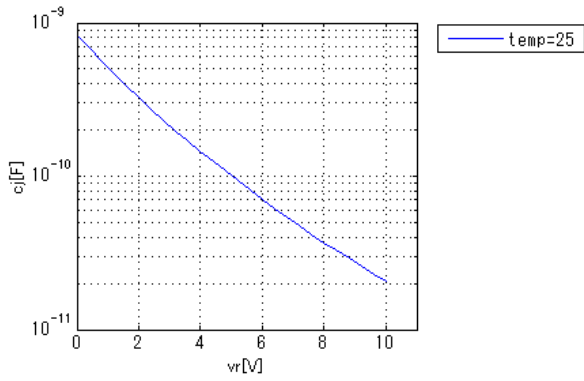
Item	Range			Unit
	Min.		Max.	
Reverse Voltage	0	to	32	V
Temperature	-55	to	125	deg C

Simulation results are following.

Explanatory notes — : simulated

CjVr

Freq. = 1MHz



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