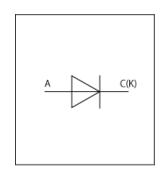


# LTspice Model ESD Protection Diode VISHAY SE10FJHM3



## **Model Information**

Model A macro model based on general SPICE diode model

Call Name MDC\_SE10FJHM3\_LT

Pin Assign 1:A 2:C

File List Model Library MDC\_SE10FJHM3\_LT01.lib

Model Report MDC\_SE10FJHM3\_LT.pdf (this file)

**Verified Simulator Version** 

Note

LTspice version XVII

#### References

The information which was used for modeling is as follow:

[Data Sheet]

Date/VersionProduct name03-Feb-15SE10FJHM3

Company name Vishay Intertechnology, Inc.

● Characteristics IfVf[Temp],IrVr[Temp],CjVr,IrVr(Breakdown)

#### Simulation Range

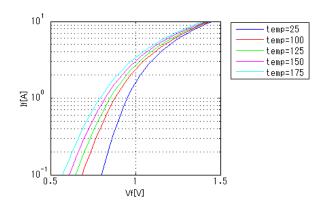
This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Forward Voltage	0	to	15	V
Reverse Voltage	0	to	1,200	V
Temperature	-55	to	175	deg C

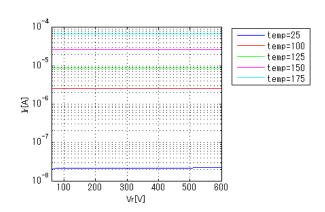


Simulation results are following. Explanatory notes — : simulated

# IfVf[Temp]

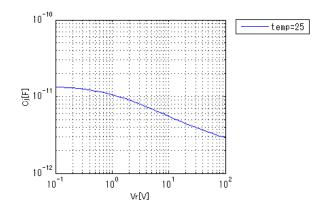


# IrVr[Temp]

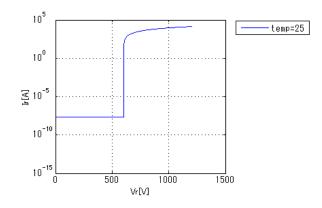


#### CjVr

Freq. = 1MHz



## IrVr(Breakdown)





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