

MDC_FN18X104K500PBG_LT

LTspice Model Ceramic Chip Capacitors Prosperity Dielectrics MDC_FN18X104K500PBG_LT

Model Information

Model	A macro mode		
Call Name	MDC_FN18X104K500PBG_LT		
Pin Assign	1:1 2:2 (No Polarity)		
File List	Model Library	MDC_FN18X104K500PBG_LT.lib	
	Model Report	MDC_FN18X104K500PBG_LT.pdf(this file)	
Verified Simulator Version		LTspice XVII	
Note			

References

The information which was used for modeling is as follow:

[Data Sheet]

Date/Version	
Product name	
Company name	

2020.11.02/FN-000-001-17 FN18X104K500PBG Prosperity Dielectrics Co., Ltd.

[Characteristics listed]

Characteristics

Impedance vs Frequency

Simulation Condition

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Condition	Unit
Temperature	25	deg C



Model Functions Table

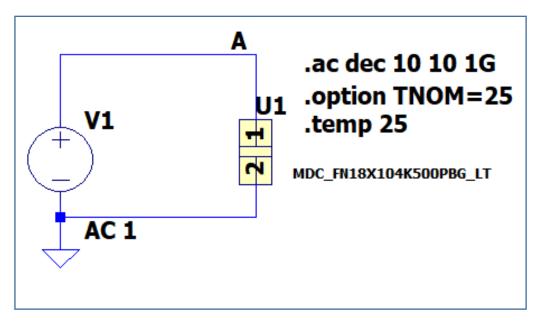
Functions	Implemented
Capacitance , Impedance vs Frequency	0



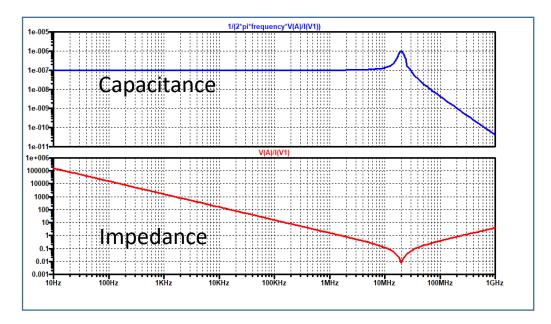


Capacitance, Impedance vs Frequency

TestBench



Simulation results are following. Explanatory notes — : simulated





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