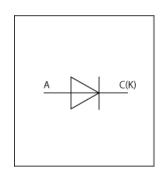


LTspice Model Rectifier Diode TAIWAN SEMICONDUCTOR HT18GA1



Model Information

Model A macro model based on general SPICE diode model

Call Name MDC HT18GA1 LT

Pin Assign 1:A 2:C

File List Model Library MDC_HT18GA1_LT01.lib

Model Report MDC_HT18GA1_LT.pdf (this file)

Verified Simulator Version

Note

LTspice version XVII

References

The information which was used for modeling is as follow:

[Data Sheet]

Date/VersionProduct nameUnknownHT18GA1

Company name Taiwan Semiconductor Co., Ltd.

● Characteristics IrVr[Temp],IfVf[Temp],CjVr,Trrlflr,TrrWaveform,SurgeWavef

orm

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Reverse Voltage	0	to	1,000	V
Temperature	-55	to	150	deg C



Model Functions Table

Diode

O: Implemented

× : Not Implemented

—: Not applicable

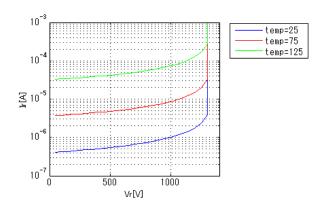
_			
	$^{\Lambda}$	JK	

Functions	RANK	Implemented
IF-VF(Temp)	1	0
IR-VR(Temp)	1	0
Capacitance	1	0
Reverse recovery characteristics	1	0
Rectification characteristics(Bridge)	1	1
Surge voltage-Transient	2	_

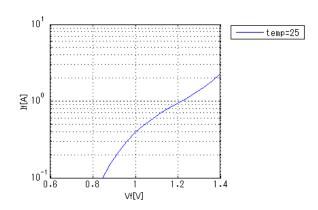


Simulation results are following. Explanatory notes — : simulated

IrVr[Temp]

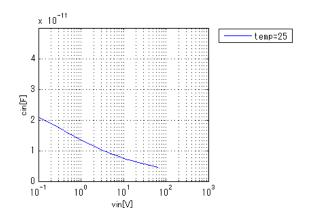


IfVf[Temp]



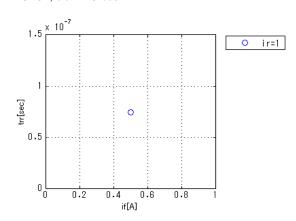
CjVr

Freq = 1000000Hz



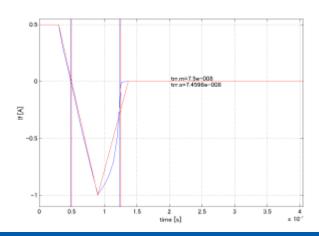
Trrlflr

irr = 0.25A, didt = 25A/us



TrrWaveform (Red: Datasheet Blue: Simulation)

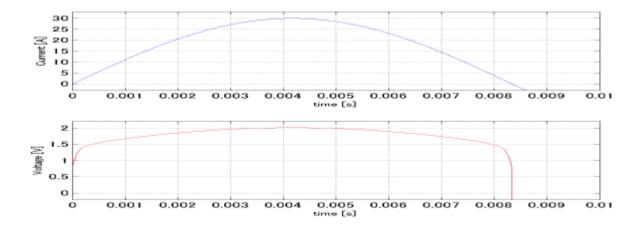
If = 0.5A, ir = 1A, irr = 0.25A, didt = 25A/us





Simulation results are following. Explanatory notes — : simulated

Surge Waveform (60Hz half sine wave)





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