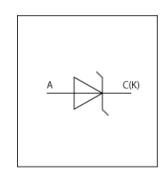


PSpice Model TVS SHINDENGEN ST06-33CE



Model Information

Model A macro model based on general SPICE diode model

Call Name MDC_ST06-33CE_PS

Pin Assign 1:C 2:A

File List Model Library MDC_ST06-33CE_PS01.lib

Model Report MDC_ST06-33CE_PS.pdf (this file)

Verified Simulator Version

Note

PSpice version 17.2

References

The information which was used for modeling is as follow:

[Data Sheet]

Date/VersionProduct name2020.01ST06-33CE

Company name Shindengen Electric Manufacturing Co., Ltd.

● Characteristics Vrlr[Temp],BvTemp,IrTemp[Vr],CapacitanceFreq[Vr],CjVr,Su

rgeCurrentWaveform

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Reverse Voltage	33	to	33	V
Temperature	-55	to	175	deg C



Model Functions Table

Diode

O: Implemented

×: Not Implemented

—: Not applicable

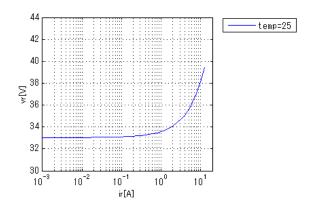
RANK=1

	IV WINT	
Functions	RANK	Implemented
IF-VF(Temp)	1	_
IR-VR(Temp)	1	0
BV(Temp)	1	0
Capacitance	1	0
Reverse recovery characteristics	1	
Zz-Iz	1	_
Rectification characteristics(Bridge)	1	_
Surge-Transient	1	0
tlp	1	_

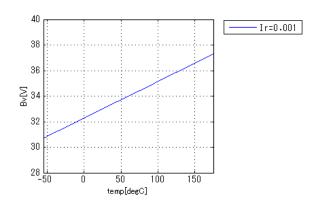


Simulation results are following. Explanatory notes — : simulated

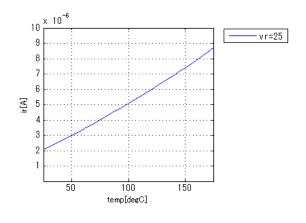
Vrlr[Temp]



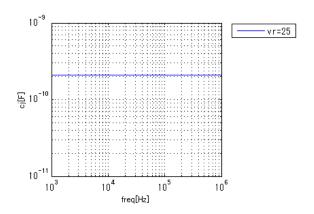
BvTemp



IrTemp[Vr]

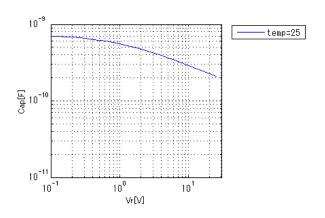


CapacitanceFreq[Vr]



CjVr

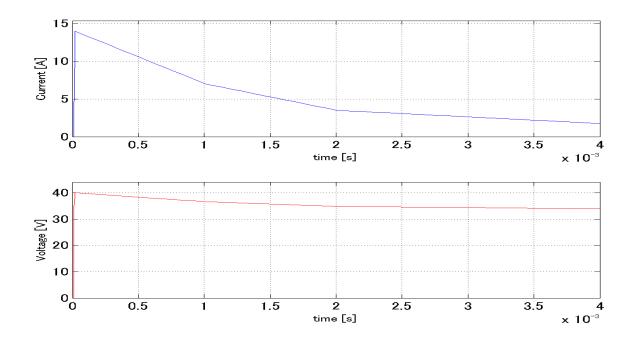
Freq = 100000Hz





Simulation results are following. Explanatory notes — : simulated

Surge Current Waveform (Reverse 10u/1000u)





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