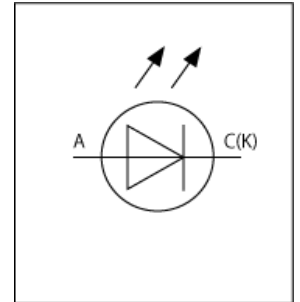


# LTspice Model

## Light Emitting Diode

### STANLEY

### FKR3C64X-H



### Model Information

**Model** A macro model based on general SPICE diode model  
**Call Name** MDC\_FKR3C64X-H\_LT  
**Pin Assign** 1:A 2:C  
**File List** Model Library MDC\_FKR3C64X-H\_LT02.lib  
 Model Report MDC\_FKR3C64X-H\_LT.pdf (this file)

**Verified Simulator Version** LTspice version XVII  
**Note**

### References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version 2014.09.10
- Product name FKR3C64X-H
- Company name STANLEY ELECTRIC CO., LTD.
- Characteristics IfVf[Temp], VfTemp[If], IrVr[Temp]

### Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

| Item            | Range |    |      | Unit  |
|-----------------|-------|----|------|-------|
|                 | Min.  |    | Max. |       |
| Reverse Voltage | 5     | to | 5    | V     |
| Temperature     | -40   | to | 100  | deg C |

## Diode

○ : Implemented  
× : Not Implemented  
— : Not applicable

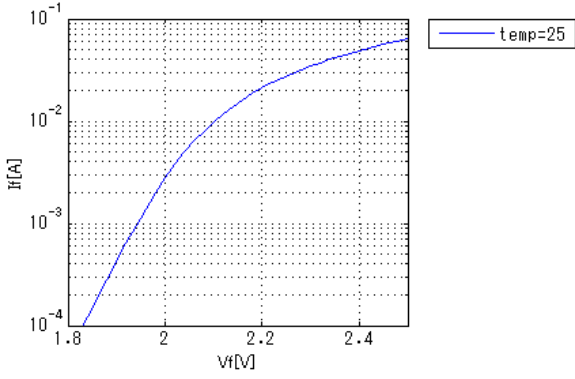
**Model Functions Table**

RANK=1

| Functions                             | RANK | Implemented |
|---------------------------------------|------|-------------|
| IF-VF(Temp)                           | 1    | ○           |
| IR-VR(Temp)                           | 1    | ○           |
| Capacitance                           | 1    | —           |
| Reverse recovery characteristics      | 1    | —           |
| Zz-Iz                                 | 1    | —           |
| Rectification characteristics(Bridge) | 1    | —           |
| Surge voltage-Transient               | 1    | —           |

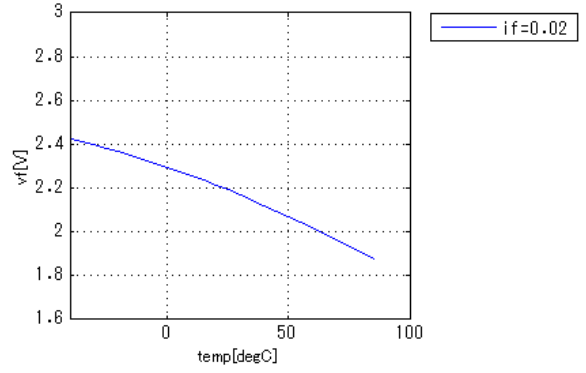
Simulation results are following.  
 Explanatory notes — : simulated

**IfVf[Temp]**

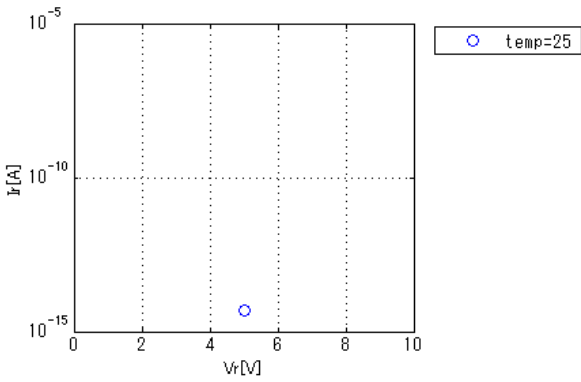


**VfTemp[If]**

if = 20e-3



**IrVr[Temp]**



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