

PSpice Model

ESD Protection Diode

JCET

CESD12VAP



Model Information

Model A macro model based on general SPICE diode model
Call Name MDC_CESD12VAP_PS
Pin Assign 1:A 2:C
File List Model Library MDC_CESD12VAP_PS01.lib
 Model Report MDC_CESD12VAP_PS.pdf (this file)

Verified Simulator Version PSpice version 16.6
Note

References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version C, Feb,2014
- Product name CESD12VAP
- Company name Jiangsu Changjiang Electronics Technology Co., Ltd.
- Characteristics IfVf[Temp],CjVr,IrVr(Breakdown)

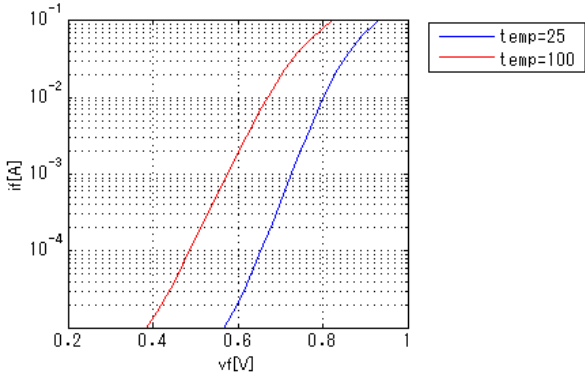
Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Forward Voltage	0	to	9.5	V
Reverse Voltage	0	to	29.8	V
Temperature	-55	to	150	deg C

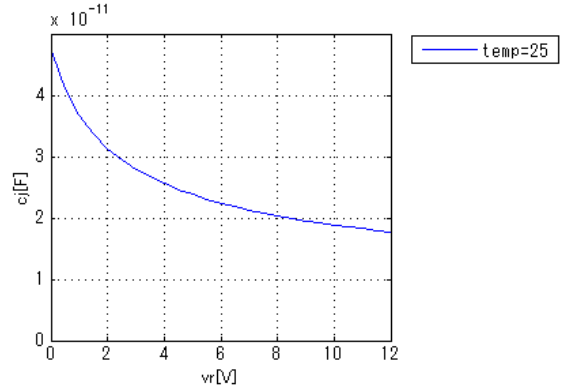
Simulation results are following.
 Explanatory notes — : simulated

IfVf[Temp]

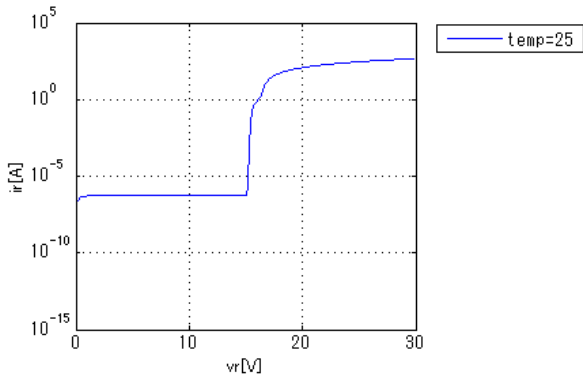


CjVr

Freq. = 1MHz



IrVr(Breakdown)



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