

MDC_KSA1156_PS

PSpice Model PNP ON KSA1156

Model Information

ModelGummel-Poon modelCall NameMDC_KSA1156_PSPin Assign1:C 2:B 3:EFile ListModel Library
Model Report

MDC_KSA1156_PS02.lib MDC_KSA1156_PS.pdf (this file)

Verified Simulator Version Note

PSpice version 16.6

References

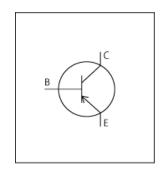
The information which was used for modeling is as follow:

Rev.A, February 2000 KSA1156 ON Semiconductor. hFElc[Temp],Vce(sat)lc[Temp],Vbe(sat)lc[Temp]

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Collector-emitter voltage (DC)	0	to	-400	V
Base current (DC)	0	to	-0.250	А
Temperature	-55	to	150	deg C

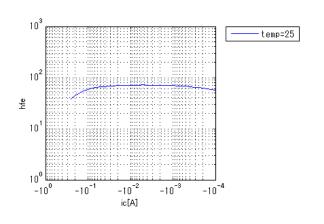




Simulation results are following. Explanatory notes -: simulated

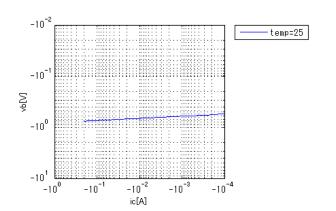
hFElc[Temp]





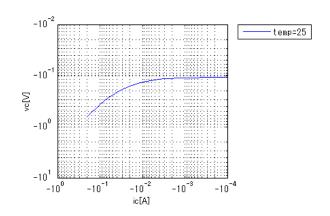
Vbe(sat)Ic[Temp]

IC/IB = 10



Vce(sat)lc[Temp]







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