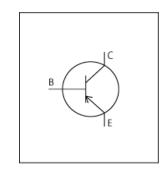


LTspice Model PNP ON KSH210



Model Information

Model Mextram model
Call Name MDC_KSH210_LT
Pin Assign 1:C 2:B 3:E

Model Library MDC_KSH210_LT01.lib

Model Report MDC_KSH210_LT.pdf (this file)

Verified Simulator Version

Note

File List

LTspice version XVII

References

The information which was used for modeling is as follow:

[Data Sheet]

Date/Version Rev. A4, October 2002

● Product name KSH210

Company nameON Semiconductor.

● Characteristics hFEIc[Vce], Vce(sat)lc[Temp], Vbe(sat)lc[Temp], Cob

Simulation Range

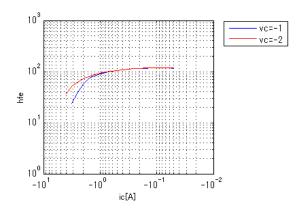
This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Collector-emitter voltage (DC)	0	to	-25	V
Base current (DC)	0	to	-1	А
Temperature	-65	to	150	deg C



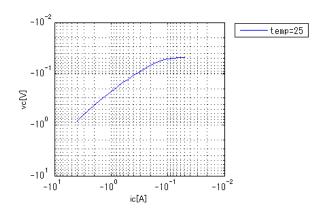
Simulation results are following. Explanatory notes — : simulated

hFEIc[Vce]



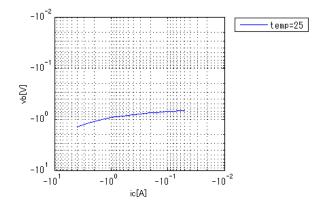
Vce(sat)lc[Temp]

IC/IB = 10



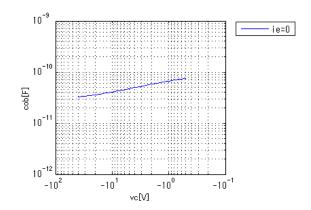
Vbe(sat)lc[Temp]

IC/IB = 10



Cob

Freq. = 1MHz





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