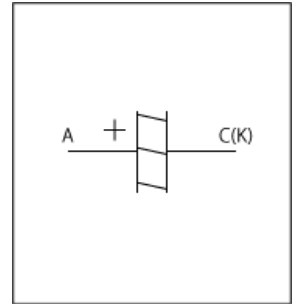


LTspice Model

Aluminum Electrolytic Capacitor

Panasonic

EEEFT1V101AP



Model Information

Model	An original macro model		
Call Name	MDC_EEEFT1V101AP_LT		
Pin Assign	1:A 2:C		
File List	Model Library	MDC_EEEFT1V101AP_LT01.lib	
	Model Report	MDC_EEEFT1V101AP_LT.pdf (this file)	
Verified Simulator Version	LTspice version XVII		
Note			

References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version 00 Nov. 2012
- Product name EEEFT1V101AP
- Company name Panasonic Corporation
- Characteristics CapFreq[Vf],ESRFreq[Vf],ImpedanceFreq[Vf],ESRFreq[Vf]2, CapFreq[Vf]2,InductanceFreq[Vf]

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Temperature	-55	to	105	deg C

Capacitor

○ : Implemented
 × : Not Implemented
 — : Not applicable

Model Functions Table

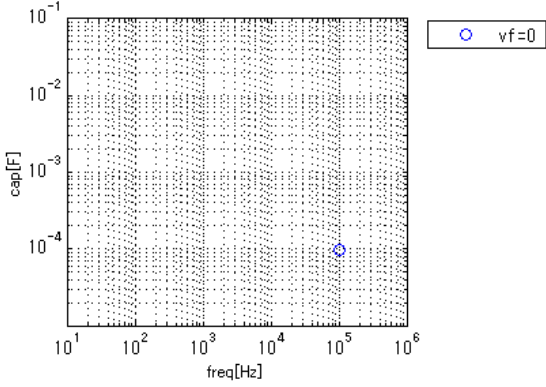
RANK=1

Functions	RANK	Implemented
Impedance-Frequency	1	○
ESR-Frequency	1	○
Capacitance-Frequency	1	○
Inductance-Frequency	1	○
Capacitance-Voltage	1	—
Capacitance-Temperature	1	—

Simulation results are following.
 Explanatory notes — : simulated

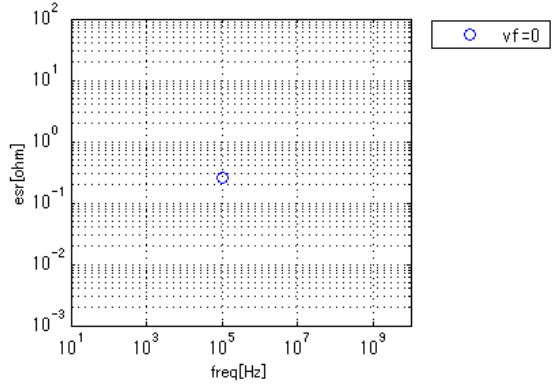
CapFreq[Vf]

Temp = 20degC



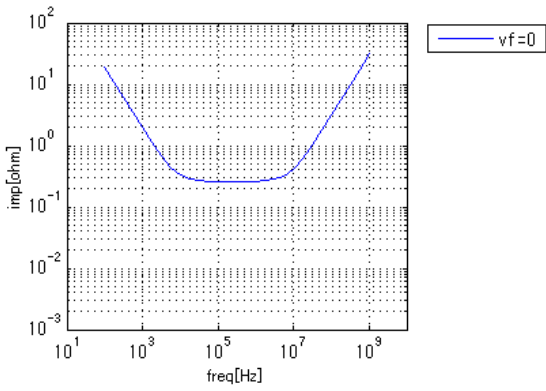
ESRFreq[Vf]

temp = 20degC



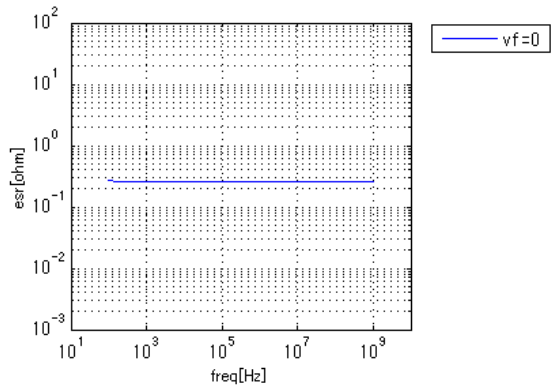
ImpedanceFreq[Vf]

temp = 25degC



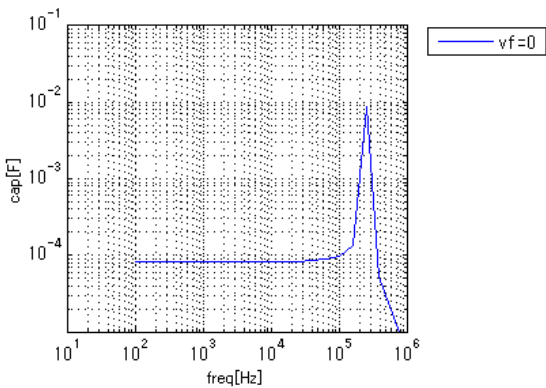
ESRFreq[Vf]2

temp = 25degC



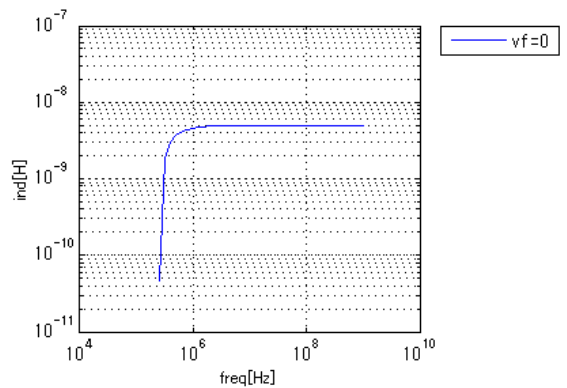
CapFreq[Vf]2

Temp = 25degC



InductanceFreq[Vf]

temp = 25degC



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