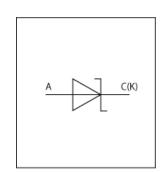


ADS Model TVS SHINDENGEN ST04-24F1



Model Information

Model A macro model based on general SPICE diode model

Call Name MDC_ST04_24F1_AD

Pin Assign 1:C 2:A

File List Model Library MDC_ST04_24F1_AD.zip

Model Report MDC_ST04-24F1_AD.pdf (this file)

Verified Simulator Version

Note

ADS version 2020 Update 2

References

The information which was used for modeling is as follow:

[Data Sheet]

Date/Version Rev.01(2020.01)Product name ST04-24F1

●Company name Shindengen Electric Manufacturing Co., Ltd.

● Characteristics Vrlr[Temp],BvTemp,IrTemp[Vr],CjVr,CapacitanceFreq[Vr],Su

rgeWaveform

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Breakdown voltage	24	to	24	V
Temperature	-55	to	175	deg C



Model Functions Table

Diode

O:Implemented

× : Not Implemented

—: Not applicable

RANK=1

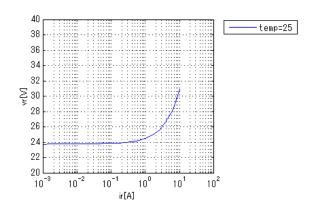
	10 0110 2	
Functions	RANK	Implemented
IF-VF(Temp)	1	_
IR-VR(Temp)	1	0
Capacitance	1	0
Reverse recovery characteristics	1	_
Zz-Iz	1	_
Rectification characteristics(Bridge)	1	_
Surge voltage-Transient	1	0



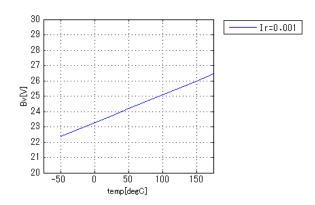
Simulation results are following.

Explanatory notes — : simulated

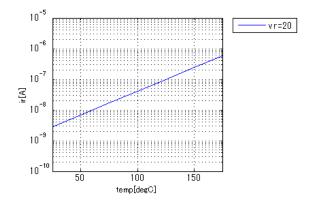
VrIr[Temp]



BvTemp

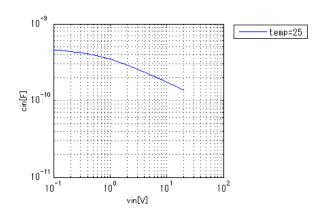


IrTemp[Vr]

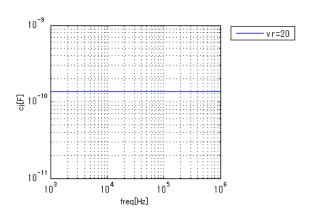


CjVr

Freq = 1000000Hz



CapacitanceFreq[Vr]

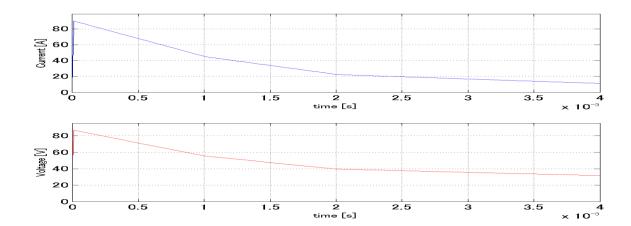




Simulation results are following.

Explanatory notes — : simulated

Surge Waveform





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