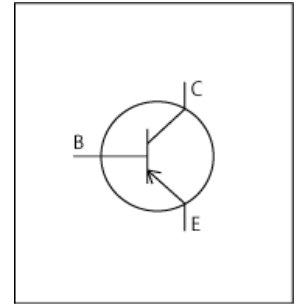


# PSpice Model

## PNP

## ON

## NSV1C200MZ4T1G



### Model Information

**Model** Gummel-Poon model  
**Call Name** MDC\_NSV1C200MZ4T1G\_PS  
**Pin Assign** 1:C 2:B 3:E  
**File List** Model Library MDC\_NSV1C200MZ4T1G\_PS02.lib  
 Model Report MDC\_NSV1C200MZ4T1G\_PS.pdf (this file)

**Verified Simulator Version** PSpice version 16.6  
**Note**

### References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version Rev. 5
- Product name NSV1C200MZ4T1G
- Company name ON Semiconductor.
- Characteristics  $hFEIc[Temp]$ ,  $Vce(sat)Ic[Temp]$ ,  $Vbe(sat)Ic[Temp]$ ,  $IcVbe[Temp]$ ,  $Vce(sat)Ib[Ic]$ ,  $Cib$ ,  $Cob$ ,  $fT[Ic[Vce]]$

### Simulation Range

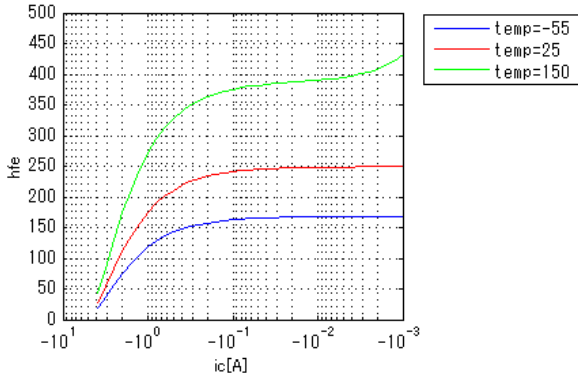
This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Collector-emitter voltage (DC)	0	to	-100	V
Base current (DC)	0	to	-1	A
Temperature	-55	to	150	deg C

Simulation results are following.  
 Explanatory notes — : simulated

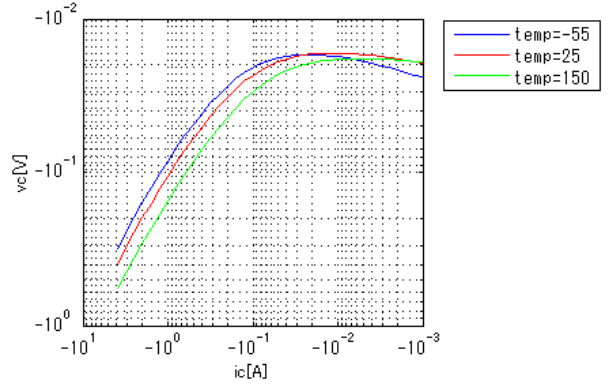
**hFEIc[Temp]**

Vce = -2V



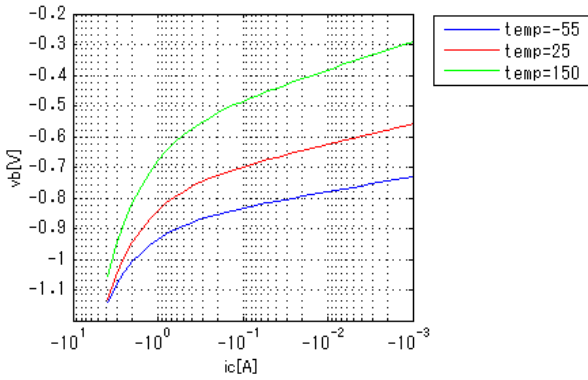
**Vce(sat)Ic[Temp]**

IC/IB = 10



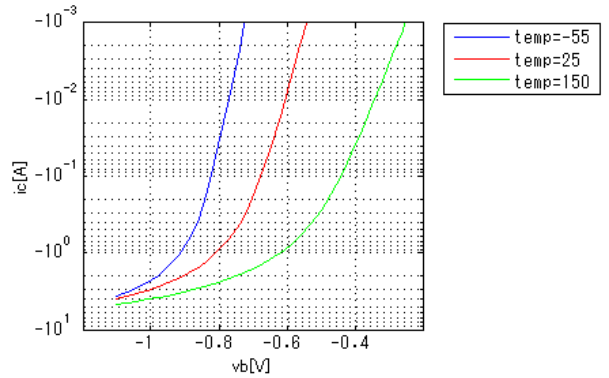
**Vbe(sat)Ic[Temp]**

IC/IB = 10

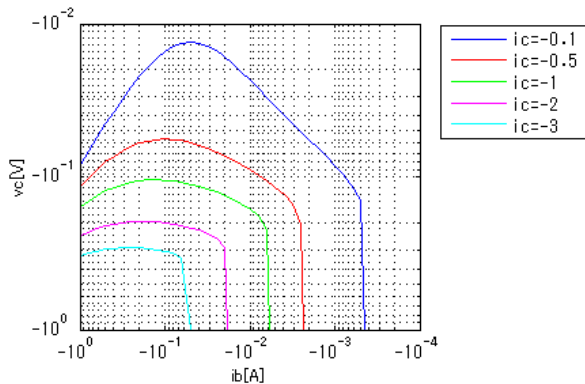


**IcVbe[Temp]**

Vce = -2V

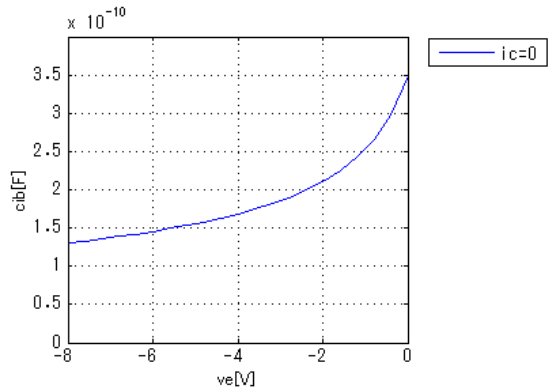


**Vce(sat)Ib[Ic]**



**Cib**

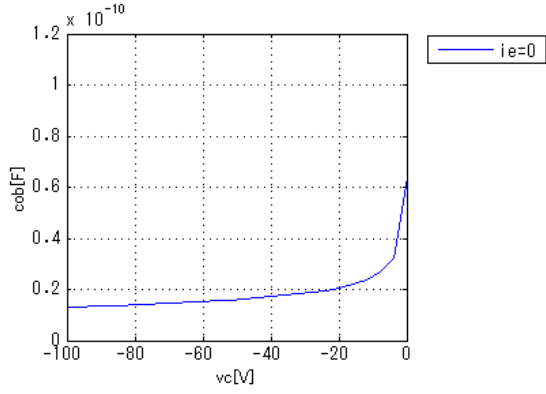
Freq. = 1MHz



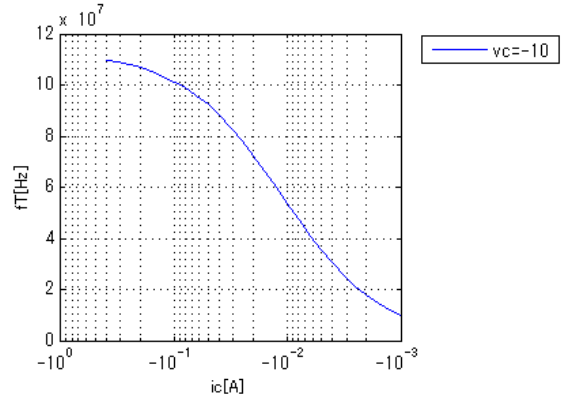
Simulation results are following.  
 Explanatory notes — : simulated

**Cob**

Freq. = 1MHz



**fTic[Vce]**



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