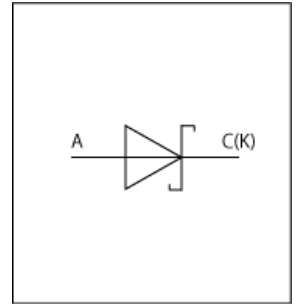


# LTspice Model

## Schottky Diode

### KEC

### KDR105S



### Model Information

**Model** A macro model based on general SPICE diode model  
**Call Name** MDC\_KDR105S\_LT  
**Pin Assign** 1:A 2:C  
**File List** Model Library MDC\_KDR105S\_LT01.lib  
 Model Report MDC\_KDR105S\_LT.pdf (this file)

**Verified Simulator Version** LTspice version XVII  
**Note**

### References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version Rev 3
- Product name KDR105S
- Company name KEC Corporation
- Characteristics IfVf[Temp],IrVr[Temp],CjVr

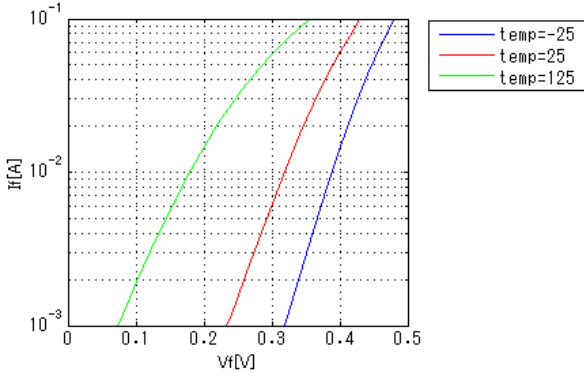
### Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

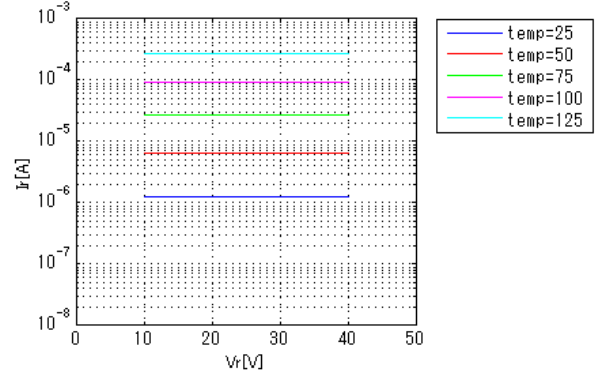
Item	Range			Unit
	Min.		Max.	
Forward Voltage	0	to	5	V
Reverse Voltage	0	to	100	V
Temperature	-55	to	125	deg C

Simulation results are following.  
 Explanatory notes — : simulated

**IfVf[Temp]**

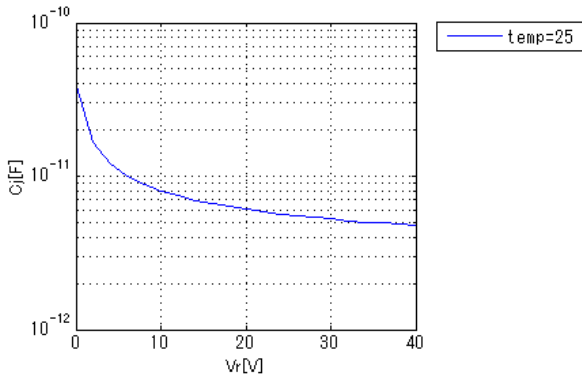


**IrVr[Temp]**



**CjVr**

Freq. = 1MHz



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