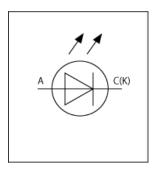


PSpice Model Light Emitting Diode STANLEY VXLW1155GDS-3D7K3-Y-TR



Model Information

 Model
 A macro model based on general SPICE diode model

 Call Name
 MDC_VXLW1155GDS-3D7K3-Y-TR_PS

 Pin Assign
 1:C 2:A

 File List
 Model Library
 MDC_VXLW1155GDS-3D7K3-Y-TR_PS01.lib

 Model Report
 MDC_VXLW1155GDS-3D7K3-Y-TR_PS.pdf (this file)

Verified Simulator Version Note

PSpice version 17.2

References

The information which was used for modeling is as follow:

2020.10.30 VXLW1155GDS-3D7K3-Y-TR STANLEY ELECTRIC CO., LTD. IfVf[Temp],VfTemp[If],SurgeWaveform

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Reverse Voltage	0	to	5	V
Temperature	-40	to	120	deg C

MoDeCH

Diode		O : Implemented × : Not Implemented — : Not applicable	
Model Functions Table	RANK=1		
Functions	RANK	Implemented	
IF-VF(Temp)	1	0	
IR-VR(Temp)	1	—	
Capacitance	1	—	
Reverse recovery characteristics	1	—	
Zz-Iz	1	—	
Rectification characteristics(Bridge)	1	—	
Surge-Transient	1	0	
tlp	1	—	

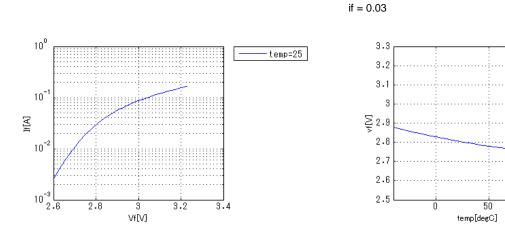


100

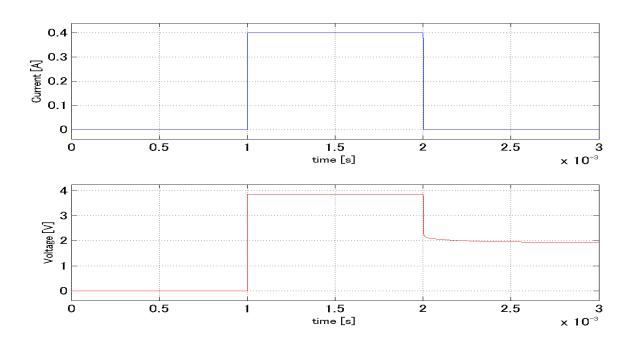
if=0.03

Simulation results are following. Explanatory notes -: simulated

lfVf[Temp]



Surge Current Waveform (Forward tp=1ms)



VfTemp[lf]



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