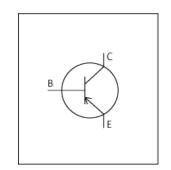


# PSpice Model PNP Nexperia 2PA1576S



## **Model Information**

Model Gummel-Poon model Call Name MDC\_2PA1576S\_PS

Pin Assign 1:B 2:E 3:C
File List Model Library

Model Library MDC\_2PA1576S\_PS01.lib Model Report MDC\_2PA1576S\_PS.pdf (this file)

**Verified Simulator Version** 

Note

PSpice version 17.2

#### References

The information which was used for modeling is as follow:

[Data Sheet]

Date/Version Rev. 06 - 17 November 2009

Product nameCompany name2PA1576SNexperia B.V.

● Characteristics hFEIc[Temp], Vce(sat)Ic[Temp], Cob, fTIc[Vce]

#### **Simulation Range**

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

| Item                           | Range |    |      | Unit  |
|--------------------------------|-------|----|------|-------|
|                                | Min.  |    | Max. |       |
| Collector-emitter voltage (DC) | 0     | to | -50  | V     |
| Collector current (DC)         | 0     | to | -150 | mA    |
| Temperature                    | -65   | to | 150  | deg C |



**Model Functions Table** 

BJT

O: Implemented

×: Not Implemented

—: Not applicable

# RANK=1

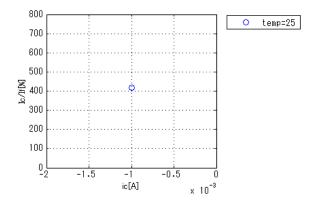
|                 | 10 01410 - |             |
|-----------------|------------|-------------|
| Functions       | RANK       | Implemented |
| IC-VBE(Temp)    | 1          | _           |
| IC-VCE-IB(Temp) | 1          |             |
| IC-hFE(Temp)    | 1          | 0           |
| VCE(sat)-IC     | 1          | 0           |
| VBE(sat)-IC     | 1          |             |
| Capacitance     | 1          | 0           |
| Transition      | 1          | 0           |
| Switching       | 1          | _           |



Simulation results are following. Explanatory notes — : simulated

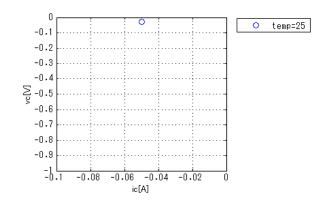
### hFElc[Temp]

Vce = -6V



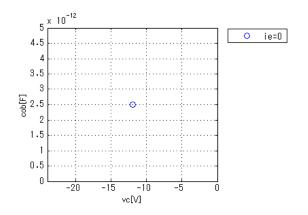
# Vce(sat)lc[Temp]

IC/IB = 10



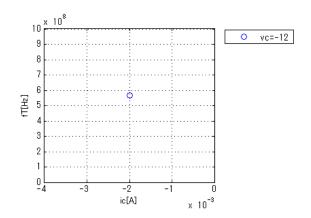
#### Cob

Freq = 1000000Hz



#### fTlc[Vce]

Freq = 10000000Hz





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