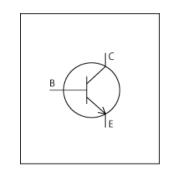


# PSpice Model NPN TOSHIBA TTC3710B



# **Model Information**

Model Gummel-Poon model Call Name MDC\_TTC3710B\_PS

Pin Assign 1:C 2:B 3:E
File List Model Library

Model Library MDC\_TTC3710B\_PS02.lib
Model Report MDC\_TTC3710B\_PS.pdf (this file)

**Verified Simulator Version** 

Note

PSpice version 16.6

#### References

The information which was used for modeling is as follow:

[Data Sheet]

● Date/Version 2014-11-06 Rev 3.0

● Product name TTC3710B

Company name Toshiba Corporation

● Characteristics IcVce[ib],hFEIc[Temp],Vce(sat)Ic[Temp],Vbe(sat)Ic[Temp],Ic

Vbe[Temp]

#### **Simulation Range**

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

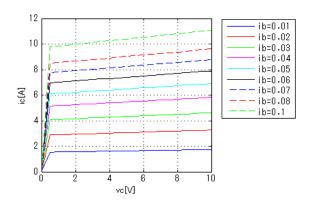
Item	Range			Unit
	Min.		Max.	
Collector-emitter voltage (DC)	0	to	80	V
Base current (DC)	0	to	2	Α
Temperature	-55	to	150	deg C



Simulation results are following. Explanatory notes — : simulated

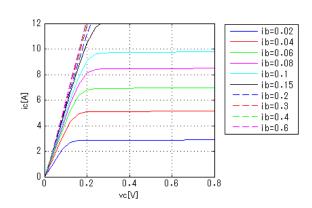
#### IcVce[ib]

Temp. = 25deg C



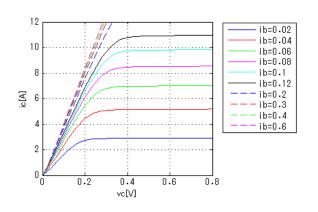
### IcVce[ib]

Temp. = -55deg C



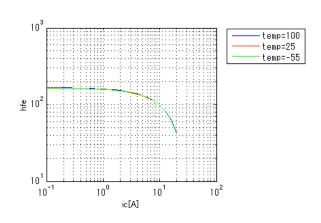
#### IcVce[ib]

Temp. = 100deg C



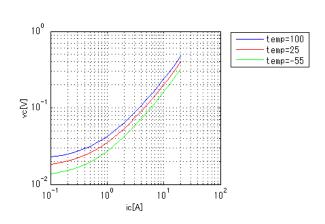
### hFElc[Temp]

Vce = 1V



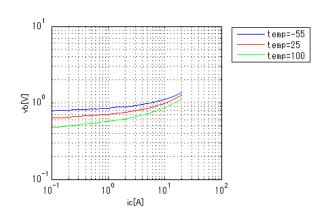
### Vce(sat)lc[Temp]

IC/IB = 20



### Vbe(sat)lc[Temp]

IC/IB = 20

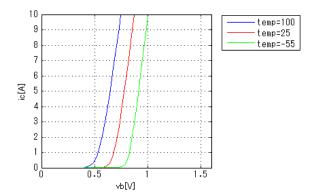




Simulation results are following. Explanatory notes — : simulated

# IcVbe[Temp]

Vce = 1V





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