

# LTspice Model Capacitor Nichicon UPJ1H100MDD

## **Model Information**

Model A macro model

Call Name MDC UPJ1H100MDD LT

Pin Assign 1:1 2:2

File List Model Library MDC UPJ1H100MDD LT.lib

Model Report MDC UPJ1H100MDD LT.pdf(this file)

Verified Simulator Version LTspice XVII

Note

#### References

The information which was used for modeling is as follow:

[Data Sheet]

Date/Version

Product nameCompany nameUPJ1H100MDDNichicon Corp.

[Characteristics listed]

Characteristics

Capacitance, Impedance-Frequency

### **Simulation Condition**

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Condition	Unit
Temperature	20	deg C

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Rev 1.0



# MDC\_UPJ1H100MDD\_LT

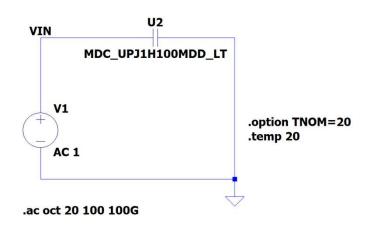
O: Implemented

Functions	RANK	Implemented
Capacitance-Frequency	1	0
Impedance-Frequency	1	0



### Capacitance, Impedance-Frequency Testbench

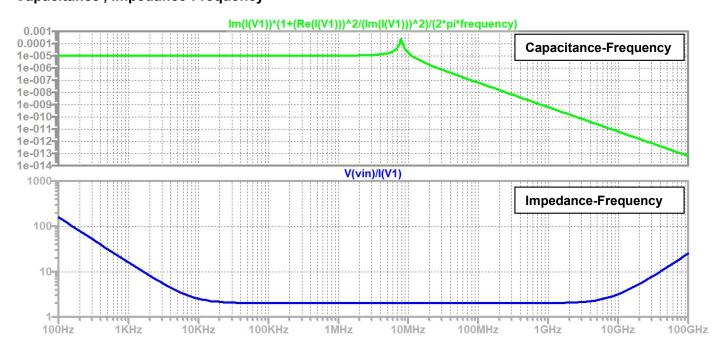
## **Referred to Data Sheet**



Simulation results are following.

Explanatory notes — : simulated

### Capacitance, Impedance-Frequency





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