

# PSpice Model

## PNP

## NXP

## BF550



### Model Information

**Model** Gummel-Poon model  
**Call Name** MDC\_BF550\_PS  
**Pin Assign** 1:B 2:E 3:C  
**File List** Model Library MDC\_BF550\_PS01.lib  
 Model Report MDC\_BF550\_PS.pdf (this file)

**Verified Simulator Version** PSpice version 17.2  
**Note**

### References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version 2004/1/16
- Product name BF550
- Company name NXP
- Characteristics hFEIc,IcVbe,CreVce,fTlc

### Simulation Range

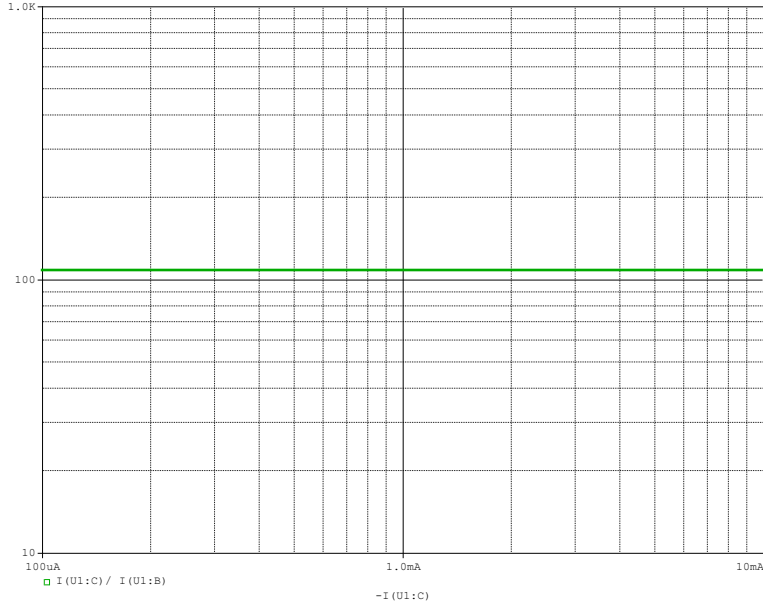
This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Collector-emitter voltage (DC)	0	to	-40	V
Base current (DC)	0	to	-25m	A
Temperature	-65	to	150	deg C

Simulation results are following.  
 Explanatory notes — : simulated

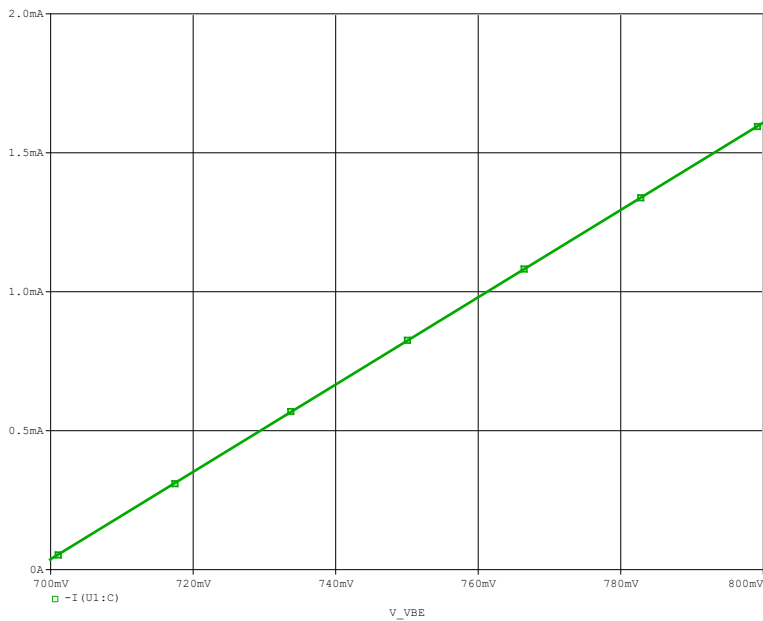
**hFEIc**

Vce = -10V



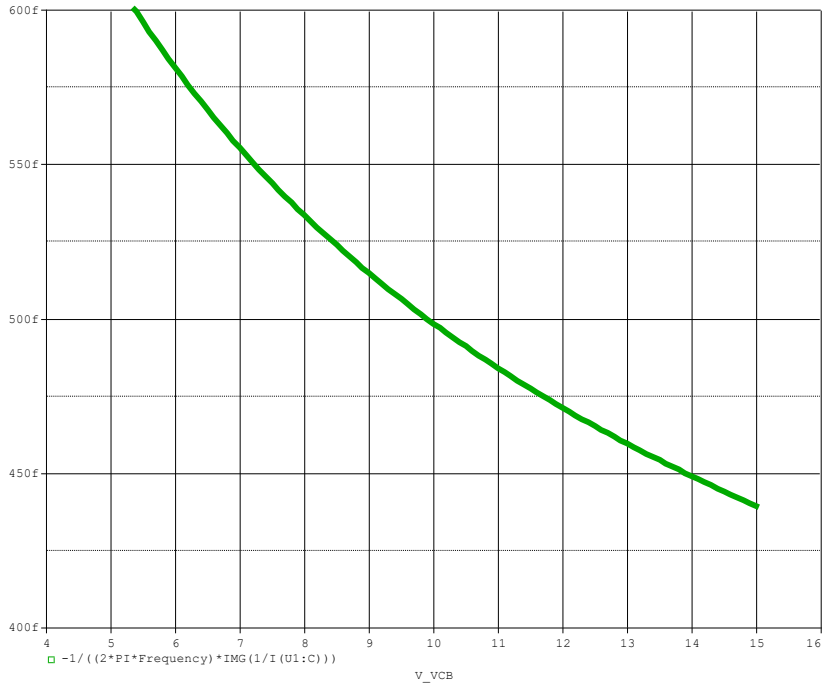
**IcVbe**

Vce = -10V



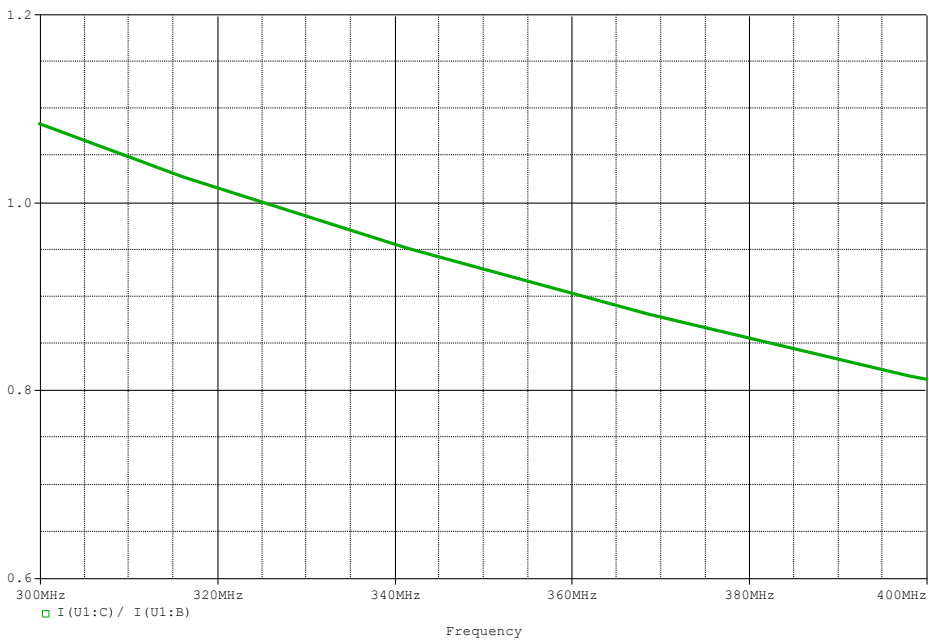
Simulation results are following.  
 Explanatory notes — : simulated

**Cre**



**fTic**

Vce = -10V



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