

# PSpice Model Photocoupler Tr. Output VISHAY CNY65ABST

## **Model Information**

	An original macro model MDC CNY65ABST PS		
Pin Assign	1:Anode 2:Cathode 3:Emitter 4:Collector		
File List	Model Library	MDC_CNY65ABST_PS01.lib	
	Model Report	MDC_CNY65ABST_PS.pdf (this file)	

Verified Simulator Version Note

PSpice version 16.6

#### References

The information which was used for modeling is as follow:

[Data Sheet] • Date/Versi

> ●P ●C ●C

1.1
CNY65ABST
Vishay Intertechnology, Inc.
lfVf[Temp],IcVce[If],hFEIf[Vce]

### **Simulation Range**

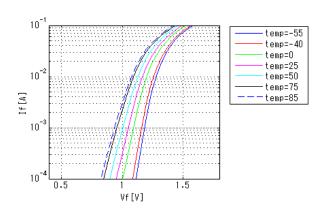
This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Forward current (DC)	0	to	0.075	А
Collector-emitter voltage (DC)	0	to	32	V
Temperature	-55	to	100	deg C



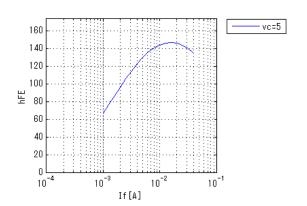
# Simulation results are following. Explanatory notes -: simulated

## lfVf[Temp]



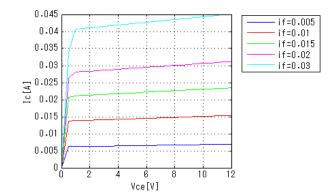
### hFElf[Vce]

Temp. = 25deg C



## lcVce[lf]

Temp. = 25deg C





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