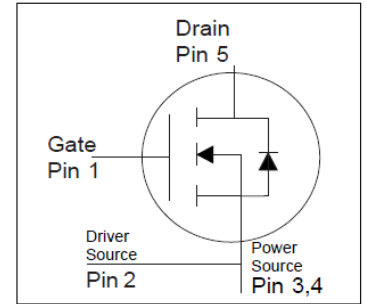
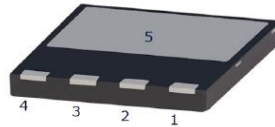


# LTspice Model NMOS Infineon IPL65R230C7

ThinPAK 8x8



## Model Information

<b>Model</b>	A macro model based on BSIM3 model		
<b>Call Name</b>	MDC_IPL65R230C7_LT		
<b>Pin Assign</b>	1:G 2:S 3:S 4:S 5:D		
<b>File List</b>	Model Library	MDC_IPL65R230C7_LT01.lib	
	Model Report	MDC_IPL65R230C7_LT.pdf (this file)	
<b>Verified Simulator Version</b>	LTspice version XVII		
<b>Note</b>			

## References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version Rev. 2.0, 2013-04-29
- Product name IPL65R230C7
- Company name Infineon Technologies AG
- Characteristics IdVds[Vgs], IdVds[Vgs]2, Rds(on)Id[Vgs], Rds(on)Temp[Id], IdVgs[Temp], VgsQg[Vdd], IsVsd[Temp], BvTemp[Ir], Capacitance Vds[Cname], SwitchingIdd[Tname], Trrlf[Ir], Qrrlf[Ir], Switching Waveform, TrrWaveform

## Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Drain-source voltage (DC)	0	to	650	V
Gate-source voltage (DC)	-20	to	20	V
Temperature	-40	to	150	deg C

## MOSFET

○ : Implemented  
× : Not Implemented  
— : Not applicable

Model Functions Table

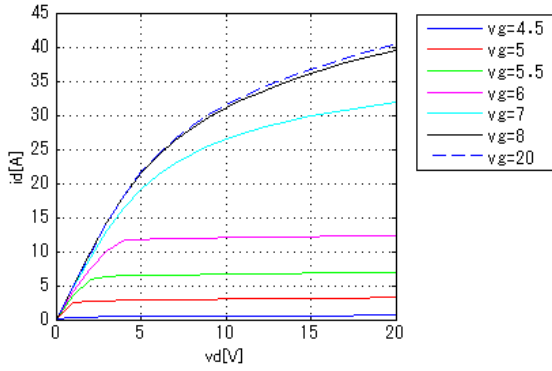
RANK=1

Functions	RANK	Implemented
ID-VDS-VGS	1	○
ID-VGS(Temp)	1	○
RDS(on)	1	○
Capacitance	1	○
Gate Charge	1	○
IS-VSD(Forward)	1	○
Reverse recovery	1	○
Switching(Typ.)	1	○
Bv	1	○
Yfs	1	—
Vth	1	—

Simulation results are following.  
 Explanatory notes — : simulated

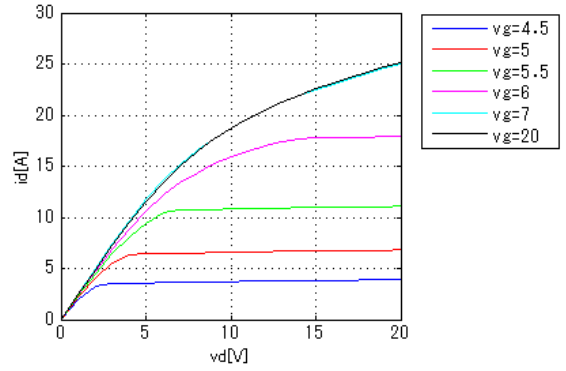
**IdVds[Vgs]**

Temp = 25degC



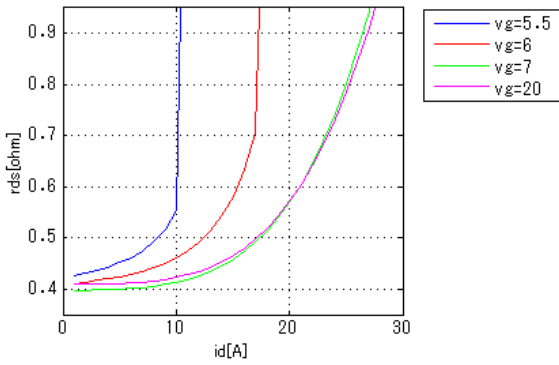
**IdVds[Vgs]2**

Temp = 125degC



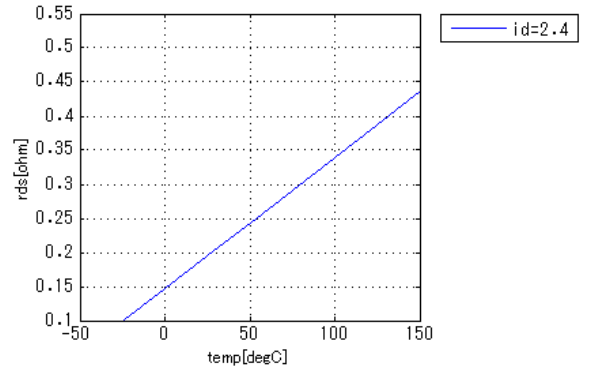
**Rds(on)Id[Vgs]**

Temp = 125degC



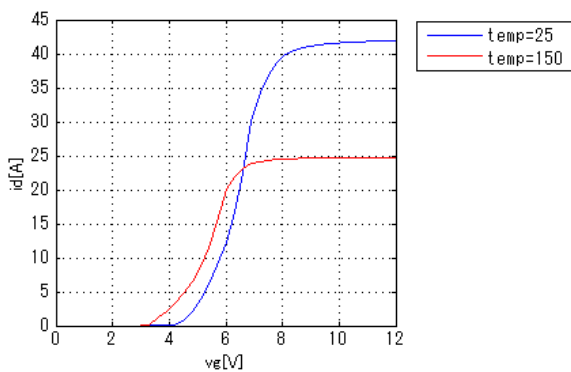
**Rds(on)Temp[Id]**

Vgs = 10V



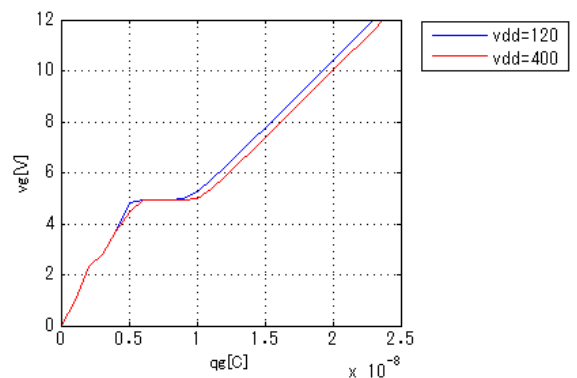
**IdVgs[Temp]**

Vds = 20V



**VgsQg[Vdd]**

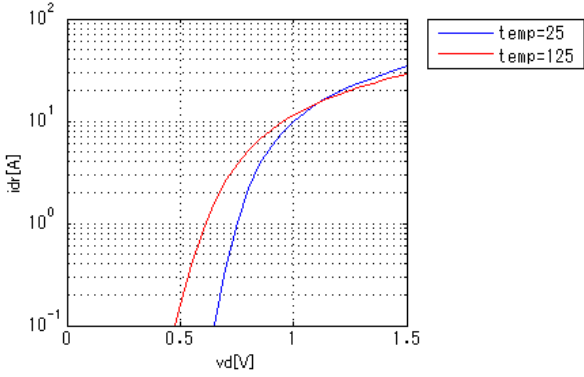
Id = 2.4A



Simulation results are following.  
 Explanatory notes — : simulated

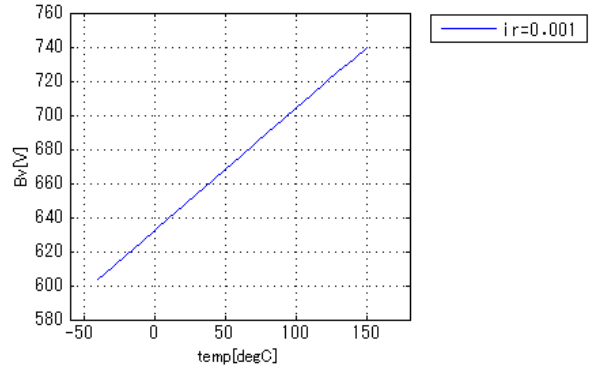
**IsVsd[Temp]**

vg = 0V



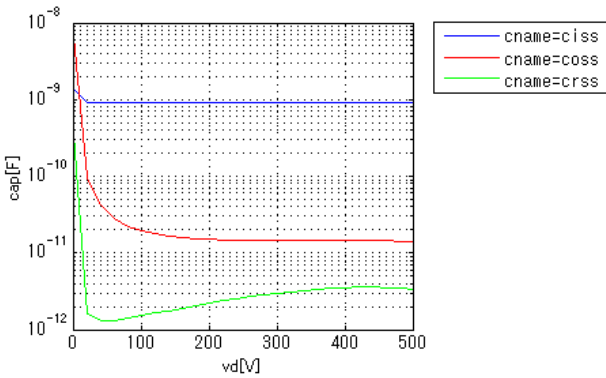
**BvTemp[ir]**

ir = 0.001A



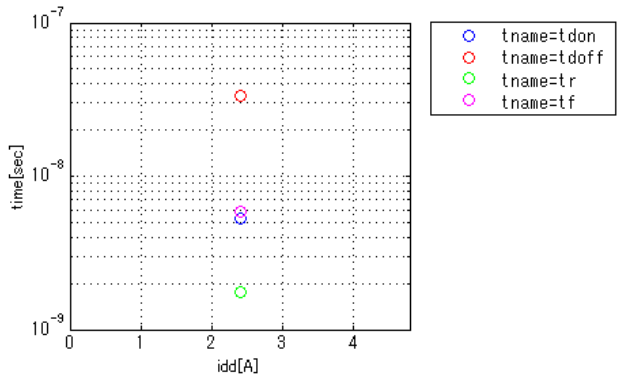
**CapacitanceVds[Cname]**

freq = 250000Hz



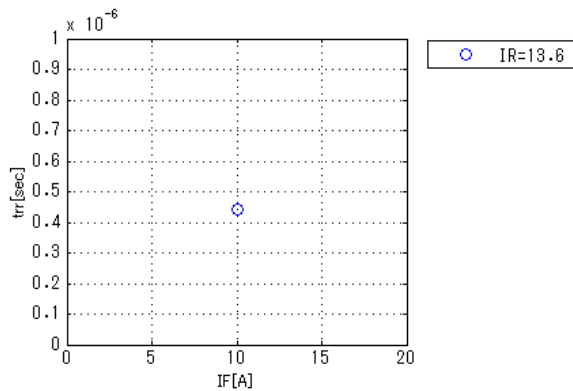
**SwitchingIdd[Tname]**

vgg = 13V, vdd = 400V, RGG = 10ohm



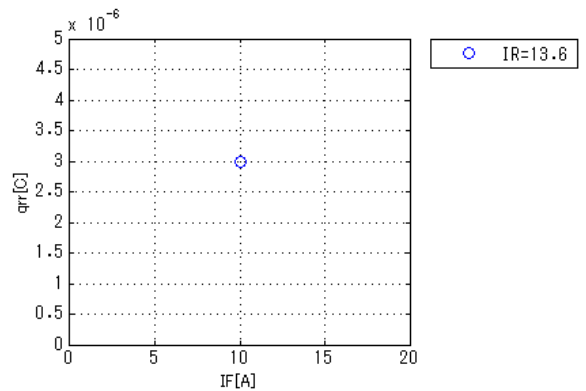
**Trrlf[Ir]**

vdd = 400V, didt = 55A/us, Temp = 25degC



**Qrrlf[Ir]**

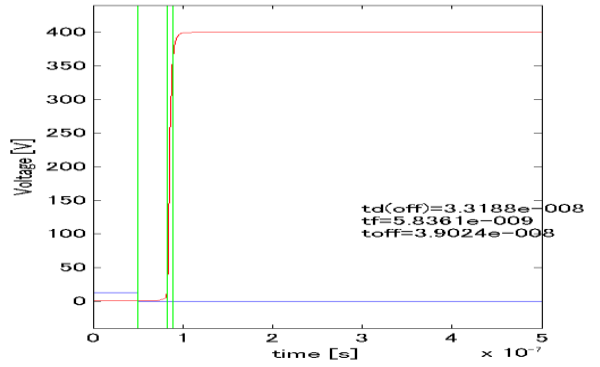
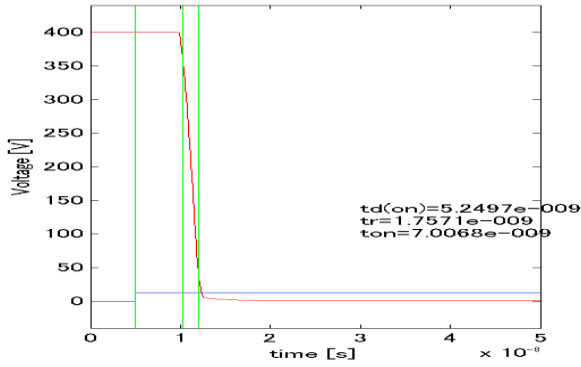
vdd = 400V, didt = 55A/us, Temp = 25degC



Simulation results are following.  
 Explanatory notes — : simulated

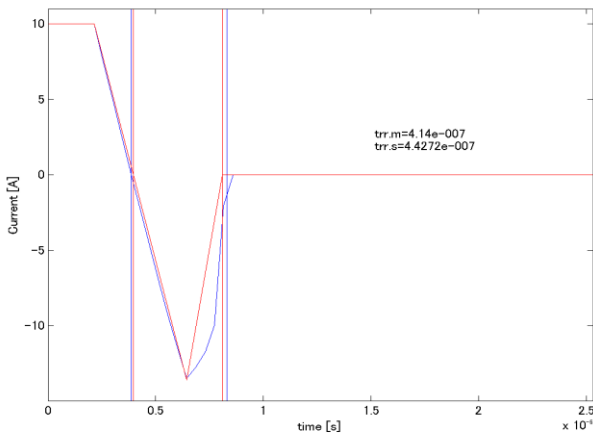
**Switching Waveform ( Blue : INPUT Red : OUTPUT )**

v<sub>gg</sub> = 13V, v<sub>dd</sub> = 400V, R<sub>GG</sub> = 10ohm, i<sub>dd</sub> = 2.4A



**Trr Waveform ( Red : Datasheet Blue : Simulation )**

v<sub>dd</sub> = 400V, didt = 55A/us, Temp = 25degC, I<sub>f</sub> = 10A, I<sub>r</sub> = 13.6A



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MoDeCH Inc.

Head Office

Location: 5-15 Yokoyama-cho, Hachioji-Shi, Tokyo 192-0081, Japan

Tel:+81-42-656-3360

E-Mail:[model-on-support@modech.co.jp](mailto:model-on-support@modech.co.jp)

URL:<http://www.modech.com/en/>