

PSpice Model ESD Protection Diode TOSHIBA DF5A5.6LFU

Model Information

 Model
 A macro model based on general SPICE diode model

 Call Name
 MDC_DF5A5_6LFU_PS

 Pin Assign
 1:A 2:C

 File List
 Model Library
 MDC_DF5A5_6LFU_PS02.lib

 Model Report
 MDC_DF5A5_6LFU_PS.pdf (this file)

Verified Simulator Version Note

PSpice version 16.6

References

The information which was used for modeling is as follow:

[Data Sheet]

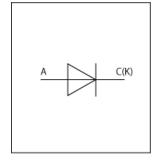
Date/Version
Product name
Company name
Characteristics

2014-03-01 DF5A5.6LFU Toshiba Corporation CjVr,IrVr(Breakdown)

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	F	Unit		
	Min.		Max.	
Reverse Voltage	0	to	11	V
Temperature	-55	to	125	deg C

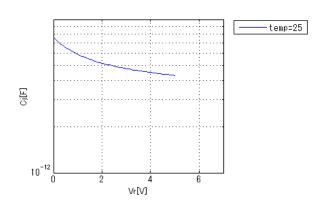




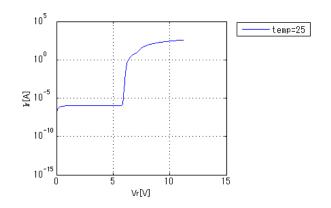
Simulation results are following. Explanatory notes -: simulated



Freq. = 1MHz



IrVr(Breakdown)





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