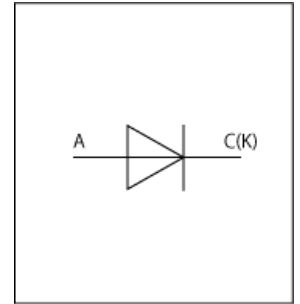


PSpice Model

ESD Protection Diode

VISHAY

VTVS40GSMF-HM3



Model Information

Model A macro model based on general SPICE diode model
Call Name MDC_VTVS40GSMF-HM3_PS
Pin Assign 1:A 2:C
File List Model Library MDC_VTVS40GSMF-HM3_PS01.lib
 Model Report MDC_VTVS40GSMF-HM3_PS.pdf (this file)

Verified Simulator Version PSpice version 16.6
Note

References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version 15-Dec-15
- Product name VTVS40GSMF-HM3
- Company name Vishay Intertechnology, Inc.
- Characteristics IrVr(Breakdown)

Simulation Range

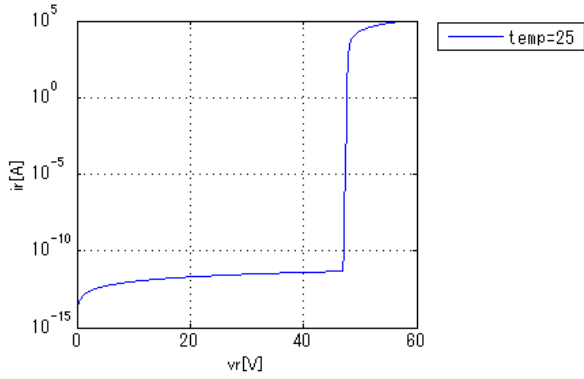
This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Reverse Voltage	0	to	94.11	V
Temperature	-55	to	175	deg C

Simulation results are following.

Explanatory notes — : simulated

IrVr(Breakdown)



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MoDeCH Inc.

Head Office

Location: Mitsuseimei Hachioji Bldg., 5-15 Yokoyama-cho, Hachioji-Shi, Tokyo 192-0081, Japan

Tel:+81-42-656-3360

E-Mail:model-on-support@modech.co.jp

URL:<http://www.modech.com/en/>