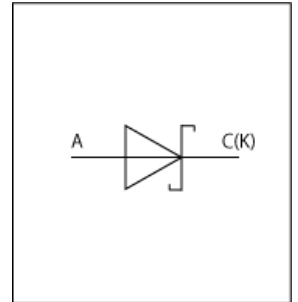


LTspice Model Schottky Diode Nexperia PMEG4030EP



Model Information

Model A macro model based on general SPICE diode model
Call Name MDC_PMEG4030EP_LT
Pin Assign 1:K 2:A
File List Model Library MDC_PMEG4030EP_LT01.lib
 Model Report MDC_PMEG4030EP_LT.pdf (this file)

Verified Simulator Version LTspice version XVII
Note

References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version 22 November 2017
- Product name PMEG4030EP
- Company name Nexperia B.V.
- Characteristics IfVf[Temp],Irvr[Temp],CjVr

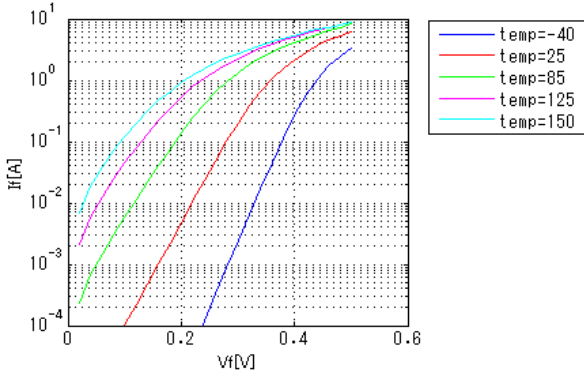
Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

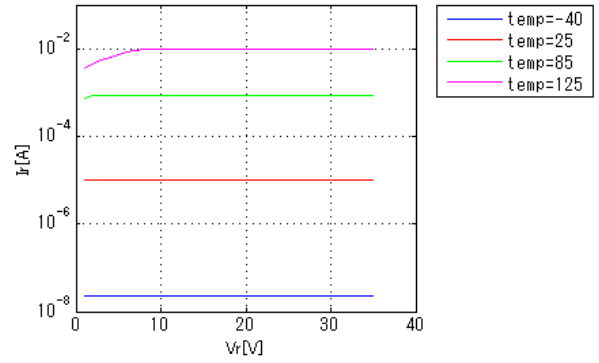
Item	Range			Unit
	Min.		Max.	
Forward Voltage	0	to	5	V
Reverse Voltage	0	to	80	V
Temperature	-65	to	150	deg C

Simulation results are following.
 Explanatory notes — : simulated

IfVf[Temp]

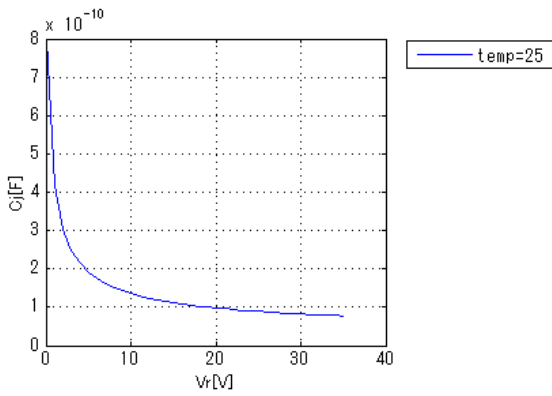


IrVr[Temp]



CjVr

Freq. = 1MHz



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