

MDC_8050SS_PS

PSpice Model NPN JCET 8050SS

Model Information

ModelMextram modelCall NameMDC_8050SS_PSPin Assign1:C 2:B 3:EFile ListModel Library
Model Report

MDC_8050SS_PS03.lib MDC_8050SS_PS.pdf (this file)

Verified Simulator Version Note

PSpice version 16.6

References

The information which was used for modeling is as follow:

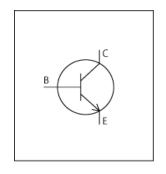
[Data Sheet]
Date/Version
Product name
Company name
Characteristics

C,Jan,2015 8050SS Jiangsu Changjiang Electronics Technology Co., Ltd. IcVce[ib],hFEIc[Temp],Vce(sat)Ic[Temp],Vbe(sat)Ic[Temp],Ic Vbe[Temp],Cob,Cib,fTIc[Vce]

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Collector-emitter voltage (DC)	0	to	25	V
Base current (DC)	0	to	0.15	А
Temperature	-55	to	150	deg C

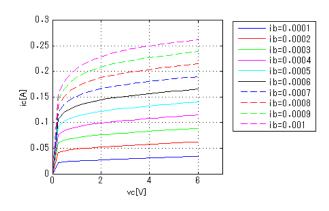




Simulation results are following. Explanatory notes — : simulated

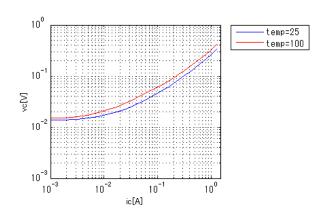
IcVce[ib]

Temp. = 25deg C



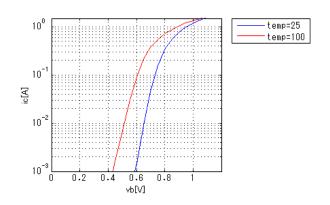
Vce(sat)lc[Temp]

IC/IB = 10



IcVbe[Temp]

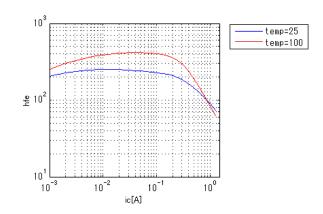
Vce = 1V



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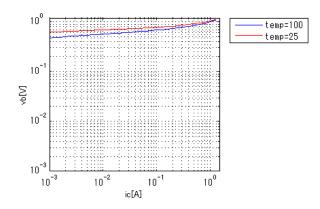
hFElc[Temp]

Vce = 1V

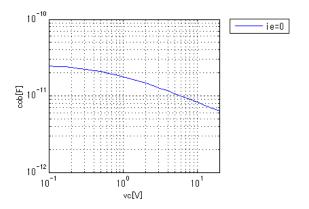


Vbe(sat)lc[Temp]

IC/IB = 10



Cob Freq. = 1MHz

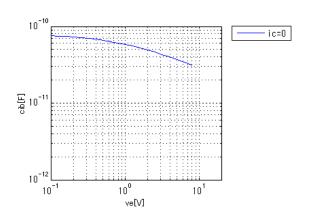




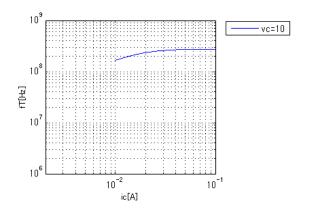
Simulation results are following. Explanatory notes -: simulated







fTlc[Vce]





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MoDeCH Inc.

Head Office Location: Mitsuiseimei Hachioji Bldg., 5-15 Yokoyama-cho, Hachioji-Shi, Tokyo 192-0081, Japan Tel:+81-42-656-3360 E-Mail:model-on-support@modech.co.jp URL:http://www.modech.com/en/