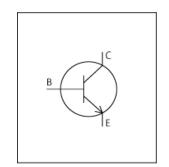


PSpice Model NPN Comchip MMBT5551-G



Model Information

Model Gummel-Poon model Call Name MDC_MMBT5551-G_PS

Pin Assign 1:C 2:B 3:E **File List** Model Librar

Model Library MDC_MMBT5551-G_PS02.lib

Model Report MDC_MMBT5551-G_PS.pdf (this file)

Verified Simulator Version

Note

PSpice version 16.6

References

The information which was used for modeling is as follow:

[Data Sheet]

Date/Version REV:B

Product name
MMBT5551-G

■Company name Comchip Technology Co., Ltd.

● Characteristics IcVce[ib],hFEIc[Temp],Vbe(sat)Ic[Temp],Vce(sat)Ic[Temp],Ic

Vbe[Temp],Cob,Cib,fTlc[Vce]

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

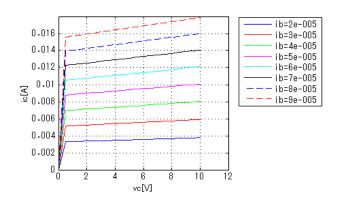
Item	Range			Unit
	Min.		Max.	
Collector-emitter voltage (DC)	0	to	160	V
Base current (DC)	0	to	60	Α
Temperature	-55	to	150	deg C



Simulation results are following. Explanatory notes — : simulated

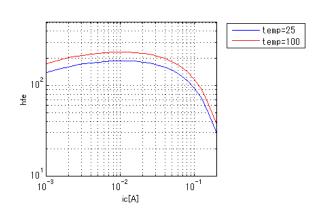
IcVce[ib]

Temp. = 25deg C



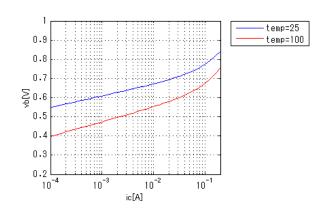
hFEIc[Temp]

Vce = 5V



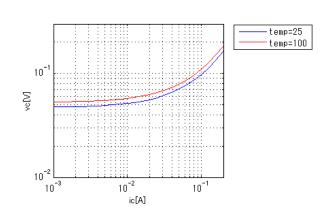
Vbe(sat)lc[Temp]

IC/IB = 10



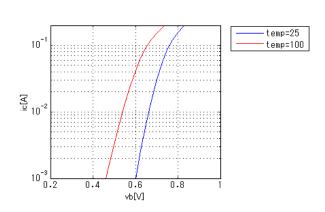
Vce(sat)lc[Temp]

IC/IB = 10



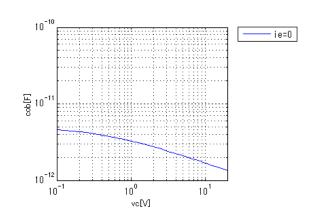
IcVbe[Temp]

Vce = 5V



Cob

Freq. = 1MHz



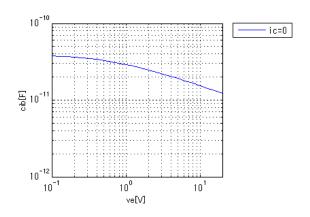


Simulation results are following.

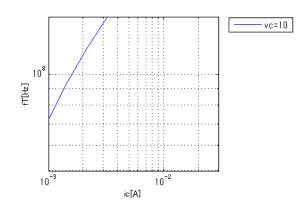
Explanatory notes — : simulated

Cib

Freq. = 1MHz



fTIc[Vce]





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