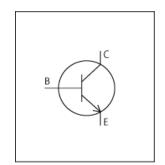


# PSpice Model NPN ON FJL6920



## **Model Information**

Model Mextram model
Call Name MDC\_FJL6920\_PS
Pin Assign 1:C 2:B 3:E

File List Model Library

Model Report MDC\_FJL6920\_PS.pdf (this file)

**Verified Simulator Version** 

Note

PSpice version 16.6

MDC\_FJL6920\_PS01.lib

#### References

The information which was used for modeling is as follow:

[Data Sheet]

Date/Version Rev. A, May 2001

● Product name FJL6920

Company nameON Semiconductor.

● Characteristics IcVce[ib],hFEIc[Temp],Vce(sat)Ic[Temp],IcVbe[Temp]

### **Simulation Range**

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Collector-emitter voltage (DC)	0	to	800	V
Base current (DC)	0	to	7	А
Temperature	-55	to	150	deg C

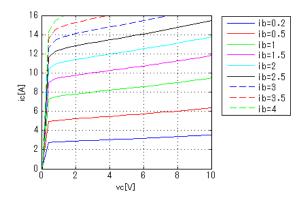


Simulation results are following.

Explanatory notes — : simulated

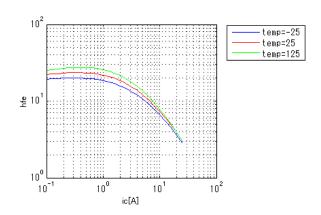
#### IcVce[ib]

Temp. = 25deg C



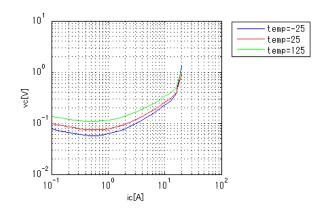
## hFEIc[Temp]

Vce = 5V



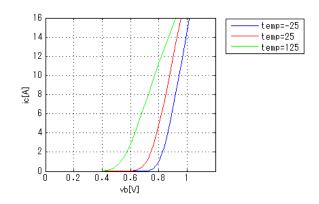
#### Vce(sat)lc[Temp]

IC/IB = 3



#### IcVbe[Temp]

Vce = 5V





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