

PSpice Model CRD SEMITEC S-701T

Model Information

 Model
 A macro model based on general SPICE diode model

 Call Name
 MDC_S-701T_PS

 Pin Assign
 1:A 2:C

 File List
 Model Library
 MDC_S-701T_PS02.lib

 Model Report
 MDC_S-701T_PS.pdf (this file)

Verified Simulator Version Note

PSpice version 17.2

References

The information which was used for modeling is as follow:

[Data Sheet]

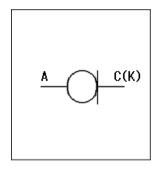
| Date/Version |
|-----------------|
| Product name |
| Company name |
| Characteristics |

2014.06 S-701T SEMITEC Corporation IfVf,IfTemp

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

| Item | Range | | | Unit |
|-------------|-------|----|------|-------|
| | Min. | | Max. | |
| Temperature | -40 | to | 150 | deg C |





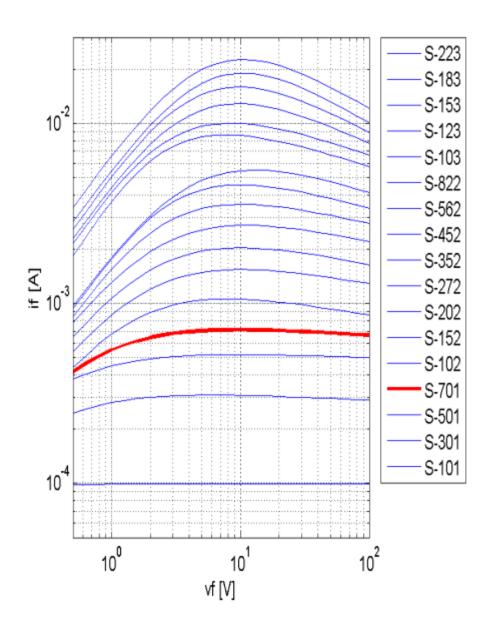
| Diode Model Functions Table | RANK=1 | O : Implemented × : Not Implemented — : Not applicable |
|---------------------------------------|--------|--|
| Functions | RANK | Implemented |
| IF-VF(Temp) | 1 | 0 |
| IR-VR(Temp) | 1 | — |
| Capacitance | 1 | — |
| Reverse recovery characteristics | 1 | — |
| Zz-Iz | 1 | — |
| Rectification characteristics(Bridge) | 1 | — |
| Surge voltage-Transient | 1 | — |



Simulation results are following. Explanatory notes -: simulated

lfVf

Temp = 25 degC



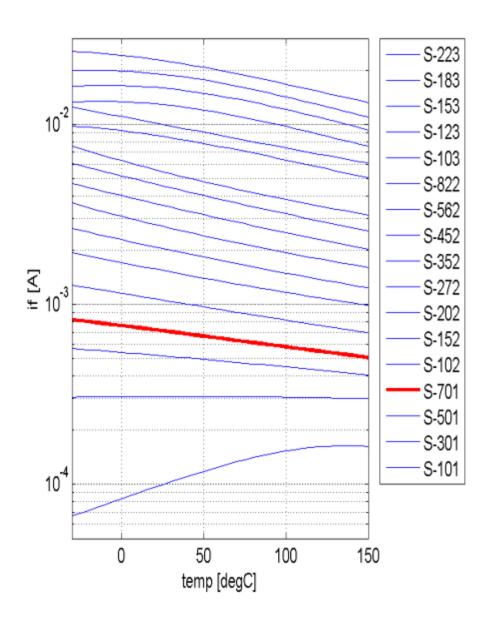
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Simulation results are following. Explanatory notes — : simulated

IfTemp

Vf = 10V





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