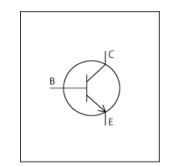


# PSpice Model NPN JCET 2SD2118



# **Model Information**

Model Mextram model
Call Name MDC\_2SD2118\_PS

Pin Assign 1:C 2:B 3:E
File List Model Library

Model Library MDC\_2SD2118\_PS01.lib
Model Report MDC\_2SD2118\_PS.pdf (this file)

**Verified Simulator Version** 

Note

PSpice version 16.6

#### References

The information which was used for modeling is as follow:

[Data Sheet]

Date/VersionProduct nameD,Mar,20162SD2118

● Company name Jiangsu Changjiang Electronics Technology Co., Ltd.

● Characteristics IcVce[ib],hFEIc[Temp],Vce(sat)Ic[Temp],Vbe(sat)Ic[Temp],Ic

Vbe[Temp],Cob,Cib,fTlc[Vce]

#### **Simulation Range**

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

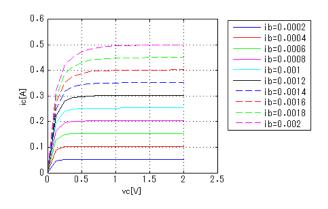
Item	Range			Unit
	Min.		Max.	
Collector-emitter voltage (DC)	0	to	20	V
Base current (DC)	0	to	0.125	Α
Temperature	-55	to	150	deg C



Simulation results are following. Explanatory notes — : simulated

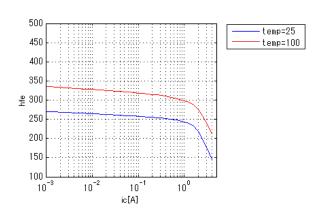
## IcVce[ib]

Temp. = 25deg C



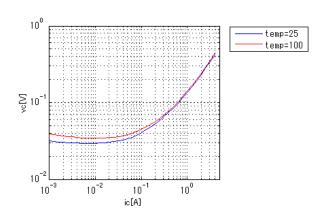
## hFEIc[Temp]

Vce = 2V



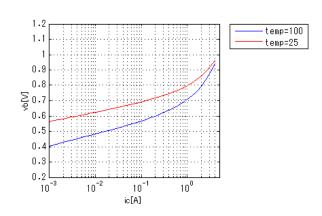
# Vce(sat)lc[Temp]

IC/IB = 40



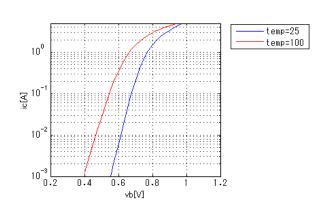
# Vbe(sat)lc[Temp]

IC/IB = 40



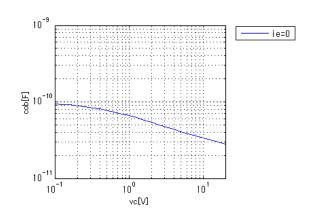
### IcVbe[Temp]

Vce = 2V



#### Cob

Freq. = 1MHz

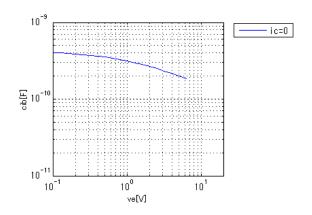




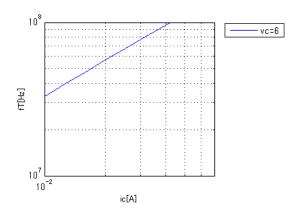
Simulation results are following. Explanatory notes — : simulated

### Cib

Freq. = 1MHz



# fTIc[Vce]





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