

### MDC\_ERZV14D471\_PS

# Model Information

**ERZV14D471** 

**PSpice Model** 

Varistor

**Panasonic** 

ModelAn original macro modelCall NameMDC\_ERZV14D471\_PSPin Assign1:1 2:2File ListModel Library<br/>Model Report

MDC\_ERZV14D471\_PS01.lib MDC\_ERZV14D471\_PS.pdf (this file)

Verified Simulator Version Note

PSpice version 17.2

#### References

The information which was used for modeling is as follow:

2021/12/23 ERZV14D471 Panasonic Corporation Vrlr[Temp],CjVr,SurgeWaveform

#### Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

| Item        | Range |    |      | Unit  |
|-------------|-------|----|------|-------|
|             | Min.  |    | Max. |       |
| Temperature | -40   | to | 125  | deg C |



| Surge Absorber          | O : Implemented<br>× : Not Implemente |                    |
|-------------------------|---------------------------------------|--------------------|
| Model Functions Table   | RANK=1                                | — : Not applicable |
| Functions               | RANK                                  | Implemented        |
| V-I(Temp)               | 1                                     | 0                  |
| Capacitance             | 1                                     | 0                  |
| Surge current-Transient | 1                                     | 0                  |

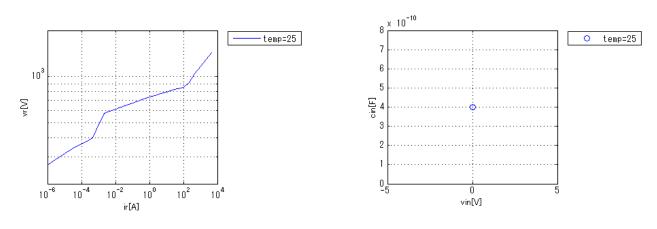


#### Simulation results are following. Explanatory notes — : simulated

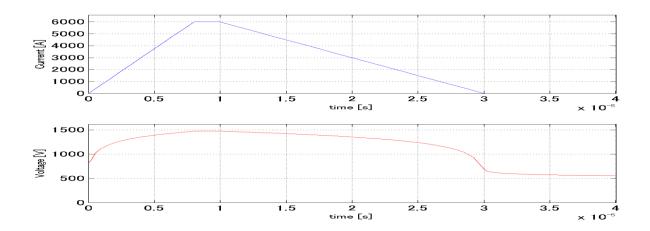
## Vrlr[Temp]



Freq. = 0.001MHz



#### Surge Current Waveform ( 8u / 20u )





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