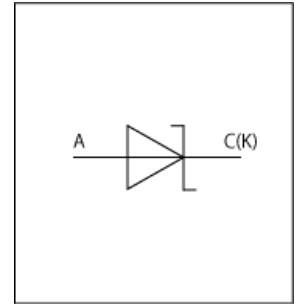


PSpice Model

Zener Diode

VISHAY

SML4760HE3



Model Information

Model A macro model based on general SPICE diode model
Call Name MDC_SML4760HE3_PS
Pin Assign 1:A 2:C
File List Model Library MDC_SML4760HE3_PS01.lib
 Model Report MDC_SML4760HE3_PS.pdf (this file)

Verified Simulator Version PSpice version 16.6
Note

References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version 08-Apr-15
- Product name SML4760HE3
- Company name Vishay Intertechnology, Inc.
- Characteristics IfVf[Temp],Irvr[Temp],BvTemp,Irvr(Breakdown)

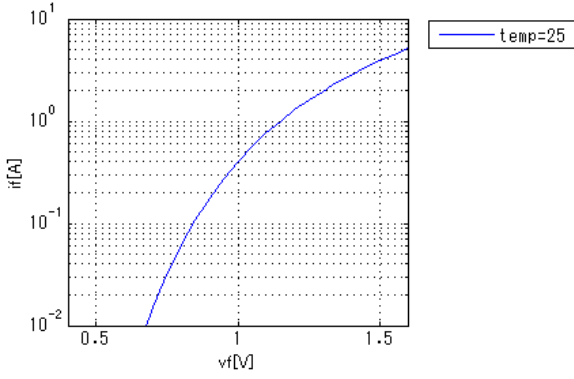
Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

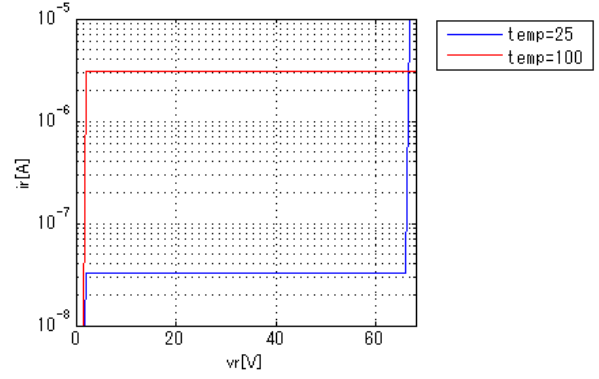
Item	Range			Unit
	Min.		Max.	
Forward Voltage	0	to	16	V
Reverse Voltage	0	to	136	V
Temperature	-65	to	150	deg C

Simulation results are following.
 Explanatory notes — : simulated

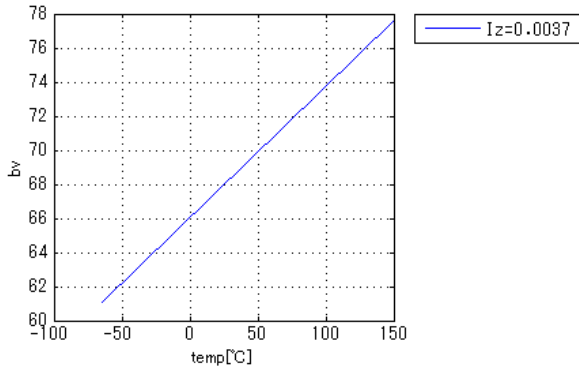
IfVf[Temp]



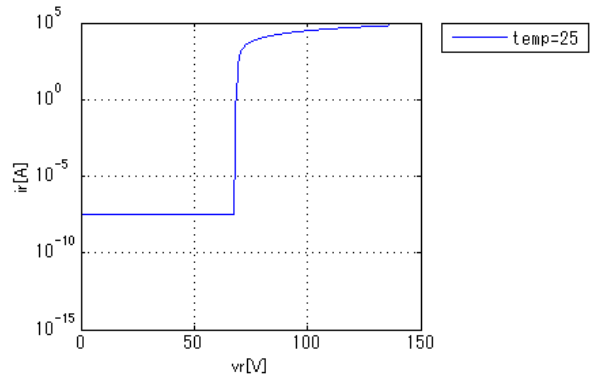
IrVr[Temp]



BvTemp



IrVr(Breakdown)



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