

# PSpice Model

## ESD Protection Diode

### TI

### TPD4E001DRLR



### Model Information

**Model** A macro model based on general SPICE diode model  
**Call Name** MDC\_TPD4E001DRLR\_PS  
**Pin Assign** 1:IO1 2:IO2 3:GND 4:IO3 5:IO4 6:VCC  
**File List** Model Library MDC\_TPD4E001DRLR\_PS00.lib  
 Model Report MDC\_TPD4E001DRLR\_PS.pdf (this file)

**Verified Simulator Version** PSpice version 16.6  
**Note**

### References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version Jul. 2019
- Product name TPD4E001DRLR
- Company name Texas Instruments Inc.
- Characteristics IfVf[Temp],IrVr[Temp],CjVr,IrVr(Breakdown)

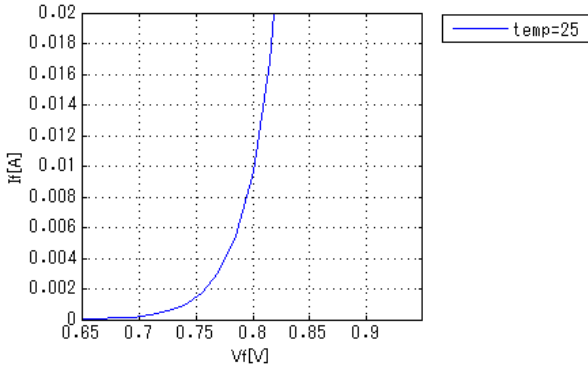
### Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

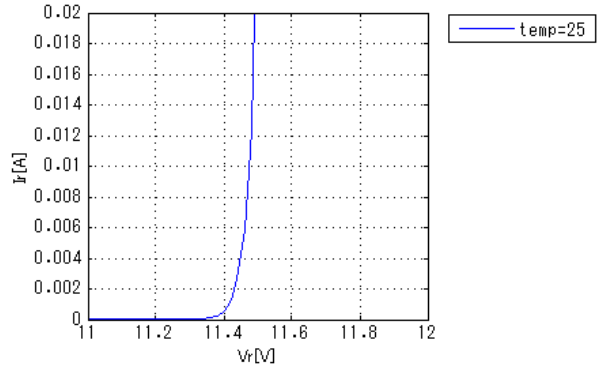
Item	Range			Unit
	Min.		Max.	
Reverse Voltage	0	to	22	V
Temperature	-65	to	150	deg C

Simulation results are following.  
 Explanatory notes — : simulated

**IfVf[Temp]**

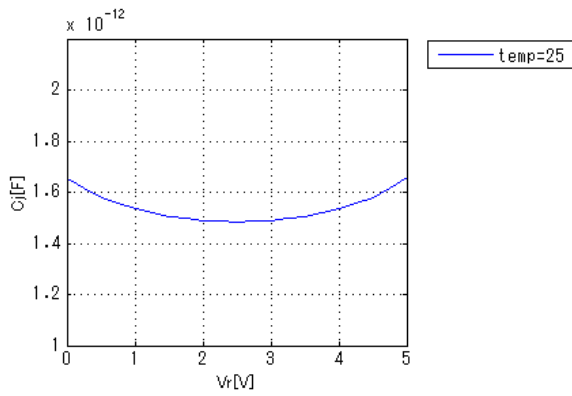


**IrVr[Temp]**

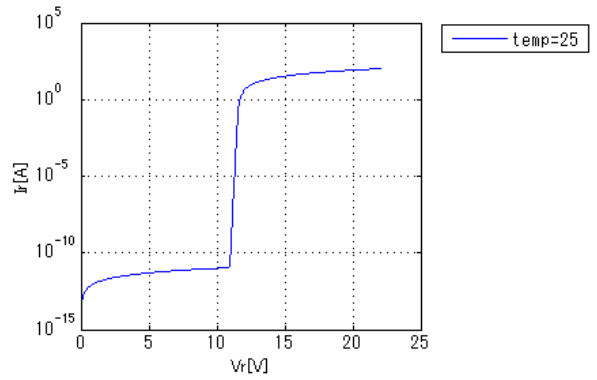


**CjVr**

Freq. = 10MHz



**IrVr(Breakdown)**



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