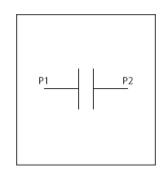


# PSpice Model Ceramic Capacitor TDK C1608X8R2A103K080AA



# **Model Information**

Model An original macro model

Call Name MDC C1608X8R2A103K080AA PS

Pin Assign 1:P1 2:P2

File List Model Library MDC\_C1608X8R2A103K080AA\_PS01.lib

Model Report MDC\_C1608X8R2A103K080AA\_PS.pdf (this file)

**Verified Simulator Version** 

Note

PSpice version 16.6

#### References

The information which was used for modeling is as follow:

[Measured]

Date 6/20/2016Company name TDK Corporation

Characteristics Cap.Freq.,Imp.Freq.,EsrFreq.

#### Simulation Range

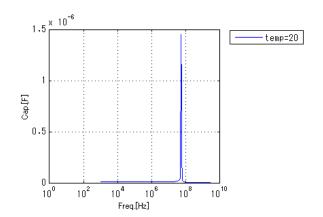
This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Frequency	1,003	to	3,000,000,000	Hz
Temperature	20	to	20	deg C

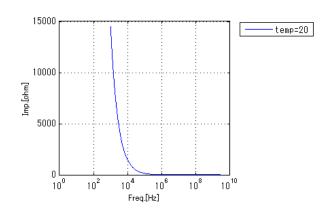


Simulation results are following. Explanatory notes — : simulated

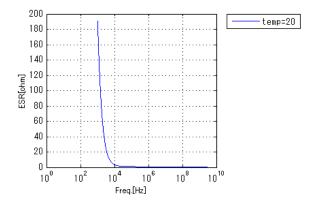
# Cap.Freq.



## Imp.Freq.



## EsrFreq.





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