

LTspice Model Capacitor NISSEI MPEC0500J2540B34P045

Model Information

Model	A macro model	
Call Name	MDC_MPEC0500J2540B34P045_LT	
Pin Assign	1:1 2:2	
File List	Model Library	MDC_MPEC0500J2540B34P045_LT.lib
	Model Report	MDC_MPEC0500J2540B34P045_LT.pdf(this file)
Verified Simulator Version	LTspice XVII	

Note

References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version
- Product name MPEC0500J2540B34P045
- Company name NISSEI

[Characteristics listed]

- Characteristics

Capacitance , Impedance - Frequency

Simulation Condition

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Condition	Unit
Temperature	20	deg C

○ : Implemented
 × : Not Implemented
 — : Not applicable

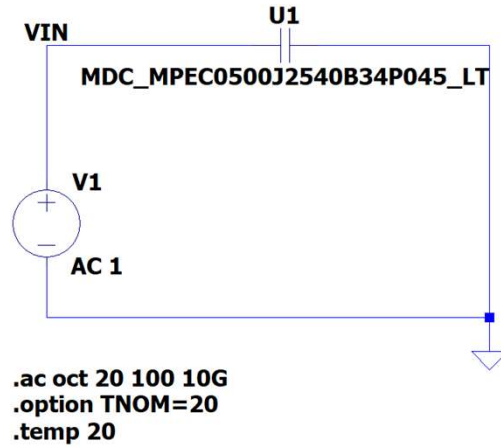
Model Functions Table

RANK=1

Functions	RANK	Implemented
Impedance-Frequency	1	○
Capacitance-Frequency	1	○

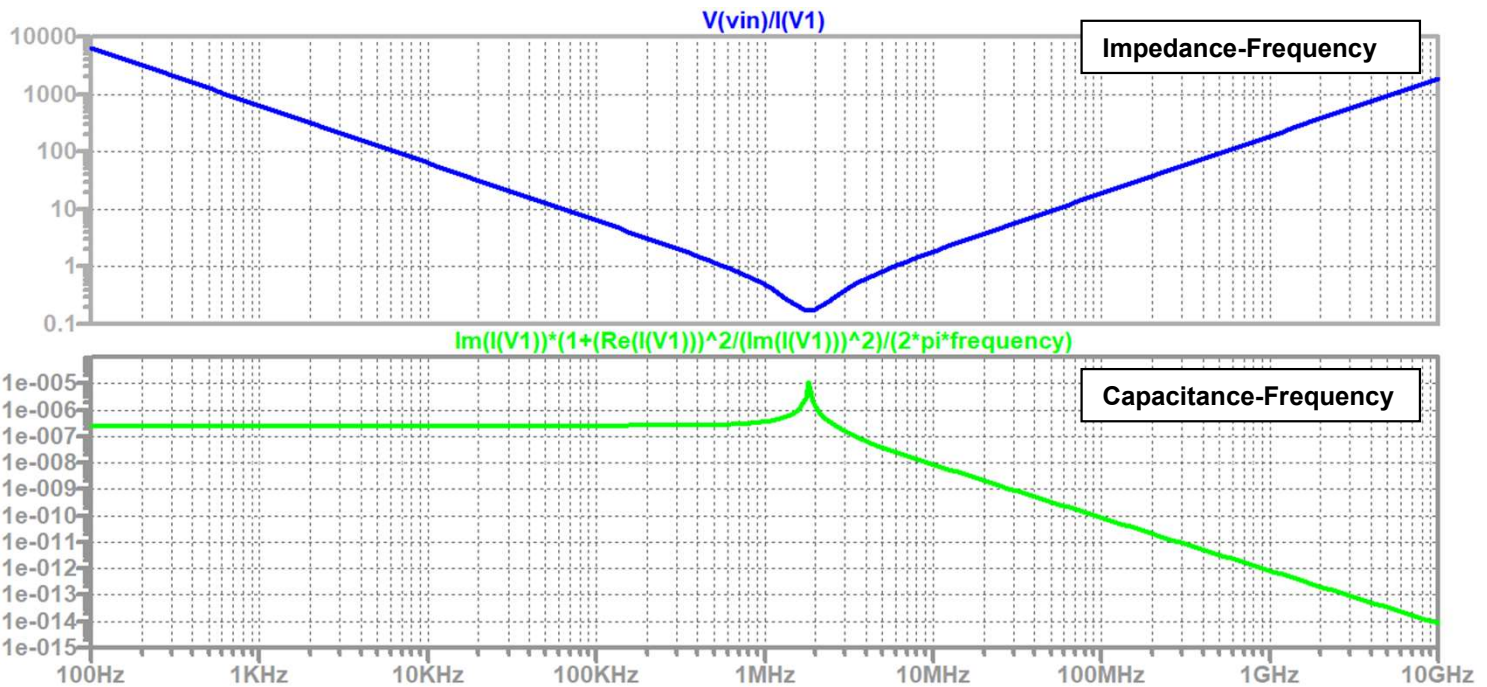
Capacitance , Impedance-Frequency Testbench

Referred to Data Sheet



Simulation results are following.
 Explanatory notes — : simulated

Capacitance , Impedance-Frequency



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MoDeCH Inc.

Head Office

Location: 5-15 Yokoyama-cho, Hachioji-Shi, Tokyo 192-0081, Japan

Tel:+81-42-656-3360

E-Mail:model-on-support@modech.co.jp

URL:<http://www.modech.com/en/>