

LTspice Model

ESD Protection Diode

TOSHIBA

DF2S5.6FS



Model Information

Model A macro model based on general SPICE diode model
Call Name MDC_DF2S5_6FS_LT
Pin Assign 1:A 2:C
File List Model Library MDC_DF2S5_6FS_LT02.lib
 Model Report MDC_DF2S5_6FS_LT.pdf (this file)

Verified Simulator Version LTspice version XVII
Note

References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version Rev.3.0
- Product name DF2S5.6FS
- Company name Toshiba Corporation
- Characteristics IrVr[Temp],CjVr,IrVr(Breakdown)

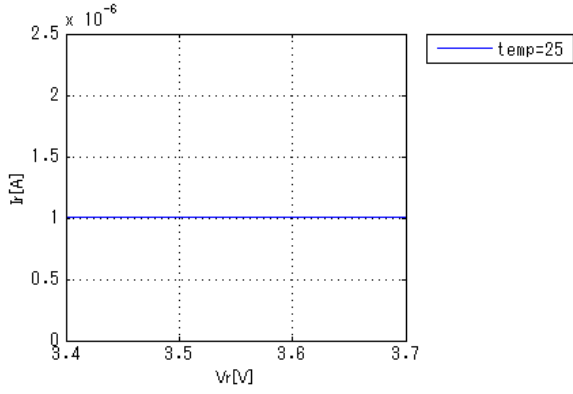
Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Reverse Voltage	0	to	11	V
Temperature	-55	to	150	deg C

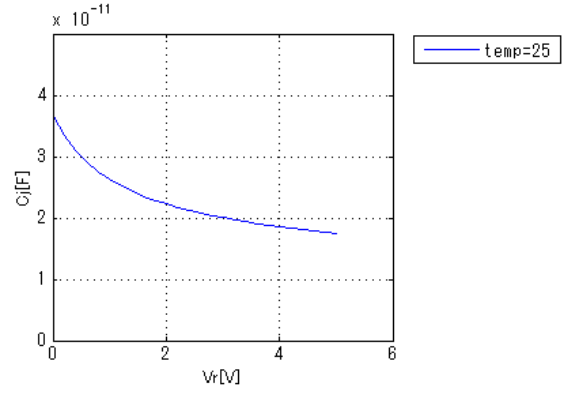
Simulation results are following.
 Explanatory notes — : simulated

IrVr[Temp]

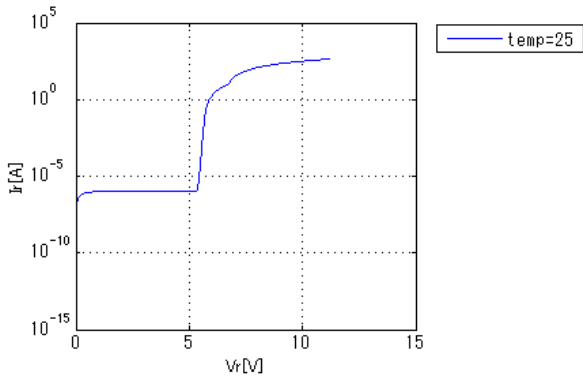


CjVr

Freq. = 1MHz



IrVr(Breakdown)



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