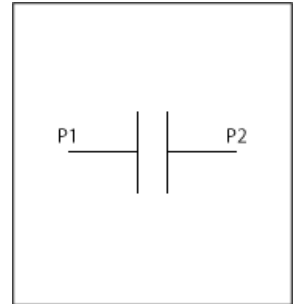


LTspice Model

Film Capacitor

NISSEI

MPEC0400J7450B18P045



Model Information

Model An original macro model
Call Name MDC_MPEC0400J7450B18P045_LT
Pin Assign 1:P1 2:P2
File List Model Library MDC_MPEC0400J7450B18P045_LT01.lib
 Model Report MDC_MPEC0400J7450B18P045_LT.pdf (this file)

Verified Simulator Version LTspice version XVII
Note

References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version 2020.05.20
- Product name MPEC0400J7450B18P045
- Company name NISSEI Electric Co., Ltd.
- Characteristics CapFreq,CapTemp,ImpFreq

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Frequency	100	to	100Meg	Hz
Temperature	-50	to	120	deg C

Capacitor

○ : Implemented
 × : Not Implemented
 — : Not applicable

Model Functions Table

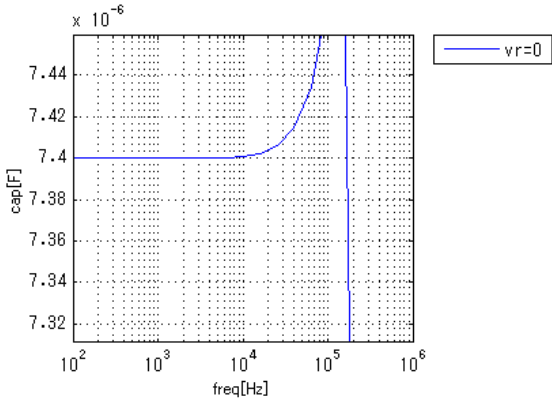
RANK=1

Functions	RANK	Implemented
Impedance-Frequency	1	○
Capacitance-Frequency	1	○
Capacitance-Temperature	1	○

Simulation results are following.
 Explanatory notes — : simulated

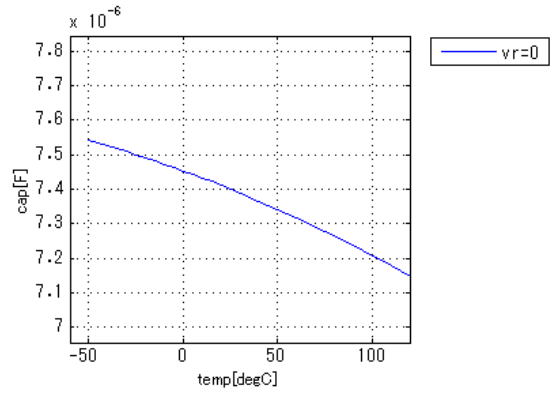
CapFreq

Temp = 20 degC



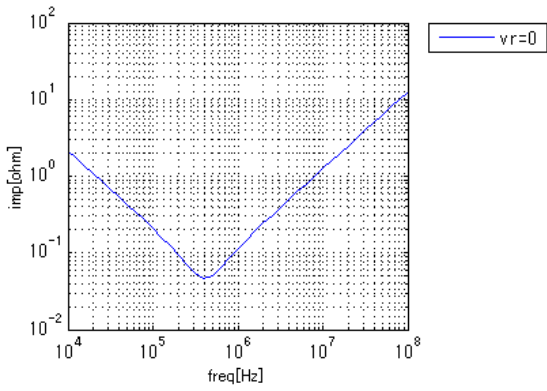
CapTemp

Freq = 1000 Hz



ImpFreq

Temp = 20 degC



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MoDeCH Inc.

Head Office

Location: Taiju-Seimei-Hachioji Bldg., 5-15 Yokoyama-cho, Hachioji-Shi, Tokyo 192-0081, Japan

Tel:+81-42-656-3360

E-Mail:model-on-support@modech.co.jp

URL:<http://www.modech.com/en/>