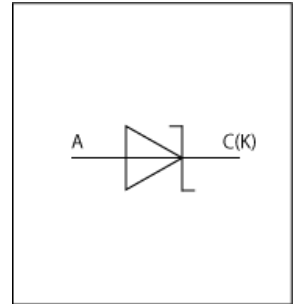


ADS Model

Zener Diode

ROHM

PDZVTR15B



Model Information

Model A macro model based on general SPICE diode model
Call Name MDC_PDZVTR15B_AD
Pin Assign 1:C 2:A
File List Model Library MDC_PDZVTR15B_AD01.lib
 Model Report MDC_PDZVTR15B_AD.pdf (this file)

Verified Simulator Version ADS version 2016
Note

References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version 2019/04/19_Rev.005
- Product name PDZVTR15B
- Company name ROHM Co., Ltd.
- Characteristics VrI_r[Temp], I_rV_r[Temp], I_fV_f[Temp], C_jV_r

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

| Item | Range | | | Unit |
|-----------------|-------|----|------|-------|
| | Min. | | Max. | |
| Reverse Voltage | 0 | to | 16 | V |
| Temperature | -55 | to | 150 | deg C |

Diode

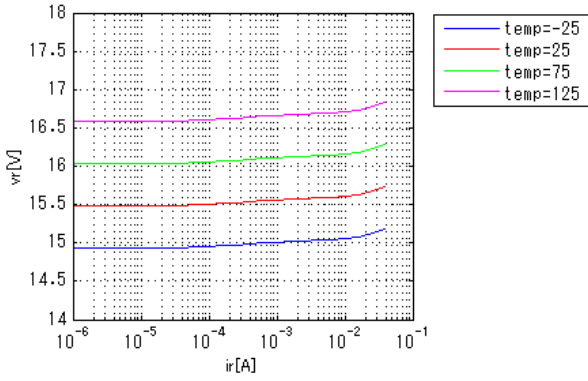
○ : Implemented
× : Not Implemented
— : Not applicable

Model Functions Table
RANK=1

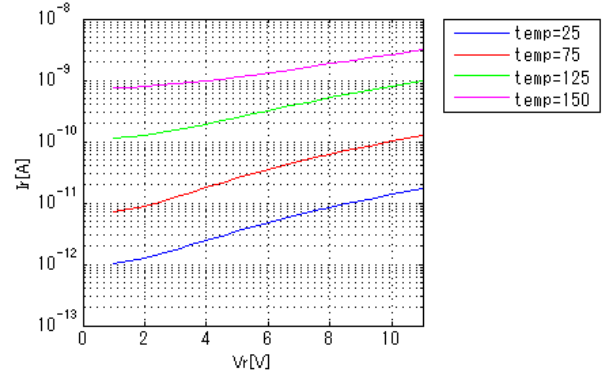
| Functions | RANK | Implemented |
|---------------------------------------|------|-------------|
| IF-VF(Temp) | 1 | ○ |
| IR-VR(Temp) | 1 | ○ |
| Capacitance | 1 | ○ |
| Reverse recovery characteristics | 1 | — |
| Zz-Iz | 1 | — |
| Rectification characteristics(Bridge) | 1 | — |
| Surge-Transient | 1 | — |
| tlp | 1 | — |

Simulation results are following.
 Explanatory notes — : simulated

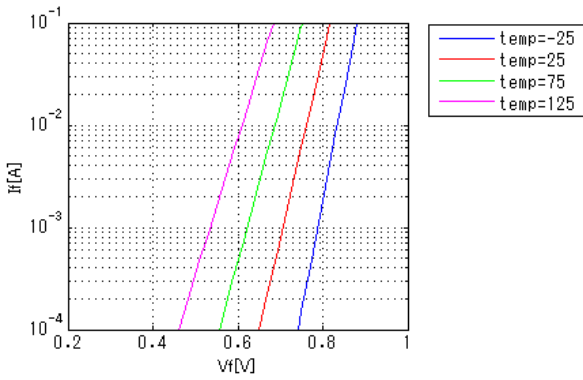
VrIr[Temp]



IrVr[Temp]

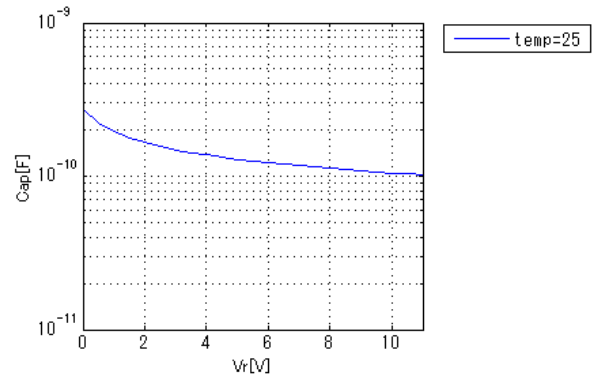


IfVf[Temp]



CjVr

Freq = 1000000Hz



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