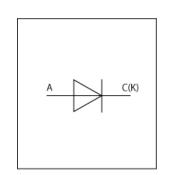


PSpice Model Rectifier Diode ON S1MFL



Model Information

Model A macro model based on general SPICE diode model

Call Name MDC_S1MFL_PS

Pin Assign 1:A 2:C

File List Model Library MDC_S1MFL_PS01.lib

Model Report MDC_S1MFL_PS.pdf (this file)

Verified Simulator Version

Note

PSpice version 16.6

References

The information which was used for modeling is as follow:

[Data Sheet]

Date/Version Rev 1.2Product name S1MFL

Company name ON Semiconductor.

Characteristics IfVf[Temp],IrVr[Temp],CjVr,Trr

Simulation Range

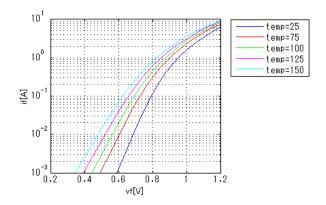
This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

| Item | Range | | | Unit |
|-----------------|-------|----|------|-------|
| | Min. | | Max. | |
| Forward Voltage | 0 | to | 12 | V |
| Temperature | -50 | to | 150 | deg C |

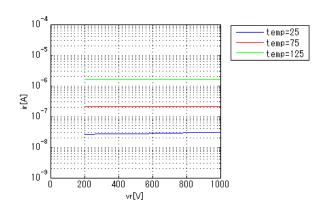


Simulation results are following. Explanatory notes — : simulated

IfVf[Temp]

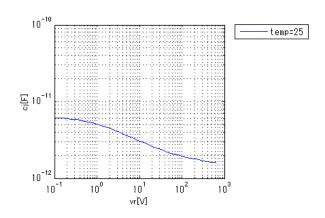


IrVr[Temp]

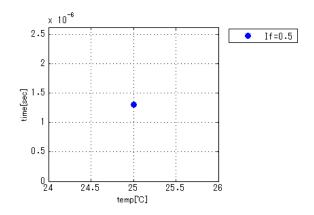


CjVr

Freq. = 1MHz



Trr





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