

# LTspice Model

## Logic IC

## TI

## SN74LV4052APWR

### Model Information

<b>Model</b>	An original macro model		
<b>Call Name</b>	MDC_SN74LV4052APWR_LT		
<b>Pin Assign</b>	1:2Y0 2:2Y2 3:2-COM 4:2Y3 5:2Y1 6:INH 7:GND 8:GND 9:B 10:A 11:1Y3 12:1Y0 13:1-COM 14:1Y1 15:1Y2 16:Vcc		
<b>File List</b>	Model Library	MDC_SN74LV4052APWR_LT01.lib	
	Model Report	MDC_SN74LV4052APWR_LT.pdf (this file)	
<b>Verified Simulator Version</b>	LTspice version XVII		
<b>Note</b>			

### References

The information which was used for modeling is as follow:

[Data Sheet]

● Date/Version	SCLS429K-MAY 1999-REVISED NOVEMBER 2016
● Product name	SN74LV4052APWR
● Company name	Texas Instruments Inc.
● Characteristics	Transient, Transient2, Transient3, Transient4, Propagation Delay Time, Enable Delay Time, Disable Delay Time

### Simulation Condition

This table shows the value of evaluated simulation condition that was not occurs any convergence problems in this area.

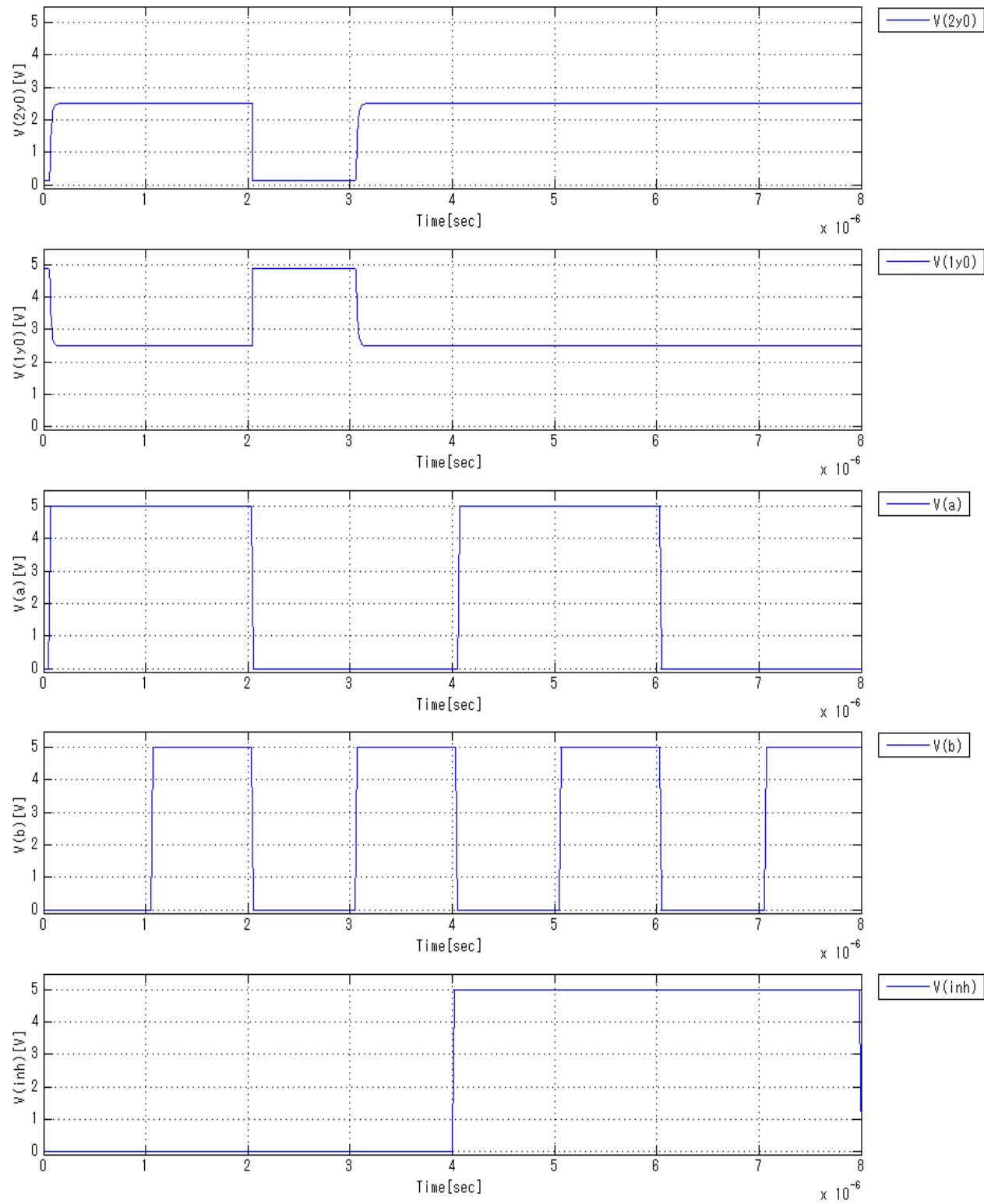
Item	Condition	Unit
Temperature	25	deg C

Simulation results are following.  
Explanatory notes    — : simulated

Transient

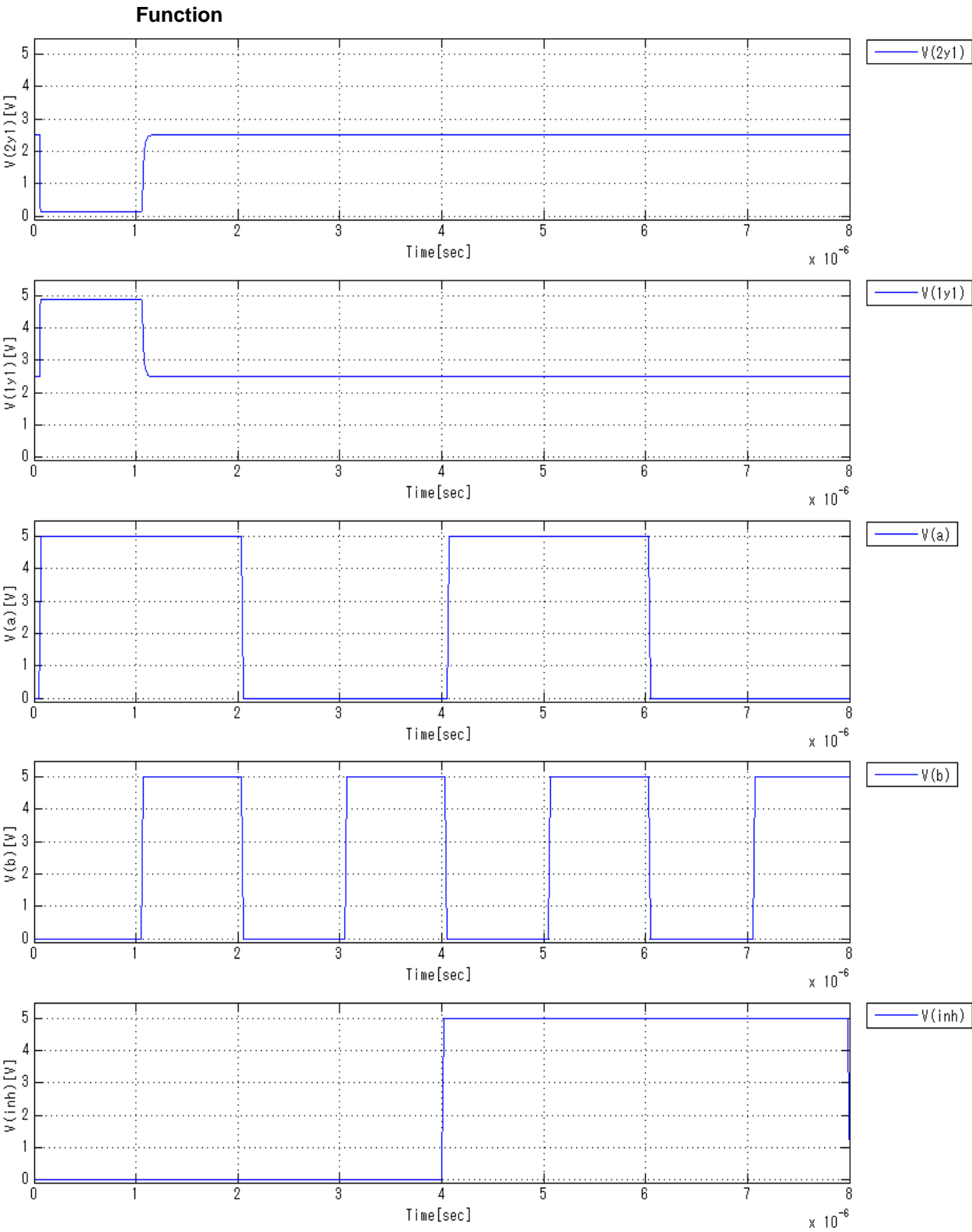
Temp. = 25deg C

Function



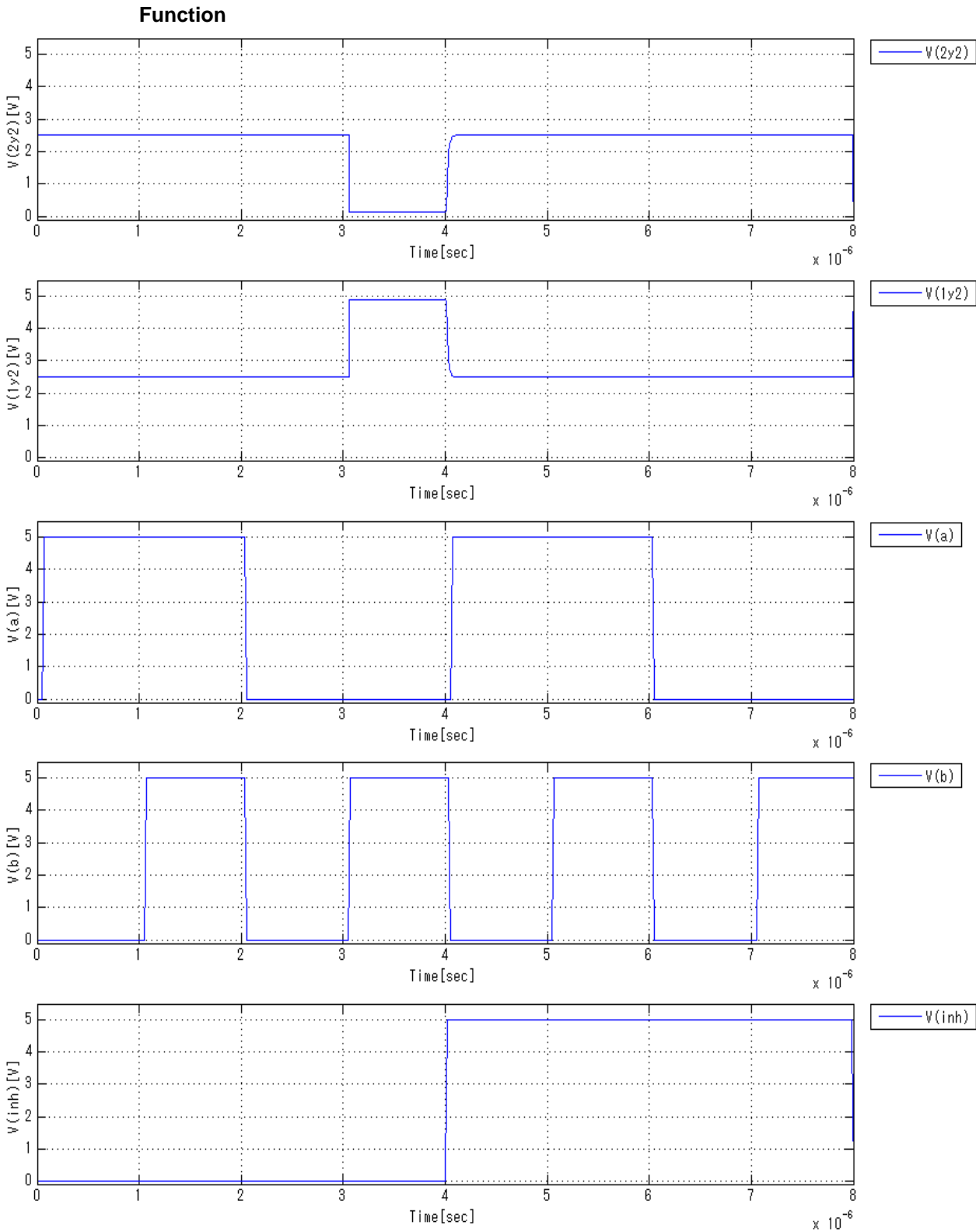
Simulation results are following.  
Explanatory notes    — : simulated

Transient2



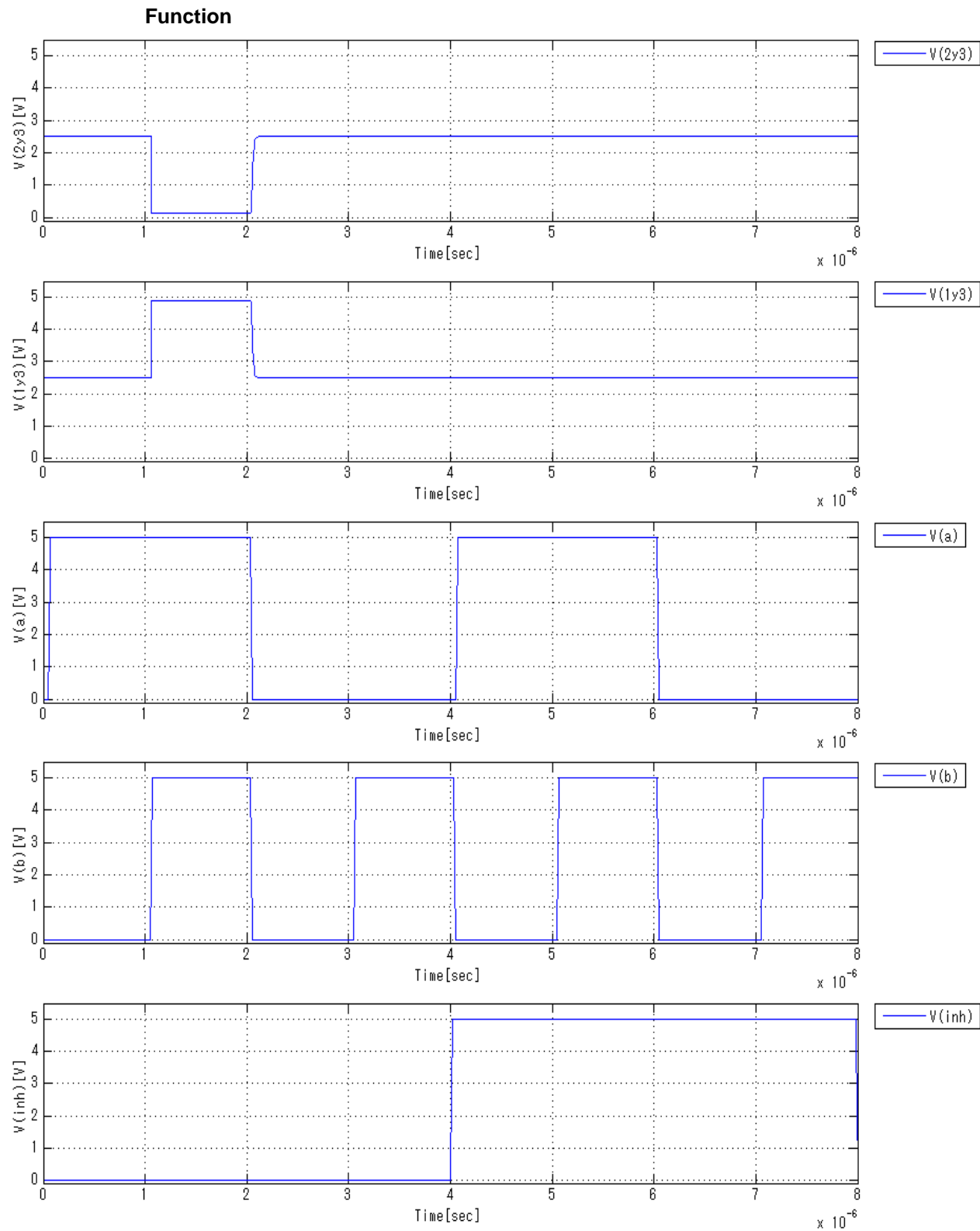
Simulation results are following.  
Explanatory notes    — : simulated

Transient3



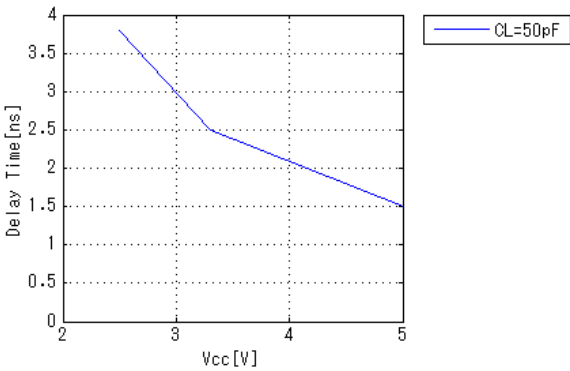
Simulation results are following.  
Explanatory notes    — : simulated

Transient4

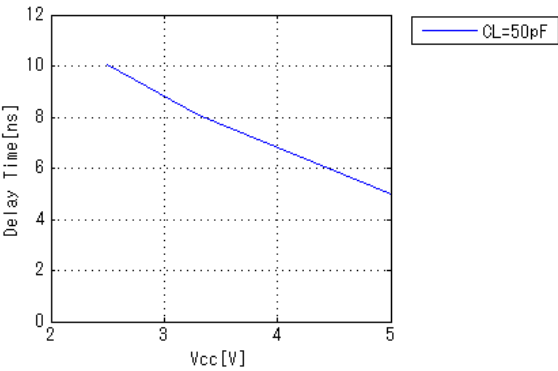


Simulation results are following.  
Explanatory notes    — : simulated

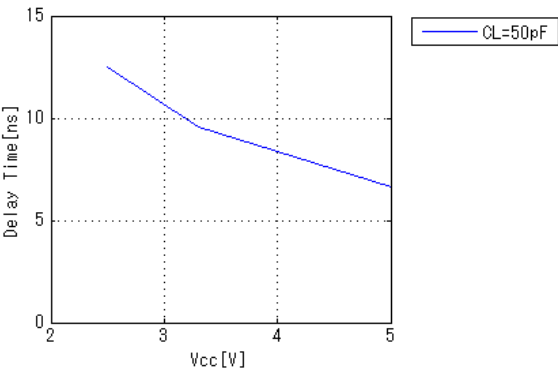
Propagation Delay Time



Enable Delay Time



Disable Delay Time



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