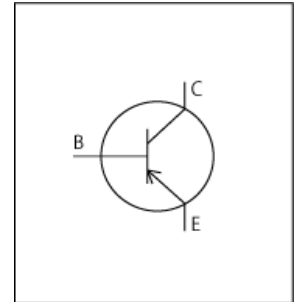


PSpice Model

PNP

ON

NSS60600MZ4T3G



Model Information

Model Gummel-Poon model
Call Name MDC_NSS60600MZ4T3G_PS
Pin Assign 1:C 2:B 3:E
File List Model Library MDC_NSS60600MZ4T3G_PS02.lib
 Model Report MDC_NSS60600MZ4T3G_PS.pdf (this file)

Verified Simulator Version PSpice version 16.6
Note

References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version Rev.5
- Product name NSS60600MZ4T3G
- Company name ON Semiconductor.
- Characteristics $hFE(Ic[Temp], Vce(sat)Ic[Temp], Vce(sat)Ib[Ic], IcVbe[Temp], Vbe(sat)Ic[Temp], Cib, Cob, fT(Ic[Vce])$

Simulation Range

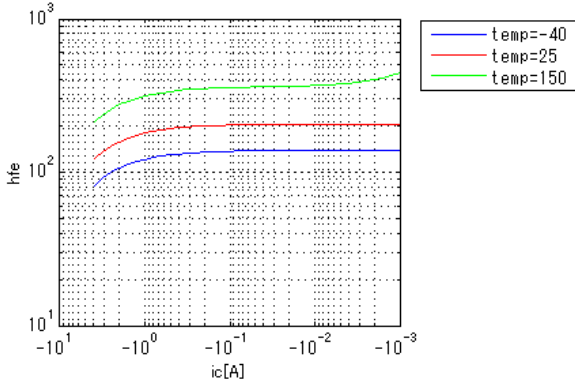
This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Collector-emitter voltage (DC)	0	to	-60	V
Base current (DC)	0	to	-0.60	A
Temperature	-55	to	150	deg C

Simulation results are following.
 Explanatory notes — : simulated

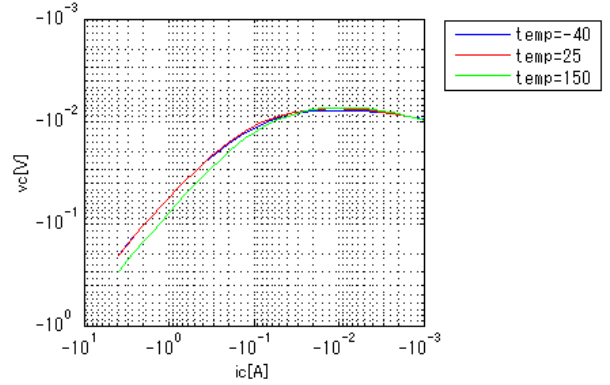
hFEIc[Temp]

Vce = -2V

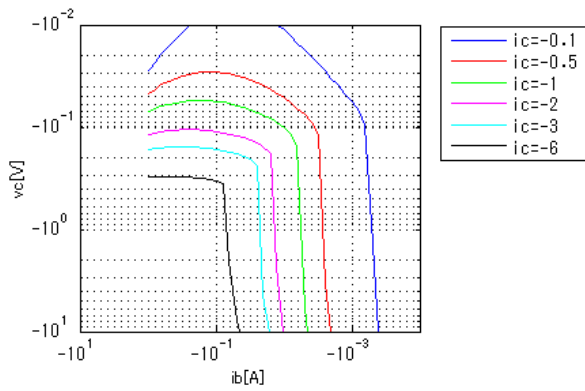


Vce(sat)Ic[Temp]

IC/IB = 10

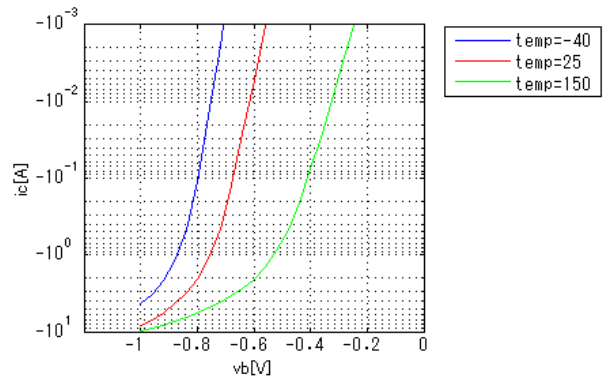


Vce(sat)Ib[Ic]



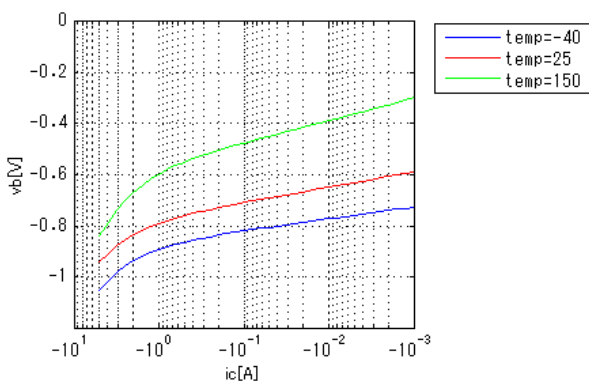
IcVbe[Temp]

Vce = -2V



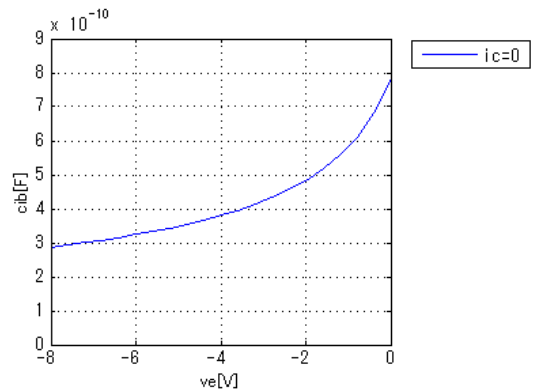
Vbe(sat)Ic[Temp]

IC/IB = 10



Cib

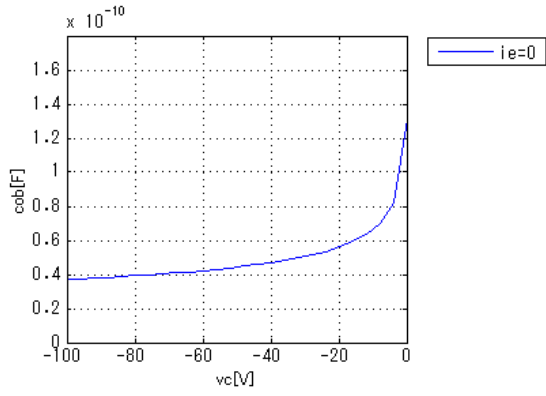
Freq. = 1MHz



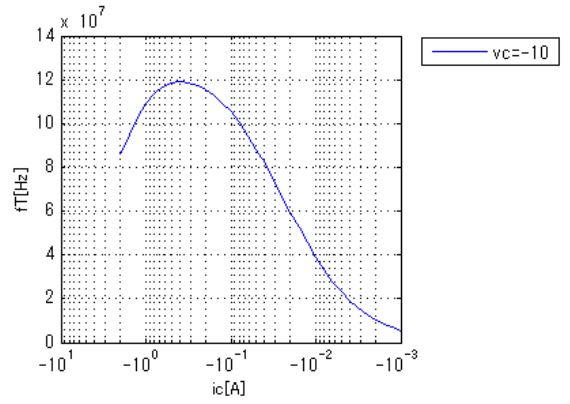
Simulation results are following.
 Explanatory notes — : simulated

Cob

Freq. = 1MHz



fTlc[Vce]



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