

PSpice Model

Inductor

Panasonic

ETQP4M1R0KVC

Model Information

Model A macro model
Call Name MDC_ETQP4M1R0KVC_PS
Pin Assign 1:P1 2:P2
File List Model Library MDC_ETQP4M1R0KVC_PS01.lib
 Model Report MDC_ETQP4M1R0KVC_PS.pdf(this file)
Verified Simulator Version Pspice 17.2

Note

References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version 2020/2/1
- Product name ETQP4M1R0KVC
- Company name Panasonic

[Characteristics listed]

- Characteristics LvsIDC

Simulation Condition

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Condition	Unit
Temperature	25	deg C

Model Functions Table

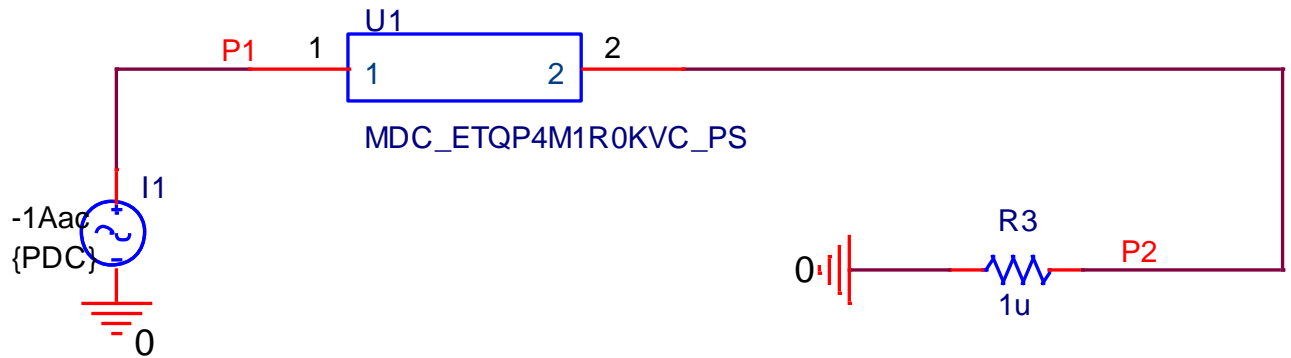
Functions	Implemented
L vs IDC	○

LvsIDC Testbench

Referred to Data Sheet

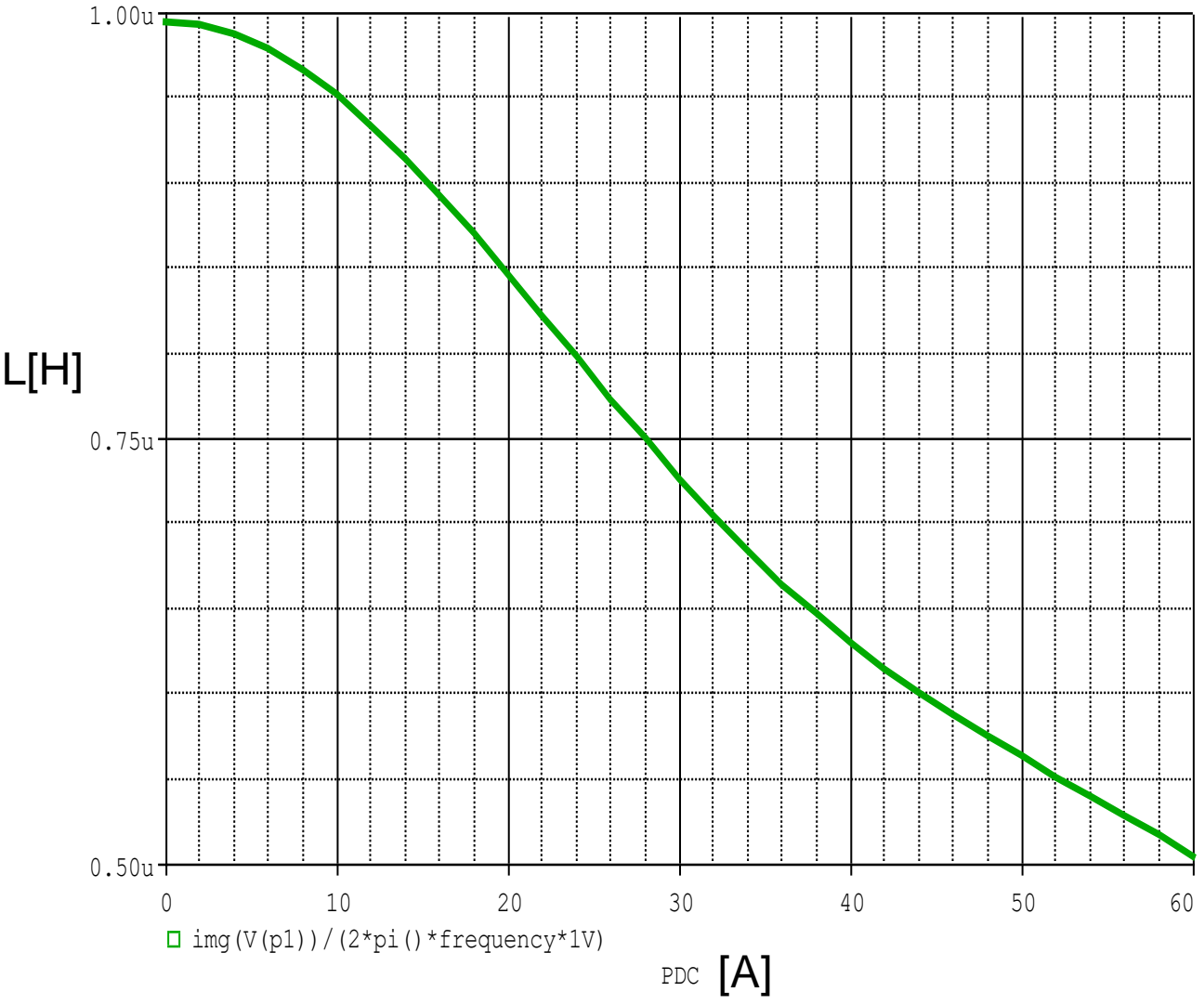
PARAMETERS:

PDC = 0



Simulation results are following.
 Explanatory notes — : simulated

LvsIDC



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