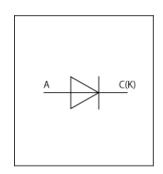


LTspice Model Fast Recovery Diode SHINDENGEN D30FD60K



Model Information

Model A macro model based on general SPICE diode model

Call Name MDC_D30FD60K_LT Pin Assign 1:NC 2:C 3:A 4:C

File List Model Library MDC_D30FD60K_LT02.lib

Model Report MDC_D30FD60K_LT.pdf (this file)

Verified Simulator Version

Note

LTspice version XVII

References

The information which was used for modeling is as follow:

[Data Sheet]

Date/VersionProduct nameRev.01(2020.01)D30FD60K

●Company name Shindengen Electric Manufacturing Co., Ltd.

●Characteristics IfVf[Temp],IrVr[Temp],CjVr,TrrlfIr,TrrWaveform,SurgeWavef

orm

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Reverse Voltage	0	to	600	V
Temperature	-55	to	150	deg C



Model Functions Table

Diode

O: Implemented

×: Not Implemented

—: Not applicable

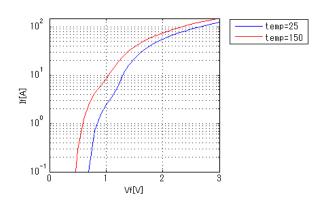
RANK=1

	10 000	
Functions	RANK	Implemented
IF-VF(Temp)	1	0
IR-VR(Temp)	1	0
Capacitance	1	0
Reverse recovery characteristics	1	0
Zz-Iz	1	_
Rectification characteristics(Bridge)	1	_
Surge-Transient	1	0
tlp	1	_

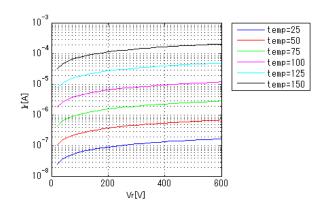


Simulation results are following. Explanatory notes — : simulated

IfVf[Temp]

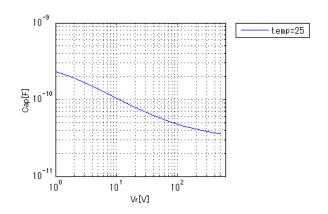


IrVr[Temp]



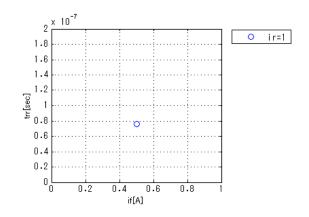
CjVr

Freq = 1000000Hz



Trrlflr

irr = 0.25A, didt = 25A/us



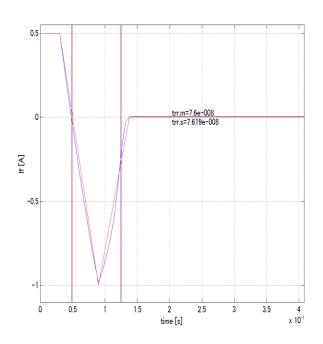


Simulation results are following.

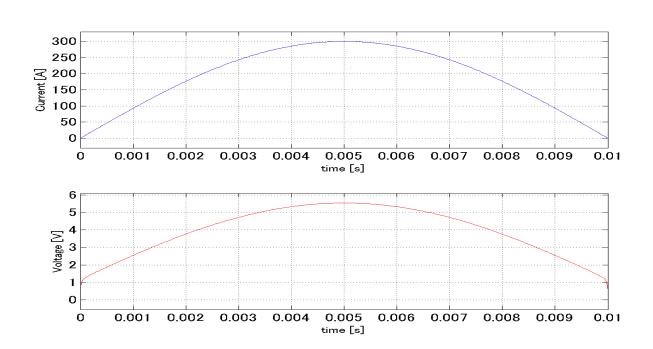
Explanatory notes — : simulated

Trr Waveform (Red: Datasheet Blue: Simulation)

didt = 25A/us, if = 0.5A, ir = 1A, irr = 0.25A



Surge Current Waveform (Forward sine=50Hz)





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