

# LTspice Model

## 7 CIRCUIT DARLINGTON TRANSISTOR ARRAY

### KEC

### KID65003AF

#### Model Information

**Model** An original macro model  
**Call Name** MDC\_KID65003AF\_LT  
**Pin Assign** 1:I1 2:I2 3:I3 4:I4 5:I5 6:I6 7:I7 8:GND 9:COMMON  
 10:Q7 11:Q6 12:Q5 13:Q4 14:Q3 15:Q2 16:Q1

**File List** Model Library MDC\_KID65003AF\_LT01.lib  
 Model Report MDC\_KID65003AF\_LT.pdf (this file)

**Verified Simulator Version** LTspice version XVII  
**Note**

#### References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version 12
- Product name KID65003AF
- Company name KEC Corporation
- Characteristics Transient,IN\_VIN[Temp],VCESAT[Temp],IF\_VF[Temp],  
hFE\_IOUT[Temp]

#### Simulation Condition

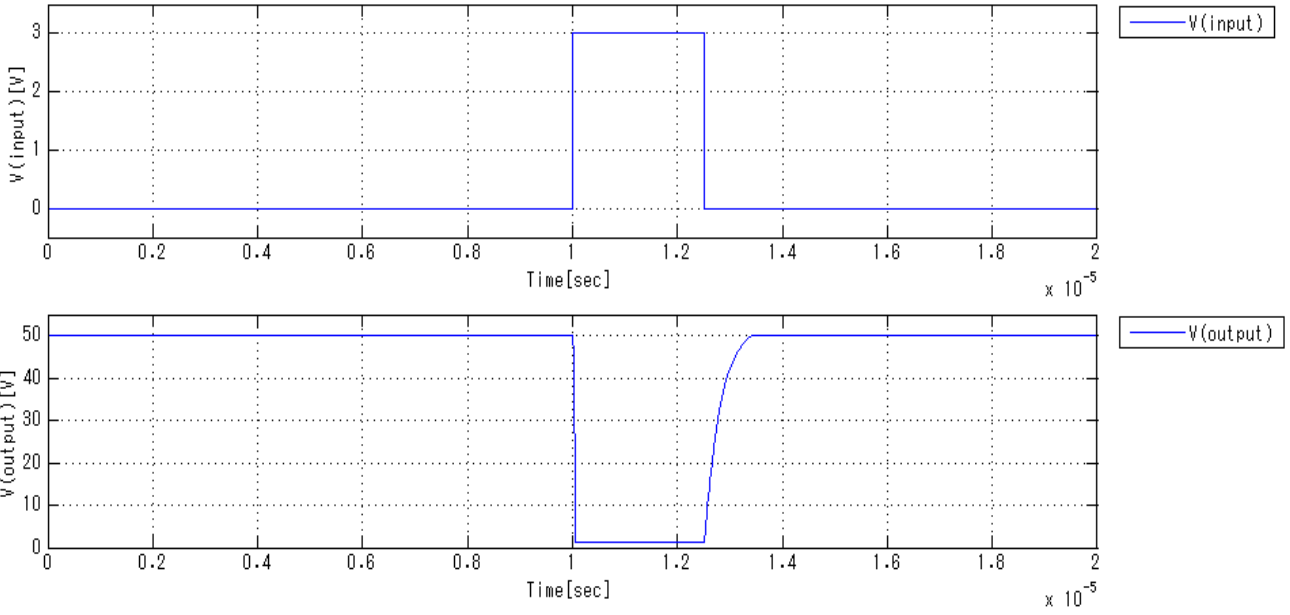
This table shows the value of evaluated simulation condition that was not occurs any convergence problems in this area.

Item	Condition	Unit
Temperature	25	deg C

Simulation results are following.  
Explanatory notes — : simulated

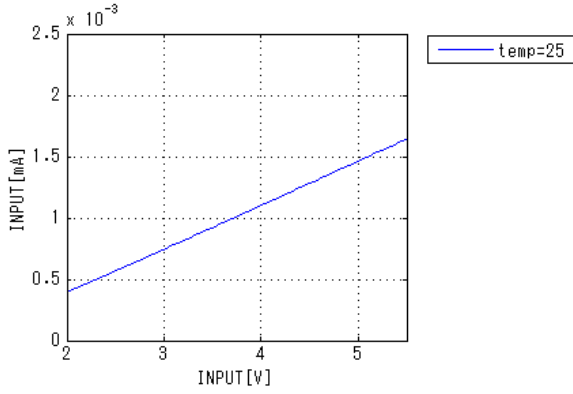
**Transient**

Temp. = 25deg C

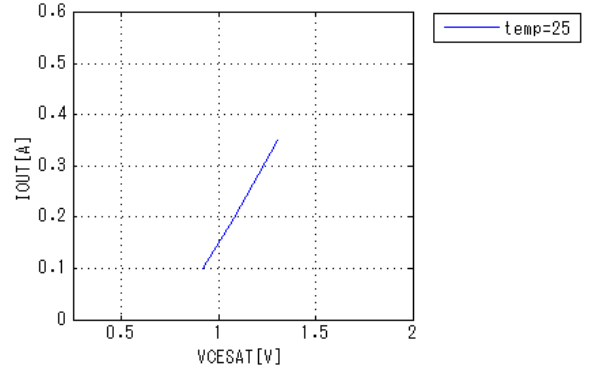


Simulation results are following.  
 Explanatory notes — : simulated

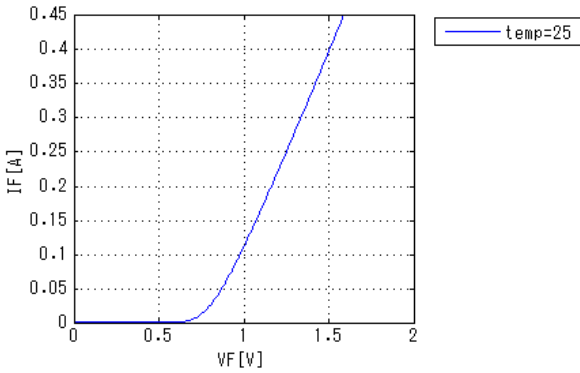
**IN\_VIN[Temp]**



**VCESAT[Temp]**

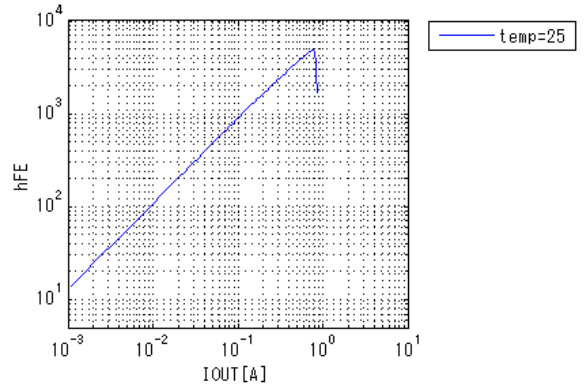


**IF\_VF[Temp]**



**hFE\_IOUT[Temp]**

VCE = 2.0V



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