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FH101RF DEVELOPMENT KIT

APPLICATION NOTE: FH101RF EVALUATION



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Introduction

1 Introduction

This document describes the essential components and steps of an evaluation. These are functions, tools and possible examples of an evaluation of the RFicient® radio receiver using FH101RF development kit. The development kit provides all the necessary evaluation tools and combines them in a graphical software application "WakeUpEvalSuite". This simplifies the interaction with the FH101RF radio receiver.

The main features of the software application are configuring the receiver, generating and sending data, creating and managing configurations, receiving and evaluating the data. The following chapters explain the main functions of WakeUpEvalSuite.

2 Getting Started

The structure of the application consists of a main window shown in the Figure 1, which opens at application startup. The main window shows all FH101RF development boards connected to the PC and recognized. The display is done by symbols and line entries with the information about the respective development board. The symbols in the upper area of the main window represent the buttons. A mouse click on the respective button starts the window of the application corresponding to the symbol name. The menu bar in the upper part of the main window represents additional functions like: Update devices and Change mode of the respective development boards.

The Eval firmware distinguishes between two types of modes.

- 1. TX(Tx-Kit): generation and parameterization of the transmission data as well as setting of the data rate, transmission power and periodicity of the telegrams
- 2. RX(FH101RF): parameterization of the FH101RF receiver as well as reception and evaluation of the transmitted data.

The current mode is displayed by the board mode leds attached to the development board. Additionally it is displayed in the main window of the application by the first two symbols of the firmware information.

Changing the mode is done as follows. We recommend, to connect only one development board to the PC to change the mode.

- Open WakeUpEvalSuite main window, select the "Devices" menu item, in the upper menu bar of the main window. Devices detected by the tool are displayed with their serial number. To change the mode the "change device mode" menu must be activated under the corresponding entry.
- The status led acknowledges the change
- The firmware reboots for about 2 seconds. Then press the refresh button, lower left corner, in the main window to update the devices. Mode is changed.

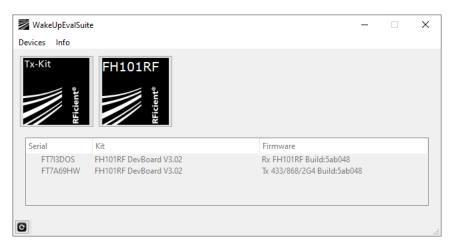


Figure 1: WakeUpEvalSuite Main Window

Note: Only one instance of a device mode (TX or RX) can be displayed in the main window. If it is necessary to run two development boards in same mode and PC, a second instance of WakeUpEvalSuite must be started.

TX-Mode

3 TX-Mode

As already mentioned, the TX mode offers all necessary tools for signal generation and transmission via radio. In the TX mode, shown in the Figure 2 of the WakeUpEvalSuite, all important parameters of the transmitter are available for setting. These are the radio band, data rate, data word, transmission power periodicity and number of telegrams. The available settings especially the radio band, frequency and transmission power are dependent on the respective development board firmware. These can differ depending on the firmware implementation.

Tx				-	- 🗆 X
RF Settings			Send Settings		_
TX Mode	RF Freq.	Power	Send Rep.	Rep. Pause(ms)	Infinite
Band 868 MHz	✓ 868.4 MHz ✓	0dBm \checkmark	1	150	on
Telegram Settings Preamble Data Rate Ikbps ✓ Send Data	Payload Data Rate Payloa 32kbps V 7da8	d(hex)		Payload Length 2 bytes	Export data Export
			Send		

Figure 2: TX-Mode

The following is a brief description of the TX mode settings:

- TX Mode: Radio band setting, 433/868/915/2G4
- RF Freq: Frequency setting for the respective band, important is the observance of the respective radio regulatory domain.
- Power: Tx power setting for the respective band and frequency, important is the observance of the respective radio regulatory domain
- Send Rep: Number of telegrams to be sent, without effect if Infinite is set
- Rep: Pause between the telegrams
- Infinite: Infinite sending until stop is activated
- Preamble DR: Preamble data rate, if enabled sends a WurX codeA with selected data rate
- Payload DR: Payload data rate, if enabled sends the data from the Payload(hex) encoded in Wurx format with selected data rate.
- Payload(HEX): The data to be sent, these are encoded before sending in WurX(A/B) format, these can be saved by Export button in a *.c/*.h file.
- Send Data: Starts the data transfer.

Note: If the settings of the preamble and payload data rate are different, the preamble (codeA) is spread to the payload data rate.

Note: If selective addressing of the wakeup is desired, the payload data rate must be set and the ID must be entered in the first two bytes of the data in the payload(HEX). If required, further data can be entered after the ID, max 6 bytes exclude the ID. The ID is evaluated and discarded by the receiver, i.e. it is not stored in the FIFO register of the receiver.

RX-Mode

4 RX-Mode

In RX mode, WakeUpEvalSuite provides all necessary tools for parameterization of the receiver. Important functions such as data reception and evaluation are available to the user of the application. Furthermore, WakeUpEvalSuite offers the possibility to store and manage user configurations. These are conveniently loaded from the application into the receiver IC with a mouse click. An export function is also available to the user. This saves the loaded configuration in a *.c/*.h file.

The current flow and power consumption at the FH101RF receiver is displayed in the window title of the RX window. These are measured values of the current measuring circuit of the development boards. Thus, the power consumption for the loaded configuration of the receiver is immediately visible.

Configurations Browser Received Data and Stati	RX							
	stics					Register	Name	Value (hex)
					RX Statistics	> 0x0 > 0x1	NFA433_SLOW NFA433_FAST	0x5 0x0
Size Data	Event Band 0x20 868/91	RxQuality A/B RxBrar 96.8%/90.3% WM	ch Time 22:02:01		Received Count	> 0x1	NFA868 SLOW	0x5
ID		96.8%/90.3% WM 96.8%/93.5% WM	22:02:01		12	> 0x2	NFA868_FAST	0x0
ID		93.5%/90.3% WM	22:02:02		12	> 0x3	NFA2G4_SLOW	0x0 0x5
ID		83.9%/87.1% WM	22:02:03		Branch W/M/S Ø%	> 0x4	NFA2G4_SLOW	0x0
ID		93.5%/87.1% WM	22:02:05			> 0x5	CALIB_STATUS	0x0
ID 0x6 affe1234567		87.1%/93.5% WM 93.5%/93.5% WM	22:02:07 22:02:20		100.00/91.67/0.00			0x8
0x6 affe1234567		93.5%/90.3% WM	22:02:20				CALIB_CTRL	0x5
0x6 affe1234567		93.5%/90.3% WM	22:02:22		Quality A/B Ø%		TSP_CTRL	0x31
ID		90.3%/93.5% WM	22:02:30		92.74/91.13	- 0.5	N_SPG_TARGET	
ID		96.8%/93.5% WM	22:02:31		52.77/51.15	> 0xa	SPG_FREQ	0x0
ID	0x20 868/91	93.5%/90.3% W	22:02:48			> 0xb	N_LCO_TARGET_433	0xe
					RX Control	> 0xc	N_LCO_TARGET_433	0x20
						> 0xd	N_LCO_TARGET_868	0xd
						> 0xe	N_LCO_TARGET_868	0x87
						> 0xf	N_LCO_TARGET_2G4	0x12
					Clear	> 0x10	N_LCO_TARGET_2G4	0x2e
						> 0x11	LCO_RANGE_433	0xe
						> 0x12	LCO_RANGE_868	0x1f
						> 0x13	LCO_RANGE_2G4	0x28
					Stop RX	> 0x14	LCO_FREQ_433	0xe
						> 0x15	LCO_FREQ_433	0x22
						> 0x16	LCO_FREQ_868	0xd
						> 0x17	LCO_FREQ_868	0x87
g Settings						> 0x18	LCO_FREQ_2G4	0x12
Address Settings		Source	FiFo Length			> 0x19	LCO_FREQ_2G4	0x3d
-			-	Mus Decido	58 MHz Band 433 MHz	> 0x1a	COMPREF_W_433	0x15
ID (hex) Ma	sk	CodeA/E	Band 240) MHz Band 8	58 MHz Band 433 MHz	> 0x1b	COMPREF_W_868	0x14
		D				> 0x1c	COMPREF_W_2G4	0x1f
7da8 II)		24 Bit	✓ 24 Bit	✓ 16 Bit ✓	> 0x1d	COMPREF_M_433	0x1f
		FiFo Len	jth			> 0x1e	COMPREF_M_868	0x1f
		FiFo Ove	rflow			> 0x1f	COMPREF_M_2G4	0x28
						> 0x20	COMPREF_S_433	0x2c
				— .		> 0x21	COMPREF_S_868	0x2c
		Band 868/915 MHz 9	ettings		MHz Settings	> 0x22	COMPREF_S_2G4	0x30
				Rx Frequer	cy	> 0x23	D_CORNER_CTRL	0x0
		Rx Frequency				> 0x24	BAND_BRANCH_CTRL	0x27
x Frequency		Rx Frequency 867		2407				0x0
x Frequency		867				> 0x25	TESTBUF_CTRL	
x Frequency 434 ata Rate		867 Data Rate		Data Rate		> 0x26	IFAMP_GAIN_CTRL	0x2a
Band 433 MHz Settings x Frequency 434 ata Rate Preamble		867					-	
x Frequency 434 ata Rate		867 Data Rate		Data Rate	5	> 0x26	IFAMP_GAIN_CTRL	0x2a
x Frequency 434 4ata Rate Preamble		867 Data Rate Preamble	~	Data Rate Preamble		> 0x26 > 0x27	IFAMP_GAIN_CTRL RX_ACTIVE_SELECT	0x2a 0x3

Figure 3: RX-Mode

Figure 3 shows the RX mode of the WakeUpEvalSuite application. In the right window area of the application, the register entries of the receiver are located, these can be parameterized comfortably by direct input or by folding out the respective register. The left part of the application window provides predefined functions of the application. These are settings of RX frequency, data rate, interrupt source, setting of ID and

	-
RX-Mode	
	-

data reception. They simplify the setting for the users without detailed knowledge about the receiver. Settings that are made using predefined functions are mapped to register entries immediately after the setting. This applies vice versa also for the direct input of the register values.

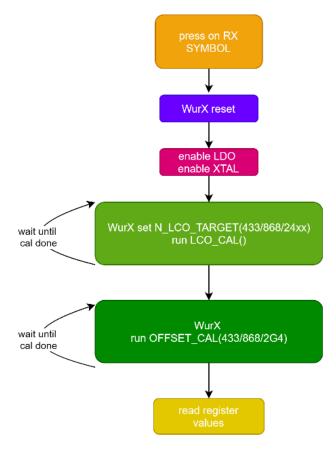


Figure 4: RX Symbol-Trigger sequence

When the RX mode is started by clicking on the RX symbol (FH101RF) of the application, a sequence of defined initialization steps of the receiver takes place see Figure **4**.

This establishes an operational state of the receiver. The initialization sequence can be performed at any time by clicking on the RX symbol (FH101RF) of the application. Even if RX window of the application is already open.

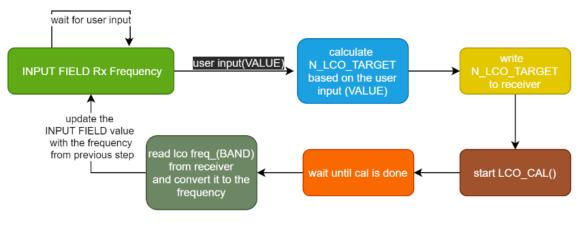


Figure 5: Frequency input sequence

RX-Mode		

Note: When entering of the receive frequency, frequency of the transmitter, through the Rx Frequency input field, the application executes the sequence as shown in the Figure 5. The LO Target values are calculated based on the following parameters: User input of frequency, Intermediate frequency = 40 MHz, Hi Side LO injection.

Note: It may happen that the displayed frequency differs from the user input by a few units. This is not an error but is due to the granularity of the adjustability of the LO frequency and has no effect on the receiver RF performance.

In the following the predefined functions as well as the parameters of data reception in RX mode are described.

Received Data and Statistics: Shows the entries of the received data.

- Size: These have the number of received payload bytes if available.
- Data: If an address is received without subsequent payload data, only the ID is displayed; if additional payload data has been received, this is displayed instead of the ID. The display shows CodeA or CodeB if they have been received and the interrupt source is set to CodeA/B.
- Event: Shows the event code which has triggered the entry. Details of the event code, see data sheet of the FH101RF.
- BAND: Shows the band on which the telegram was received.
- Signal Quality A/B: Values of the KORREL_VAL register. These indicate how many errors a received CodeA or CodeB had. For further information the data sheet of the FH101RF should be consulted. Important: The value is only an indication and does not give any far-reaching information about reception errors.
- Time: The local time at the time of reception
- Received Count: Number of telegrams received, this is reset to zero after pressing the Clear button.
- Quality A/B avg: Average of all Signal Quality A/B values, this is reset to zero after pressing the Clear button.
- StartRX/StopRX: Starts or stops data reception

Irq Setting: Sets the interrupts and the ID of the receiver.

- ID: Setting the id of the receiver, expects input of a 2-byte hex number
- Mask: Setting of the ID mask, possible settings are ID (Triggers only exactly at the ID), GroupID (Triggers only exactly at the GroupID), BroadcastID (Triggers only exactly at the BroadcastID) and combinations of all.
- Source: Activation of the interrupt sources.
- FiFo Length: Setting of the FiFo threshold for interrupt generation, ineffective without FiFo Length interrupt activation

Band433/(868/915)/2G4 MHz Settings: Sets the frequency and data rate of the respective band.

- Rx Frequency: Reception frequency of the respective band, expects a decimal integer number. Important: The **frequency of the transmitter** must be entered here.
- Preamble: Sets the data rate of the preamble.
- Payload: If an addressed WakeUp or data reception is required, this setting must be activated and the data rate must be set accordingly. Important: For multi-band use, not all data rate combinations are functional, please consult the FH101RF data sheet.

					-					-		
Board Mode	Firmware	Red LED (TX)	Green LED (RX)	Antenna TxRx	Antenna RFin433	Antenna RFin868/915	Antenna RFin2G4	D2XX Driver	USB – PC connection	Pin-Header-Jumpers	CR2032	Onboard current meas. switch
RX	evalfw _(vers) _(build).hex	off	on	-	yes, if the Band 433 MHz is used	yes, if the Band 868 MHz or 915 MHz is used	yes, if the Band 24xx MHz is used	yes	yes	Fully equipped	No	on
ТХ	evalfw _(vers) _(build).hex	on	off	yes according to the transmis- sion fre- quency	-	-	-	yes	yes	Fully equipped	No	-

5 Assembly and Function Matrix

6 Evaluating FH101RF

With any radio receiver, the question about the reliability of the data reception in a certain radio environment arises or if any data is lost. The ratio between data received and sent determines the average bit error ratio (BER = 1 – received data amount/sent data amount). In the case of the RFicient® FH101RF receiver, data is encoded in bit-error tolerant codeA or codeB sequences, and thus not the erroneous bits but the erroneous code sequences are relevant.

This ratio is referred to as WUER (WakeUp error rate).

This section shows two possible ways to address specific nodes to receive data with FH101RF In different radio environments. Before starting, it must be ensured that the hardware kits are ready, that necessary USB drivers are installed and that the hardware is connected to the computer.

6.1.1 Use Case 1

Requirements:

A network with an abstract main radio is to be set up, that consists of several Ultra Low Power radio sensors nodes that are to be woken up with very low latency when required. Waking up a specific node with a unique ID in form of a network address should be possible.

The network requirements are:

- RF-Frequency: 868 MHz
- WakeUp Latency: Max. 80 ms
- System current consumption: max. 5 μA
- The network is controlled by a master node

FH101RF Configuration, Sensor Node

To meet the requirement, the following setting must be set on the FH101RF:

- Enable the required band 868 MHz: Set the Band to the 868 MHz setting and the payload check box in the FH101RF RX tab. In order to save power, unused bands are switched off and the preamble data rate is set to 1k (1 kbps) and the payload to 32k (32 kbps). A further reduction in the preamble data rate further lowers the current consumption of the FH101RF, but at the expense of latency.
 - In order to use the request for the addressed WakeUp, the ID interrupt check-box must be activated, all other interrupts are off. The corresponding check-box can be found in the FH101RF RX tab under irq Settings / Source.

- The ID is set to the default value: 0x7da8. For test purposes, only one node is used in this example. In real world use cases, each node gets its own ID.
- In this case no broadcast or group WakeUps are required, therefore ID-Mask is set to "ID". It can be found in the FH101RF RX tab irq Settings / Mask.

Figure 9 shows the resulting GUI after setting it up with the aforementioned example.

TX Configuration, Master Node

In order to WakeUp a node which was configured according to the description above, the following settings must be made on the transmitter (TX) side:

- Select the Band 868 MHz on which the network nodes receive. The RF frequency will be set automatically to 868.39 MHz.
- Select the transmit power depending on which range between master and node is required. Higher power equals more range. Please note the regulatory domain requirements regarding TX power in the respective RF band.
- Set the preamble and payload data rate to 1k and 32k.
- Set the Payload to the ID of the node, in this example the ID is 0x7da8.
- "Send Rep." and "Rep pause(ms)" can remain in the standard setting.

In order to visualize the discussed TX settings, they are shown in Figure 10.

	V					- 0	×
nfigurations Browser RX				Register	Name	Value (hex)	^
eceived Data and Statistics			RX Statistics	> 0x3	NFA868_FAST	0x0	
Size Data Event Band	Signal Quality A/B Time		Received Count	> 0x4	NFA2G4_SLOW	0x5	
res l'este l'étern l'heart l'h			Received Count	> 0x5	NFA2G4_FAST	0x0	
			0	> 0x6	CALIB_STATUS	0x0	
				> 0x7	CALIB_CTRL	0x8	
			Quality A avg	> 0x8	TSP_CTRL	0x5	
				> 0x9	N_SPG_TARGET	0x31	
				> Oxa	SPG_FREQ	0x0	
			Quality B avg	> 0xb	N_LCO_TARGET_433	Oxe	
			don't only	> 0xc	N_LCO_TARGET_433	0x20	
				> 0xd	N_LCO_TARGET_868	Oxd	
				> Oxe	N_LCO_TARGET_868	0x87	
			RX Control	> Oxf	N_LCO_TARGET_2G4	0x12	
				> 0x10	N_LCO_TARGET_2G4	Oxce	
				> 0x11	LCO_RANGE_433	0x24	
				> 0x12	LCO_RANGE_868	0x35	
			Clear	> 0x13	LCO_RANGE_2G4	0x23	
				> 0x14	LCO_FREQ_433	0xe	
				> 0x15	LCO_FREQ_433	0x24	
			· · · · · · · · · · · · · · · · · · ·	> 0x16	LCO_FREQ_868	Oxd	
			Start RX	> 0.17	LCO FREQ 868	0x89	
				> 0x17			
				> 0x17 > 0x18	LCO_FREQ_2G4	0x12	
				2000	LCO_FREQ_2G4 LCO_FREQ_2G4	0x12 0xd3	
				> 0x18 > 0x19 > 0x1a			
				> 0x18 > 0x19	LCO_FREQ_2G4	0xd3	
				> 0x18 > 0x19 > 0x1a	LCO_FREQ_2G4 COMPREF_W_433	0xd3 0x1e	
Address Settings		Length		> 0x18 > 0x19 > 0x1a > 0x1b > 0x1c > 0x1d	LCO_FREQ_2G4 COMPREF_W_433 COMPREF_W_868	0xd3 0x1e 0x1d 0x1b 0x24	
q Settings Address Settings ID (hex) Mask	CodeA/B Ba	Length Band 868 M	Htz Band 433 MHz	> 0x18 > 0x19 > 0x1a > 0x1b > 0x1b > 0x1c > 0x1d > 0x1e	LCO_FREQ_2G4 COMPREF_W_433 COMPREF_W_868 COMPREF_W_2G4 COMPREF_M_433 COMPREF_M_868	0xd3 0x1e 0x1d 0x1b 0x24 0x24	
Address Settings			NHz Band 433 MHz	> 0x18 > 0x19 > 0x1a > 0x1b > 0x1c > 0x1c > 0x1d > 0x1e > 0x1f	LCO_FREQ_2G4 COMPREF_W_433 COMPREF_W_868 COMPREF_W_2G4 COMPREF_M_488 COMPREF_M_868 COMPREF_M_2G4	0xd3 0x1e 0x1d 0x1b 0x24 0x24 0x24	
Address Settings ID (hex) Mask	CodeA/B Ba	nd 2400 MHzBand 868 M		 > 0x18 > 0x19 > 0x1a > 0x1b > 0x1c > 0x1d > 0x1e > 0x1f > 0x1f > 0x20 	LCO_FREQ_2G4 COMPREF_W_433 COMPREF_W_868 COMPREF_W_2G4 COMPREF_M_433 COMPREF_M_868 COMPREF_M_2G4 COMPREF_M_2G4	0xd3 0x1e 0x1d 0x1b 0x24 0x24 0x24 0x24 0x24	
Address Settings ID (hex) Mask	CodeA/B Ba	nd 2400 MHzBand 868 M		 > 0x18 > 0x19 > 0x1a > 0x1b > 0x1c > 0x1c > 0x1d > 0x1d > 0x1f > 0x1f > 0x20 > 0x21 	LCO_FREQ_2G4 COMPREF_W_433 COMPREF_W_868 COMPREF_W_264 COMPREF_M_368 COMPREF_M_264 COMPREF_M_264 COMPREF_S_433 COMPREF_S_868	0xd3 0x1e 0x1d 0x1b 0x24 0x24 0x24 0x24 0x1c	
Address Settings ID (hex) Mask	CodeA/B Ba	nd 2400 MHzBand 868 M		 > 0x18 > 0x19 > 0x1a > 0x1b > 0x1c > 0x1d > 0x1d > 0x1e > 0x1e > 0x1e > 0x1e > 0x12 > 0x2 	LCO_FREQ_2G4 COMPREF_W_333 COMPREF_W_868 COMPREF_W_2G4 COMPREF_W_2G4 COMPREF_M_268 COMPREF_M_264 COMPREF_S_433 COMPREF_S_264	0xd3 0x1e 0x1d 0x24 0x24 0x24 0x24 0x1c 0x1c 0x1c	
Address Settings ID (hex) Mask	CodeA/B Ba	nd 2400 MHzBand 868 M		 > 0x18 > 0x19 > 0x1a > 0x1b > 0x1b > 0x1c > 0x1d > 0x1d > 0x1e > 0x1f > 0x20 > 0x21 > 0x22 > 0x23 	LCO_FREQ_2G4 COMPREF_W_433 COMPREF_W_264 COMPREF_M_264 COMPREF_M_868 COMPREF_M_264 COMPREF_S_433 COMPREF_S_868 COMPREF_S_264 D_CORNER_S_264	0xd3 0x1e 0x1d 0x1b 0x24 0x24 0x24 0x24 0x24 0x1c 0x1c 0x1c 0x0	
Address Settings ID (hex) Mask 7da8 ID	CodeA/B Be ID IDSLOW FFo Length FFo Overflow	A Bit V 24 Bit	✓ 16 Bit ✓	 > 0x18 > 0x19 > 0x1a > 0x1a > 0x1b > 0x1c > 0x1c > 0x1d > 0x1c > 0x12 > 0x21 > 0x22 > 0x24 	LCO_FREQ_2G4 COMPREF_W_433 COMPREF_W_868 COMPREF_W_2G4 COMPREF_M_433 COMPREF_M_4368 COMPREF_M_2G4 COMPREF_S_433 COMPREF_S_433 COMPREF_S_2G4 D_CORNER_CTRL BAND_BRANCH_CTRL	0xd3 0x1e 0x1d 0x24 0x24 0x24 0x24 0x24 0x1c 0x1c 0x1c 0x1c 0x1c 0x1c	
Address Settings ID (hex) Mask Zda8 ID Band 433 MHz Settings Interface	CodeA/B ID IDSLOW FFo Length FFo Overflow	nd 2400 MHz Band 868 M 4 Bit V 24 Bit Band 2400 MH	✓ 16 Bit ✓	 > 0x18 > 0x19 > 0x1a > 0x1b > 0x1c > 0x21 > 0x21	LCO_FREQ_2G4 COMPREF_W_433 COMPREF_W_868 COMPREF_W_2G4 COMPREF_M_433 COMPREF_M_868 COMPREF_M_868 COMPREF_S_433 COMPREF_S_433 COMPREF_S_868 COMPREF_S_264 D_CORNER_CTRL BAND_BRANCH_CTRL TESTBUF_CTRL	0xd3 0x1e 0x1d 0x24 0x24 0x24 0x24 0x1c 0x1c 0x1c 0x1c 0x27 0x0	
Address Settings ID (hex) Mask Zda8 ID Band 433 MHz Settings Interface	CodeA/B Be ID IDSLOW FFo Length FFo Overflow	A Bit V 24 Bit	✓ 16 Bit ✓	 > 0x18 > 0x19 > 0x1a > 0x1b > 0x1c > 0x1d > 0x12 > 0x20 > 0x21 > 0x22 > 0x21 > 0x22 > 0x21 > 0x21 > 0x22 > 0x21 > 0x21 > 0x21 > 0x22 > 0x21 > 0x22 > 0x21 > 0x21	LCO_FREQ_2G4 COMPREF_W_333 COMPREF_W_368 COMPREF_W_2G4 COMPREF_W_2G4 COMPREF_M_268 COMPREF_S_268 COMPREF_S_264 D_CORNER_CTRL BAND_BRANCH_CTRL IFAMP_GAIN_CTRL	0xd3 0x1e 0x1d 0x24 0x24 0x24 0x24 0x1c 0x1c 0x1c 0x1c 0x1c 0x20 0x27 0x0 0x23	
Address Settings ID (hex) Mask Zda8 ID Sand 433 MHz Settings K Frequency	CodeA/B ID IDSLOW FFo Length FFo Overflow	nd 2400 MHz Band 868 M 4 Bit V 24 Bit Band 2400 MH	✓ 16 Bit ✓	 > 0x18 > 0x19 > 0x1a > 0x1b > 0x1c > 0x1d > 0x1c > 0x1d > 0x1e > 0x1d > 0x20 > 0x21 > 0x21 > 0x24 > 0x25 > 0x26 > 0x27 	LCO_FREQ_2G4 COMPREF_W_433 COMPREF_W_264 COMPREF_M_264 COMPREF_M_368 COMPREF_M_264 COMPREF_S_463 COMPREF_S_463 COMPREF_S_264 D_CORNER_CTRL BAND_BRANCH_CTRL TESTBUF_CTRL IFAMP_GAIN_CTRL RX_ACTIVE_SELECT	0xd3 0x1e 0x1d 0x1b 0x24 0x24 0x24 0x24 0x1c 0x1c 0x1c 0x1c 0x0 0x27 0x0 0x23 0x3	
Address Settings ID (hex) Mask 7da8 ID Sand 433 MHz Settings K Frequency 34	CodeA/B ID IDSLOW FFo Length FFo Cength FFo Overflow Band 868 MHz Settings Rx Frequency 868	A Bit Carlos A Band 868 M 4 Bit Carlos A Band 2400 MHz Rand 2400 MHz Rand 2400 MHz 2486	✓ 16 Bit ✓	 > 0x18 > 0x19 > 0x1a > 0x1b > 0x1b > 0x1c > 0x1d > 0x1d > 0x1d > 0x1c > 0x1d > 0x20 > 0x21 > 0x22 > 0x23 > 0x24 > 0x25 > 0x26 > 0x27 > 0x28 	LCO_FREQ_2G4 COMPREF_W_433 COMPREF_W_868 COMPREF_W_2G4 COMPREF_M_433 COMPREF_M_264 COMPREF_M_264 COMPREF_S_868 COMPREF_S_868 COMPREF_S_264 D_CORNER_CTRL BAND_BRANCH_CTRL TESTBUF_CTRL IFAMP_GAIN_CTRL RX_ACTINE_SELECT CODE_SELECT	0xd3 0x1e 0x1d 0x1b 0x24 0x24 0x24 0x24 0x1c 0x1c 0x1c 0x1c 0x0 0x27 0x0 0x27 0x0 0x27 0x0 0x2a 0x3 0x10	
ID (hex) Mask Zda8 ID Band 433 MHz Settings x Frequency 434 ata Rate	CodeA/B Data Rate	A Bit Carlos A Band 2400 MHz Band 2400 MHz 24 Bit 24 Bit Band 2400 MH Rx Frequency 2486 Data Rate	✓ 16 Bit ✓	 > 0x18 > 0x19 > 0x1a > 0x1a > 0x1a > 0x1c > 0x21 > 0x21 > 0x21 > 0x21 > 0x23 > 0x24 > 0x25 > 0x26 > 0x27 > 0x28 > 0x29 	LCO_FREQ_2G4 COMPREF_W_433 COMPREF_W_868 COMPREF_W_2G4 COMPREF_M_433 COMPREF_M_4368 COMPREF_M_4368 COMPREF_S_464 COMPREF_S_464 D_CORNER_CTRL BAND_BRANCH_CTRL IFAMP_GAIN_CTRL IFAMP_GAIN_CTRL RX_ACTIVE_SELECT CODE_SELECT KORREL_THRESH_A	0xd3 0x1e 0x1d 0x24 0x24 0x24 0x24 0x24 0x24 0x1c 0x1c 0x1c 0x1c 0x27 0x0 0x27 0x0 0x27 0x0 0x23 0x3 0x3 0x10 0x1a	
Address Settings ID (hex) Mask Zda8 ID Band 433 MHz Settings ID sx Frequency 434 ata Rate Preamble	CodeA/B Data Rate Preamble	A Bit Carlos A Band 2400 MHz Band 2400 MHz 24 Bit 24 Bit 24 Bit Band 2400 MH Rx Frequency 2486 Data Rate Preamble Preamble	✓ 16 Bit ✓	 > 0x18 > 0x19 > 0x1a > 0x1b > 0x1c > 0x1d > 0x1e > 0x10 > 0x21 > 0x20 > 0x21 > 0x22 > 0x23 > 0x24 > 0x25 > 0x26 > 0x27 > 0x28 > 0x29 > 0x29 > 0x2a 	LCO_FREQ_2G4 COMPREF_W_333 COMPREF_W_368 COMPREF_W_2G4 COMPREF_W_2G4 COMPREF_M_2G4 COMPREF_M_2G4 COMPREF_S_2G4 D_CORNER_CTRL BAND_BRANCH_CTRL BAND_BRANCH_CTRL IFAMP_GAIN_CTRL RX_ACTIVE_SELECT CODE_SELECT KORREL_THRESH_A KORREL_THRESH_B	0xd3 0x1e 0x1d 0x24 0x24 0x24 0x1c 0x1c 0x1c 0x1c 0x1c 0x2a 0x2 0x0 0x27 0x0 0x2a 0x3 0x10 0x1a 0x1a	
Address Settings ID (hex) Mask Zda8 ID Band 433 MHz Settings x Frequency 434 ata Rate	CodeA/B Data Rate Preamble	A Bit Carlos A Band 2400 MHz Band 2400 MHz 24 Bit 24 Bit Band 2400 MH Rx Frequency 2486 Data Rate	✓ 16 Bit ✓	 > 0x18 > 0x19 > 0x1a > 0x1b > 0x1c > 0x1d > 0x1c > 0x1d > 0x1c > 0x1d > 0x12 > 0x21 > 0x22 > 0x23 > 0x24 > 0x25 > 0x26 > 0x27 > 0x28 > 0x29 > 0x28 > 0x29 > 0x28 > 0x28 > 0x29 > 0x28 	LCO_FREQ_2G4 COMPREF_W_333 COMPREF_W_264 COMPREF_W_264 COMPREF_M_264 COMPREF_M_264 COMPREF_S_433 COMPREF_S_433 COMPREF_S_264 D_CORNER_CTRL BAND_BRANCH_CTRL BAND_BRANCH_CTRL IFAMP_GAIN_CTRL IFAMP_GAIN_CTRL RX_ACTIVE_SELECT CODE_SELECT KORREL_THRESH_B KORREL_STATE	0xd3 0x1e 0x1d 0x24 0x24 0x24 0x24 0x1c 0x1c 0x1c 0x1c 0x1c 0x1c 0x2 0x0 0x27 0x0 0x23 0x3 0x3 0x10 0x1a 0x1a 0x1a 0x43	
Address Settings ID (hex) Address Settings ID ID ID ID ID II III III III III III	CodeA/B Data Rate Preamble	A Bit Carlos A Band 2400 MHz Band 2400 MHz 24 Bit 24 Bit 24 Bit Band 2400 MH Rx Frequency 2486 Data Rate Preamble Preamble	✓ 16 Bit ✓	 > 0x18 > 0x19 > 0x1a > 0x1b > 0x1c > 0x1d > 0x1d > 0x1d > 0x1d > 0x1d > 0x1d > 0x1e > 0x10 > 0x10 > 0x10 > 0x10 > 0x21 > 0x20 > 0x21 > 0x21 > 0x22 > 0x21 > 0x22 > 0x24 > 0x25 > 0x26 > 0x27 > 0x28 > 0x29 > 0x29 > 0x24 	LCO_FREQ_2G4 COMPREF_W_333 COMPREF_W_368 COMPREF_W_2G4 COMPREF_W_2G4 COMPREF_M_2G4 COMPREF_M_2G4 COMPREF_S_2G4 D_CORNER_CTRL BAND_BRANCH_CTRL BAND_BRANCH_CTRL IFAMP_GAIN_CTRL RX_ACTIVE_SELECT CODE_SELECT KORREL_THRESH_A KORREL_THRESH_B	0xd3 0x1e 0x1d 0x24 0x24 0x24 0x1c 0x1c 0x1c 0x1c 0x1c 0x2a 0x2 0x0 0x27 0x0 0x2a 0x3 0x10 0x1a 0x1a	

Figure 6: FH101RF RX Settings

Tx RF Settings TX Mode Band 868 MHz	RF Freq. ∽ 868.4 MHz ∨	Power OdBm V	Send Settings Send Rep.	Rep. Pause(ms)	- C ×
Telegram Settings Preamble Data Rate 1kbps V	Payload Data Rate Paylo 32kbps V 7da8	ad(hex)		Payload Length 2 bytes	Export data Export
Send Data			Send		

Figure 7: TX Settings

Network Test, Node 0x7da8 WakeUp

To wake up the sensor node, the FH101RF data receiver GUI implementation must be activated. This is done by pressing the Start RX button in FH101RF RX tab RX Control/ Start RX. To send the telegram, the send button must be pressed in the TX-kit. Once this has been done, the telegram received appears in the FH101RF RX tab. The node has awakened, as shown in Figure 11.

i i i i i i i i i i i i i i i i i i i	, currence	4.20 UA, p	ower: 14.1	1 uW							-3	
onfigurat	tions Brows	er RX										R
Deceived	Data and S	Statistics							Register	Name	Value (hex)	ì
	1120.001.500							RX Statistics	> 0x0	NFA433_SLOW	0x5	
Size	Data	Event	Band	Signal Qualit	10/ 3/250 DAD/26			Received Count	> 0x1	NFA433_FAST	0x0	
	ID	Ox1	868	87.1%/83.9%	15:55				> 0x2	NFA868_SLOW	0x5	
	ID ID	0x1 0x1	868 868	87.1%/87.1% 96.8%/93.5%	15:55	100 C 100		8	> 0x3	NFA868_FAST	OrdO	
	ID	Ox1	868	93.5%/93.5%	15:55			a da tarra	> 0x4	NFA2G4_SLOW	0x5	
	ID	Ox1	868	96.8%/93.5%	15:55			Quality A avg	> 0x5	NFA2G4_FAST	OxO	
	ID	Ox1	868	87.1%/87.1%	15:56			94.14%	> 0x6	CALIB_STATUS	OxO	
	ID	0x1	868	96.8%/93.5%	15:56				> 0x7	CALIB_CTRL	Ox8	
	ID	Ox1	868	93.5%/83.9%	15:56	805		Quality B avg	> 0x8	TSP_CTRL	0x5	
									> 0x9	N_SPG_TARGET	0x31	
								87.61%	> Oxa	SPG_FREQ	OxO	
									> 0xb	N_LCO_TARGET_433	Oxe	
								RX Control	> 0xc	N_LCO_TARGET_433	0x20	J
									> 0xd	N_LCO_TARGET_868	Oxed	
									> Oxe	N_LCO_TARGET_868	0x87	
									> 0xf	N_LCO_TARGET_2G4	0x12	
								Clear	> 0x10	N_LCO_TARGET_2G4	Oxce	
									> 0x11	LCO_RANGE_433	0x24	
									> 0x12	LCO_RANGE_868	0x35	
								Stop RX	> 0x13	LCO_RANGE_2G4	0x23	
								Stop RX	> 0x13 > 0x14	LCO_RANGE_2G4 LCO_FREQ_433	0x23 0xe	
								Stop RX	> 0x13 > 0x14 > 0x15	LCO_RANGE_2G4 LCO_FREQ_433 LCO_FREQ_433	0x23 0xe 0x24	
								Stop RX	> 0x13 > 0x14 > 0x15 > 0x16	LCO_RANGE_2G4 LCO_FREQ_433 LCO_FREQ_433 LCO_FREQ_868	0x23 0xe 0x24 0xd	
								Stop RX	> 0x13 > 0x14 > 0x15 > 0x16 > 0x17	LCO_RANGE_2G4 LCO_FREQ_433 LCO_FREQ_433 LCO_FREQ_868 LCO_FREQ_868	0x23 0xe 0x24 0xd 0x89	
Irq Settin	ngs							Stop RX	> 0x13 > 0x14 > 0x15 > 0x16 > 0x17 > 0x18	LCO_RANGE_2G4 LCO_FREQ_433 LCO_FREQ_433 LCO_FREQ_868 LCO_FREQ_868 LCO_FREQ_868 LCO_FREQ_2G4	0x23 0xe 0x24 0xd 0x89 0x12	
	ngs as Settings				Source	- FIFo Length	5	Stop RX	> 0x13 > 0x14 > 0x15 > 0x16 > 0x17 > 0x18 > 0x19	LCO_FREQ_433 LCO_FREQ_433 LCO_FREQ_433 LCO_FREQ_868 LCO_FREQ_868 LCO_FREQ_868 LCO_FREQ_264 LCO_FREQ_264	0x23 0xe 0x24 0xd 0x89 0x12 0xd3	
Addres	ss Settings	Mask				FiFo Length	MHZ Band 858 I		> 0x13 > 0x14 > 0x15 > 0x15 > 0x16 > 0x17 > 0x17 > 0x18 > 0x19 > 0x19	LCO_RANGE_2G4 LCO_FREQ_433 LCO_FREQ_433 LCO_FREQ_868 LCO_FREQ_868 LCO_FREQ_868 LCO_FREQ_264 LCO_FREQ_264 LCO_FREQ_264 COMPREF_W_433	0x23 0xe 0x24 0x89 0x12 0xc13 0xc13	
	ss Settings	Mask			CodeA/B	FIFo Length Band 2400 f	MHz Band 868 P		> 0x13 > 0x14 > 0x15 > 0x16 > 0x17 > 0x18 > 0x19 > 0x1a > 0x1b	LCO_RANGE_2G4 LCO_FREQ_433 LCO_FREQ_433 LCO_FREQ_868 LCO_FREQ_868 LCO_FREQ_264 LCO_FREQ_264 LCO_FREQ_264 COMPREF_W_433 COMPREF_W_868	0x23 0xe 0x24 0xd 0x89 0x12 0xd3 0x1e 0x1d	
Addres	ss Settings	Mask			CodeA/B		MHz Band 868 1		> 0x13 > 0x14 > 0x15 > 0x15 > 0x16 > 0x17 > 0x18 > 0x19 > 0x1a > 0x1b > 0x1b	LCO_RANGE_2G4 LCO_FREQ_433 LCO_FREQ_433 LCO_FREQ_868 LCO_FREQ_868 LCO_FREQ_264 LCO_FREQ_264 LCO_FREQ_264 COMPREF_W_268 COMPREF_W_264	0x23 0xe 0x24 0xd 0x89 0x12 0x13 0x1e 0x1d 0x1b	
Addres	nex)	Mask		>	CodeA/B				 > 0x13 > 0x14 > 0x15 > 0x16 > 0x17 > 0x18 > 0x18 > 0x19 > 0x1a > 0x1a > 0x1b > 0x1c > 0x1d 	LCO_RANGE_2G4 LCO_FREQ_433 LCO_FREQ_433 LCO_FREQ_868 LCO_FREQ_868 LCO_FREQ_264 LCO_FREQ_264 LCO_FREQ_264 COMPREF_W_433 COMPREF_W_264 COMPREF_W_433	0x23 0xe 0x24 0x89 0x12 0x12 0x12 0x14 0x1b 0x1b 0x24	
Addres	nex)			~	CodeA/B ID IDSLOW FIFo Length	Band 2400 f		NHz Band 433 MHz	 > 0x13 > 0x14 > 0x15 > 0x16 > 0x16 > 0x18 > 0x19 > 0x18 > 0x14 > 0x18 > 0x10 > 0x16 	LCO_RANGE_2G4 LCO_FREQ_433 LCO_FREQ_433 LCO_FREQ_868 LCO_FREQ_868 LCO_FREQ_2G4 LCO_FREQ_2G4 LCO_FREQ_2G4 COMPREF_W_433 COMPREF_W_2G4 COMPREF_M_433 COMPREF_M_868	0x23 0xe 0x24 0x89 0x12 0xd3 0x12 0x13 0x14 0x14 0x24	
Addres	nex)			~	CodeA/B	Band 2400 f		NHz Band 433 MHz	 > 0x13 > 0x14 > 0x15 > 0x16 > 0x16 > 0x17 > 0x18 > 0x19 > 0x1a > 0x1b > 0x1c > 0x1c > 0x1c > 0x1e > 0x1f 	LCO_RANGE_2G4 LCO_FREQ_433 LCO_FREQ_433 LCO_FREQ_868 LCO_FREQ_868 LCO_FREQ_264 LCO_FREQ_264 COMPREF_W_2664 COMPREF_W_2664 COMPREF_W_2664 COMPREF_M_868 COMPREF_M_868	0x23 0xe 0x24 0xd 0x89 0x12 0xd3 0x1e 0x1d 0x1b 0x24 0x24 0x24	
Addres	nex)			×	CodeA/B ID IDSLOW FIFo Length	Band 2400 f		NHz Band 433 MHz	 > 0x13 > 0x14 > 0x15 > 0x16 > 0x16 > 0x17 > 0x18 > 0x19 > 0x1a > 0x1a > 0x1c > 0x1d > 0x1d > 0x1f > 0x1f > 0x1f > 0x20 	LCO_RANGE_2G4 LCO_FREQ_433 LCO_FREQ_433 LCO_FREQ_868 LCO_FREQ_868 LCO_FREQ_264 LCO_FREQ_264 COMPREF_W_868 COMPREF_W_868 COMPREF_W_868 COMPREF_M_833 COMPREF_M_833 COMPREF_M_833	0x23 0xe 0x24 0xd 0x89 0x12 0x13 0x14 0x14 0x14 0x24 0x24 0x24 0x24	
Addres	as Settings hex) 8	ID			CodeA/B D DSLOW FFo Length FFo Overflow	Band 2400 f	∨ 24Bit	Nhtz Band 433 Mhtz	 > 0x13 > 0x14 > 0x15 > 0x16 > 0x17 > 0x18 > 0x18 > 0x19 > 0x1a > 0x1a > 0x1a > 0x1c > 0x21 	LCO_RANGE_2G4 LCO_FREQ_433 LCO_FREQ_433 LCO_FREQ_868 LCO_FREQ_868 LCO_FREQ_264 LCO_FREQ_264 LCO_FREQ_264 COMPREF_W_266 COMPREF_W_266 COMPREF_W_264 COMPREF_M_264 COMPREF_5_433 COMPREF_5_433	0x23 0xe 0x24 0x89 0x12 0x18 0x14 0x14 0x14 0x24 0x24 0x24 0x24 0x22	
Addres ID (h 7dal	as Settings hex) 8	ID		🗹 Ban	CodeA/8 D ID IDSLOW FFO Length FFO Overflow	Band 2400 f	 24 Bit Band 2400 Mb 	Nhtz Band 433 Mhtz	 > 0x13 > 0x14 > 0x15 > 0x16 > 0x17 > 0x18 > 0x19 > 0x1a > 0x2a 	LCO_RANGE_2G4 LCO_FREQ_433 LCO_FREQ_433 LCO_FREQ_868 LCO_FREQ_868 LCO_FREQ_264 LCO_FREQ_264 LCO_FREQ_264 COMPREF_W_868 COMPREF_W_868 COMPREF_W_868 COMPREF_M_868 COMPREF_M_868 COMPREF_S_868 COMPREF_S_868	0x23 0xe 0x24 0x89 0x12 0x12 0x12 0x14 0x14 0x14 0x24 0x24 0x24 0x24 0x24 0x26 0x1c 0x1c	
Addres ID (h 7dai	as Settings hex) 8	ID		🗹 Ban	CodeA/B D DSLOW FFo Length FFo Overflow	Band 2400 f	∨ 24Bit	Nhtz Band 433 Mhtz	 > 0x13 > 0x14 > 0x15 > 0x16 > 0x17 > 0x18 > 0x19 > 0x19 > 0x10 > 0x11 > 0x21 > 0x23 	LCO_RANGE_2G4 LCO_FREQ_433 LCO_FREQ_433 LCO_FREQ_868 LCO_FREQ_868 LCO_FREQ_2G4 LCO_FREQ_2G4 COMPREF_W_433 COMPREF_W_264 COMPREF_M_268 COMPREF_M_264 COMPREF_M_264 COMPREF_S_433 COMPREF_S_264 D_CORNER_CTRL	0x23 0xe 0x24 0x89 0x12 0x13 0x12 0x13 0x1e 0x14 0x24 0x24 0x24 0x24 0x24 0x24 0x1c 0x1c 0x1c 0x1c 0x1c 0x1c	
Addres ID (h 7dai Band 43 Rx Frequ	as Settings hex) 8	ID		🗹 Ban	CodeA/8 D ID IDSLOW FFO Length FFO Overflow	Band 2400 f	 24 Bit Band 2400 Mb 	Nhtz Band 433 Mhtz	 > 0x13 > 0x14 > 0x15 > 0x16 > 0x17 > 0x18 > 0x19 > 0x19 > 0x1a > 0x1b > 0x1b > 0x1c > 0x20 > 0x21 > 0x23 > 0x24 	LCO_RANGE_2G4 LCO_FREQ_433 LCO_FREQ_433 LCO_FREQ_868 LCO_FREQ_868 LCO_FREQ_264 LCO_FREQ_264 COMPREF_W_264 COMPREF_W_264 COMPREF_M_433 COMPREF_M_433 COMPREF_M_264 COMPREF_M_264 COMPREF_S_433 COMPREF_S_264 D_CONPREF_S_264 D_CONPREF_S_264	0x23 0xe 0x24 0xd 0x89 0x12 0xd3 0x1e 0x1d 0x1b 0x24 0x24 0x24 0x24 0x24 0x24 0x1c 0x1c 0x1c 0x1c 0x0 0x0 0x0 0x27	
ID (h 7dai) Band 43 Rx Frequ 434	ss Settings nex) 8 33 MHz Sett nency	ID		Banc Rx Fr 868	CodeA/8 JD IDSLOW FFo Length FFo Overflow i 868 MHz Settings equency	Band 2400 f	V 24 Bit Band 2400 M Rx Frequency 2485	Nhtz Band 433 Mhtz	 > 0x13 > 0x14 > 0x15 > 0x16 > 0x17 > 0x18 > 0x19 > 0x1a > 0x1b > 0x1c > 0x1c > 0x1c > 0x1d > 0x1c > 0x1c > 0x1c > 0x1c > 0x1d > 0x1d > 0x1d > 0x1d > 0x20 > 0x21 > 0x23 > 0x24 > 0x25 	LCO_RANGE_2G4 LCO_FREQ_433 LCO_FREQ_433 LCO_FREQ_868 LCO_FREQ_868 LCO_FREQ_264 LCO_FREQ_264 COMPREF_W_468 COMPREF_W_668 COMPREF_W_668 COMPREF_W_668 COMPREF_M_433 COMPREF_M_433 COMPREF_M_433 COMPREF_M_433 COMPREF_M_433 COMPREF_M_433 COMPREF_443 COMPREF_443 COMPREF_443 COMPREF_5_868 COMPREF_5_443 COMPREF_5_443 COMPREF_5_264 D_CORNER_CTRL	0x23 0xe 0x24 0x89 0x12 0x18 0x1e 0x1d 0x1b 0x24 0x24 0x24 0x24 0x24 0x1c 0x1c 0x1c 0x0 0x27 0x0	
Addres ID (h 7dai Band 43 Rx Frequ 434 Data Rate	ss Settings hex) 8 33 MHz Sett hency	ID		Banı Rx Fr 868 Data	CodeA/8 D IDSLOW FFFo Length FFFo Overflow 8 868 MHz Settings equency Rate	Band 2400 f	V 24 Bit Band 2400 M Rx Frequency 2485 Data Rate	Nhtz Band 433 Mhtz	 > 0x13 > 0x14 > 0x15 > 0x16 > 0x17 > 0x18 > 0x19 > 0x1a > 0x21 > 0x21 > 0x23 > 0x24 > 0x25 > 0x26 	LCO_RANGE_2G4 LCO_FREQ_433 LCO_FREQ_433 LCO_FREQ_868 LCO_FREQ_868 LCO_FREQ_264 LCO_FREQ_264 LCO_FREQ_264 COMPREF_W_264 COMPREF_W_264 COMPREF_M_264 COMPREF_M_264 COMPREF_S_433 COMPREF_S_433 COMPREF_S_433 COMPREF_S_433 COMPREF_S_434 COMPREF_S_434 COMPREF_S_434 COMPREF_S_435 COMPREF_S_435 COMPREF_S_435 COMPREF_S_435 COMPREF_S_435 COMPREF_S_435 COMPREF_S_435 COMPREF_S_435 COMPREF_S_435 COMPREF_S_435 COMPREF_S_435 COMPREF_S_5264 D_CORNER_CTRL BAND_BRANCH_CTRL IFAMP_GAIN_CTRL	0x23 0xe 0x24 0x89 0x12 0x18 0x1e 0x14 0x1b 0x24 0x24 0x24 0x24 0x24 0x24 0x24 0x24	
Addres ID (h 7dal] Band 43 Rx Frequ 434	ss Settings hex) 8 33 MHz Sett hency	ID		Banı Rx Fr 868 Data	CodeA/8 JD IDSLOW FFo Length FFo Overflow i 868 MHz Settings equency	Band 2400 f	V 24 Bit Band 2400 M Rx Frequency 2485	Nhtz Band 433 Mhtz	 > 0x13 > 0x14 > 0x15 > 0x16 > 0x17 > 0x18 > 0x19 > 0x18 > 0x19 > 0x16 > 0x12 > 0x21 > 0x22 > 0x24 > 0x25 > 0x26 > 0x27 	LCO_RANGE_2G4 LCO_FREQ_433 LCO_FREQ_433 LCO_FREQ_433 LCO_FREQ_868 LCO_FREQ_264 LCO_FREQ_264 LCO_FREQ_264 COMPREF_W_433 COMPREF_W_868 COMPREF_W_264 COMPREF_M_264 COMPREF_S_888 COMPREF_S_888 COMPREF_S_888 COMPREF_S_888 COMPREF_S_264 D_CORNER_CTRL BAND_BRANCH_CTRL TESTBUF_CTRL IFAMP_6AN_CTRL RX_ACTIVE_SELECT	0x23 0xe 0x24 0x89 0x12 0x13 0x1e 0x14 0x1b 0x24 0x24 0x24 0x24 0x24 0x1c 0x1c 0x1c 0x1c 0x1c 0x1c 0x2 0x2 0x2 0x2 0x2 0x2 0x2 0x2 0x2 0x2	
Addres ID (h 7dai Band 43 Rx Frequ 434 Data Rate	ss Settings nex) 8 8 33 MHz Sett iency xe	ID		☑ Banı Rx Fr 868 Data Pre	CodeA/8 D IDSLOW FFFo Length FFFo Overflow 8 868 MHz Settings equency Rate	Band 2400 f	V 24 Bit Band 2400 M Rx Frequency 2485 Data Rate	Nhtz Band 433 Mhtz	 > 0x13 > 0x14 > 0x15 > 0x16 > 0x18 > 0x19 > 0x19 > 0x19 > 0x14 > 0x10 > 0x14 > 0x14 > 0x16 > 0x14 > 0x16 > 0x20 > 0x21 > 0x22 > 0x23 > 0x24 > 0x25 > 0x26 > 0x27 > 0x28 	LCO_RANGE_2G4 LCO_FREQ_433 LCO_FREQ_433 LCO_FREQ_433 LCO_FREQ_868 LCO_FREQ_264 LCO_FREQ_264 LCO_FREQ_264 COMPREF_W_433 COMPREF_W_868 COMPREF_M_264 COMPREF_M_264 COMPREF_M_264 COMPREF_S_868 COMPREF_S	0x23 0xe 0x24 0x89 0x12 0x13 0x12 0x13 0x1e 0x14 0x24 0x14 0x24 0x24 0x24 0x24 0x24 0x1c 0x1c 0x1c 0x1c 0x1c 0x1c 0x0 0x27 0x0 0x28 0x3 0x3 0x10	
Address ID (h 7dai] Band 43 Rx Frequ 434 Data Rate Preamb 10241	ss Settings nex.) 8 33 MHz Sett nency te ble bps	ID	8	Ban Rx Fr 868 Data Pre	CodeA/8 D IDSLOW FFo Length FFo Overflow 3 868 MHz Settings equency Rate amble 24 bps	Band 2400 f	24 Bit 24 Bit Band 2400 M Rx Frequency 2486 Data Rate Preamble 1024 bps	Nhtz Band 433 Mhtz	 > 0x13 > 0x14 > 0x15 > 0x16 > 0x17 > 0x18 > 0x19 > 0x18 > 0x19 > 0x16 > 0x12 > 0x21 > 0x22 > 0x24 > 0x25 > 0x26 > 0x27 	LCO_RANGE_2G4 LCO_FREQ_433 LCO_FREQ_433 LCO_FREQ_433 LCO_FREQ_868 LCO_FREQ_264 LCO_FREQ_264 LCO_FREQ_264 COMPREF_W_433 COMPREF_W_868 COMPREF_W_264 COMPREF_M_264 COMPREF_S_888 COMPREF_S_888 COMPREF_S_888 COMPREF_S_888 COMPREF_S_264 D_CORNER_CTRL BAND_BRANCH_CTRL TESTBUF_CTRL IFAMP_6AN_CTRL RX_ACTIVE_SELECT	0x23 0xe 0x24 0x89 0x12 0x13 0x1e 0x14 0x1b 0x24 0x24 0x24 0x24 0x24 0x1c 0x1c 0x1c 0x1c 0x1c 0x1c 0x2 0x2 0x2 0x2 0x2 0x2 0x2 0x2 0x2 0x2	
Address ID (h 2dai) Band 43 Rx Frequ 434 Data Rate Preamb	ss Settings nex) 8 33 MHz Sett iency ke ble bps fload	ID		Ban Rx Fr 868 Data Pre 10	CodeA/8 D IDSLOW FFo Length FFo Overflow a68 MHz Settings equency Rate amble	Band 2400 f	V 24 Bit Band 2400 M Rx Frequency 2485 Data Rate Preamble	Nhtz Band 433 Mhtz	 > 0x13 > 0x14 > 0x15 > 0x16 > 0x18 > 0x18 > 0x19 > 0x19 > 0x1a > 0x1b > 0x1b > 0x1d > 0x1c > 0x1d > 0x1c > 0x1d > 0x1c > 0x1d > 0x1c > 0x1d > 0x1a > 0x12 > 0x20 > 0x21 > 0x23 > 0x24 > 0x26 > 0x26 > 0x26 > 0x27 > 0x28 	LCO_RANGE_2G4 LCO_FREQ_433 LCO_FREQ_433 LCO_FREQ_433 LCO_FREQ_868 LCO_FREQ_264 LCO_FREQ_264 LCO_FREQ_264 COMPREF_W_433 COMPREF_W_868 COMPREF_M_264 COMPREF_M_264 COMPREF_M_264 COMPREF_S_868 COMPREF_S_264 D_CORNER_CTRL BAND_BRANCH_CTRL TESTBUF_CTRL IFAMP_GAIN_CTRL RX_ACTIVE_SELECT CODE_SELECT	0x23 0xe 0x24 0x89 0x12 0x13 0x12 0x13 0x1e 0x14 0x24 0x14 0x24 0x24 0x24 0x24 0x24 0x1c 0x1c 0x1c 0x1c 0x1c 0x1c 0x0 0x27 0x0 0x28 0x3 0x3 0x10	

Figure 8: FH101RF received telegram

6.1.2 Use Case 2

In order to expand use case 1, a user-defined data "payload" should now also be transferred with the WakeUp telegram. This provides a low power option to transfer user data.

Requirements

In addition to a WakeUp of the sensor node, it should receive additional data, for example for parameterization and time control. We define the user data in this example to the 6-byte hex string: 0xaffe12345678.

Configuration, Sensor Node

To meet the requirement, the following additional settings must be set on the FH101RF in combination with the settings from the previous example:

In order to use the request for the addressed WakeUp, the IDSLOW interrupt check-box must be activated, all other interrupts are off. The corresponding checkbox can be found in the FH101RF RX tab under irq Settings / Source.

TX Configuration, Master Node

In the TX-Kit we add user data to the payload entry. The ID entry in the payload data field remains unaffected, it is only expanded by user data. It must match to 0x7da8affe12345678.

Network Test

To wake up the sensor node, the FH101RF data receiver GUI implementation must be activated. This is done by pressing the Start RX button in FH101RF RX tab RX Control/ Start RX. To send the telegram, the send button must be pressed in the TX-kit. Once this has been done, the telegram received appears in the FH101RF RX tab, see Figure **9**.

FH101RF, current: 4.2 uA, power: 13.										
nfigurations Browser RX						Register	Name	Value (he)	v)	^
Received Data and Statistics					a temperature t	> 0x0	NFA433 SLOW	0x5	9	
Size Data Event B	and Signal Qua	Lite A (D	Time		RX Statistics	> 0x1	NFA433 FAST	0x0		
0x6 affe12345678 0x20 8			16:26:00		Received Count	> 0x2	NFA868_SLOW	0x5		
0x6 affe12345678 0x20 8			16:26:01		9	> 0x3	NFA868_FAST	0x0		
0x6 affe12345678 0x20 8	96.8%/93.5	5%	16:26:02			> 0x4	NFA2G4_SLOW	0x5		
0x6 affe12345678 0x20 8			16:26:03		Quality A avg	> 0x5	NFA2G4_FAST	0x0		
0x6 affe12345678 0x20 8 0x6 affe12345678 0x20 8			16:26:04 16:26:05			> 0x6	CALIB_STATUS	0x0		
0x6 affe12345678 0x20 8			16:26:06		95.76%	> 0x7	CALIB_CTRL	0x8		
0x6 affe12345678 0x20 8			16:26:07			> 0x8	TSP_CTRL	0x5		
0x6 affe12345678 0x20 8	96.8%/93.5	5%	16:26:08		Quality B avg	> 0x9	N_SPG_TARGET	0x31		
					93.04%	> 0xa	SPG FREQ	0x0		
						> 0xb	N LCO TARGET 433	0xe		
					RX Control	> 0xc	N_LCO_TARGET_433	0x20		
					KA CONTO	> 0xd	N_LCO_TARGET_868	Oxd		
						> 0xe	N_LCO_TARGET_868	0x87		
						> Oxf	N_LCO_TARGET_2G4	0x12		
					Clear	> 0x10	N_LCO_TARGET_2G4	Oxce		
						> 0x11	LCO_RANGE_433	0x24		
								0x35		
						> 0x12	LCO RANGE 868			
						> 0x12 > 0x13	LCO_RANGE_868 LCO_RANGE_2G4	0x23		
					Stop RX		LCO_RANGE_2G4			
					Stop RX	> 0x13 > 0x14	LCO_RANGE_2G4 LCO_FREQ_433	0x23 0xe		
					Stop RX	> 0x13 > 0x14 > 0x15	LCO_RANGE_2G4 LCO_FREQ_433 LCO_FREQ_433	0x23		
					Stop RX	> 0x13 > 0x14	LCO_RANGE_2G4 LCO_FREQ_433 LCO_FREQ_433 LCO_FREQ_868	0x23 0xe 0x24		
					Stop RX	> 0x13 > 0x14 > 0x15 > 0x16	LCO_RANGE_2G4 LCO_FREQ_433 LCO_FREQ_433 LCO_FREQ_868 LCO_FREQ_868	0x23 0xe 0x24 0xd		
rq Settings					Stop RX	> 0x13 > 0x14 > 0x15 > 0x16 > 0x17	LCO_RANGE_2G4 LCO_FREQ_433 LCO_FREQ_433 LCO_FREQ_868	0x23 0xe 0x24 0xd 0x89		
rq Settings Address Settings		Source	FiFo Length		Stop RX	> 0x13 > 0x14 > 0x15 > 0x16 > 0x17 > 0x18	LCO_RANGE_2G4 LCO_FREQ_433 LCO_FREQ_433 LCO_FREQ_868 LCO_FREQ_868 LCO_FREQ_868 LCO_FREQ_2G4	0x23 0xe 0x24 0xd 0x89 0x12		
		Source	FiFo Length Band 2400 l	VHz - Band 868 M		> 0x13 > 0x14 > 0x15 > 0x16 > 0x17 > 0x18 > 0x19	LCO_RANGE_2G4 LCO_FREQ_433 LCO_FREQ_433 LCO_FREQ_868 LCO_FREQ_868 LCO_FREQ_868 LCO_FREQ_2G4 LCO_FREQ_2G4	0x23 0xe 0x24 0xd 0x89 0x12 0xd3		
Address Settings				VHz Band 868 M		> 0x13 > 0x14 > 0x15 > 0x16 > 0x17 > 0x18 > 0x18 > 0x19 > 0x1a	LCO_RANGE_2G4 LCO_FREQ_433 LCO_FREQ_433 LCO_FREQ_868 LCO_FREQ_868 LCO_FREQ_264 LCO_FREQ_264 LCO_FREQ_264 COMPREF_W_433	0x23 0xe 0x24 0xd 0x89 0x12 0xd3 0x1e		
Address Settings ID (hex) Mask		CodeA/B	Band 2400 I		Hz Band 433 MHz	> 0x13 > 0x14 > 0x15 > 0x16 > 0x17 > 0x18 > 0x19 > 0x1a > 0x1b	LCO_RANGE_2G4 LCO_FREQ_433 LCO_FREQ_433 LCO_FREQ_868 LCO_FREQ_868 LCO_FREQ_264 LCO_FREQ_264 COMPREF_W_433 COMPREF_W_868	0x23 0xe 0x24 0xd 0x89 0x12 0xd3 0x1e 0x1d		
Address Settings	~	CodeA/B	Band 2400 I			> 0x13 > 0x14 > 0x15 > 0x16 > 0x17 > 0x18 > 0x19 > 0x1a > 0x1b > 0x1c	LCO_RANGE_2G4 LCO_FREQ_433 LCO_FREQ_433 LCO_FREQ_868 LCO_FREQ_868 LCO_FREQ_264 LCO_FREQ_264 COMPREF_W_433 COMPREF_W_868 COMPREF_W_264	0x23 0xe 0x24 0xd 0x89 0x12 0x12 0x1e 0x1e 0x1d		
Address Settings ID (hex) Mask	~	CodeA/B	Band 2400 I		Hz Band 433 MHz	 > 0x13 > 0x14 > 0x15 > 0x16 > 0x17 > 0x18 > 0x19 > 0x1a > 0x1b > 0x1c > 0x1d 	LCO_RANGE_2G4 LCO_FREQ_433 LCO_FREQ_433 LCO_FREQ_868 LCO_FREQ_868 LCO_FREQ_264 LCO_FREQ_264 LCO_FREQ_264 COMPREF_W_433 COMPREF_W_868 COMPREF_W_264 COMPREF_W_264	0x23 0xe 0x24 0x89 0x12 0x13 0x1e 0x1d 0x1b 0x24		
Address Settings ID (hex) Mask	~	CodeA/B	Band 2400 I		Hz Band 433 MHz	 > 0x13 > 0x14 > 0x15 > 0x16 > 0x16 > 0x17 > 0x18 > 0x18 > 0x18 > 0x14 > 0x1a > 0x1c > 0x1e 	LCO_RANGE_2G4 LCO_FREQ_433 LCO_FREQ_433 LCO_FREQ_868 LCO_FREQ_868 LCO_FREQ_264 LCO_FREQ_264 COMPREF_W_433 COMPREF_W_433 COMPREF_W_264 COMPREF_M_433 COMPREF_M_433	0x23 0xe 0x24 0xd 0x89 0x12 0xd3 0x1e 0x1d 0x1b 0x24 0x24		
Address Settings	~	CodeA/B	Band 2400 I		Hz Band 433 MHz	 > 0x13 > 0x14 > 0x15 > 0x16 > 0x17 > 0x18 > 0x18 > 0x19 > 0x1a > 0x1b > 0x1c > 0x1d > 0x1e > 0x1f 	LCO_RANGE_2G4 LCO_FREQ_433 LCO_FREQ_433 LCO_FREQ_868 LCO_FREQ_868 LCO_FREQ_264 LCO_FREQ_264 COMPREF_W_868 COMPREF_W_868 COMPREF_W_868 COMPREF_M_868 COMPREF_M_868	0x23 0xe 0x24 0xd 0x89 0x12 0xd3 0x1e 0x1d 0x1b 0x24 0x24		
Address Settings ID (hex) Address Settings ID (hex) ID	~	CodeA/B	Band 2400 I		Hz Band 433 MHz	 > 0x13 > 0x14 > 0x15 > 0x16 > 0x17 > 0x18 > 0x19 > 0x18 > 0x14 > 0x1a > 0x1a > 0x1c 	LCO_RANGE_2G4 LCO_FREQ_433 LCO_FREQ_433 LCO_FREQ_868 LCO_FREQ_868 LCO_FREQ_264 LCO_FREQ_264 COMPREF_W_483 COMPREF_W_264 COMPREF_M_483 COMPREF_M_483 COMPREF_M_264 COMPREF_M_264	0x23 0xe 0x24 0x89 0x12 0x13 0x16 0x1d 0x1b 0x24 0x24 0x24 0x1c		
Address Settings ID (hex) Mask Zda8 ID Band 433 MHz Settings	~	CodeA/B ID ID IDSLOW FIFo Length FIFo Overflow	Band 2400 I	✓ 24 Bit	Hz Band 433 MHz	 >>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>>	LCO_RANGE_2G4 LCO_FREQ_433 LCO_FREQ_433 LCO_FREQ_868 LCO_FREQ_868 LCO_FREQ_264 LCO_FREQ_264 LCO_FREQ_264 COMPREF_W_264 COMPREF_W_264 COMPREF_M_264 COMPREF_M_264 COMPREF_S_433 COMPREF_S_433	0x23 0xe 0x24 0xd 0x89 0x12 0x12 0x14 0x16 0x24 0x24 0x24 0x24 0x1c 0x1c		
Address Settings ID (nex) Address Settings ID Band 433 MHz Settings Xx Frequency	✓ Band 8 Rx Freq	CodeA/B ID ID IDSLOW FIFo Length FIFo Overflow	Band 2400 I	Z4 Bit Band 2400 MH Rx Frequency	Hz Band 433 MHz	 > 0x13 > 0x14 > 0x15 > 0x16 > 0x17 > 0x18 > 0x17 > 0x18 > 0x19 > 0x1a > 0x1b > 0x1c > 0x1c > 0x1d > 0x1d > 0x1f > 0x1f > 0x1f > 0x1d > 0x1f > 0x12 > 0x21 > 0x22 	LCO_RANGE_2G4 LCO_FREQ_433 LCO_FREQ_433 LCO_FREQ_868 LCO_FREQ_868 LCO_FREQ_2G4 LCO_FREQ_2G4 COMPREF_W_433 COMPREF_W_868 COMPREF_W_2G4 COMPREF_M_868 COMPREF_M_868 COMPREF_M_264 COMPREF_M_868 COMPREF_S_868 COMPREF_S_868	0x23 0xe 0x24 0xd 0x89 0x12 0xd3 0x1e 0x1d 0x1b 0x24 0x24 0x24 0x24 0x24 0x24 0x24 0x24		
Address Settings	V Band 8 Rx Freq 868	CodeA/B ID IDSLOW FIFo Length FIFo Overflow	Band 2400 I	 24 Bit Band 2400 MH Rx Frequency 2486 	Hz Band 433 MHz	 > 0x13 > 0x14 > 0x16 > 0x16 > 0x17 > 0x18 > 0x18 > 0x19 > 0x1a > 0x1b > 0x1c > 0x1c > 0x1d > 0x1c > 0x21 > 0x23 	LCO_RANGE_2G4 LCO_FREQ_433 LCO_FREQ_433 LCO_FREQ_868 LCO_FREQ_868 LCO_FREQ_264 LCO_FREQ_264 COMPREF_W_868 COMPREF_W_868 COMPREF_W_264 COMPREF_M_264 COMPREF_M_264 COMPREF_S_433 COMPREF_S_264 D_COMPREF_S_264	0x23 0xe 0x24 0xd 0x89 0x12 0x13 0x1e 0x1d 0x1b 0x24 0x24 0x24 0x24 0x24 0x24 0x21 0x1c 0x1c 0x0		
Address Settings ID (hex) Mask Zda8 ID Band 433 MHz Settings Ix Frequency 434 IX	✓ Band 8 Rx Freq	CodeA/B ID IDSLOW FIFo Length FIFo Overflow	Band 2400 I	Z4 Bit Band 2400 MH Rx Frequency	Hz Band 433 MHz	 > 0x13 > 0x14 > 0x15 > 0x16 > 0x17 > 0x18 > 0x18 > 0x19 > 0x18 > 0x10 > 0x11 > 0x11 > 0x12 > 0x16 > 0x21 > 0x21 > 0x23 > 0x24 	LCO_RANGE_2G4 LCO_FREQ_433 LCO_FREQ_433 LCO_FREQ_868 LCO_FREQ_868 LCO_FREQ_264 COMPREF_W_868 COMPREF_W_868 COMPREF_W_264 COMPREF_M_433 COMPREF_M_264 COMPREF_M_264 COMPREF_S_433 COMPREF_S_868 COMPREF_S_264 D_CONNPRE_TS_264 D_CONNPRE_TS_264 D_CONNPRE_TS_264	0x23 0xe 0x24 0xd 0x89 0x12 0x14 0x16 0x16 0x1b 0x24 0x24 0x24 0x24 0x1c 0x1c 0x1c 0x1c 0x1c 0x27		
Address Settings ID (nex) Address Settings ID Band 433 MHz Settings Ex Frequency 434	V Band 8 Rx Freq 868	CodeA/B ID IDSLOW FIFO Length FIFO Overflow	Band 2400 I	 24 Bit Band 2400 MH Rx Frequency 2486 	Hz Band 433 MHz	 > 0x13 > 0x14 > 0x15 > 0x16 > 0x16 > 0x17 > 0x18 > 0x19 > 0x10 > 0x10 > 0x11 > 0x11 > 0x12 > 0x12 > 0x12 > 0x21 > 0x23 > 0x24 > 0x25 	LCO_RANGE_2G4 LCO_FREQ_433 LCO_FREQ_433 LCO_FREQ_868 LCO_FREQ_868 LCO_FREQ_264 LCO_FREQ_264 COMPREF_W_433 COMPREF_W_433 COMPREF_M_264 COMPREF_M_433 COMPREF_M_264 COMPREF_M_264 COMPREF_S_433 COMPREF_S_688 COMPREF_S_688 COMPREF_S_264 D_CORNER_CTRL BAND_BRANCH_CTRL	0x23 0xe 0x24 0xd 0x89 0x12 0x13 0x1e 0x1d 0x1d 0x24 0x24 0x24 0x24 0x24 0x24 0x24 0x1c 0x1c 0x1c 0x1c 0x0 0x27 0x0		
D (hex) Mask 7da8 D Band 433 MHz Settings Rx Frequency 434 Data Rate	V Band 8 Rx Freq 668 Data Ra	CodeA/8 DD	Band 2400 I	V 24 Bit Band 2400 MH Rx Frequency 2486 Data Rate	Hz Band 433 MHz	 > 0x13 > 0x14 > 0x15 > 0x16 > 0x17 > 0x18 > 0x19 > 0x18 > 0x110 > 0x20 > 0x21 > 0x21 > 0x22 > 0x23 > 0x24 > 0x25 > 0x26 > 0x27 > 0x28 	LCO_RANGE_2G4 LCO_FREQ_433 LCO_FREQ_433 LCO_FREQ_868 LCO_FREQ_868 LCO_FREQ_264 COMPREF_W_868 COMPREF_W_868 COMPREF_W_868 COMPREF_M_433 COMPREF_M_2G4 COMPREF_M_264 COMPREF_S_433 COMPREF_S_868 COMPREF	0x23 0xe 0x24 0xd 0x89 0x12 0x14 0x14 0x1b 0x24 0x24 0x24 0x24 0x24 0x1c 0x1c 0x1c 0x1c 0x1c 0x27 0x0 0x27 0x0 0x23 0x3 0x10		
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Figure 9: FH101RF received telegram + user data

Evaluating FH101RF

PC Requirements

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7 PC Requirements

The following PC requirements must be met::

- OS: Windows 10
- USB Interface: min. USB 1.1
- D2XX Driver: https://ftdichip.com/

8 Open Source Software and Licence

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List of Open Source Software

Component	OSS Module Name	Module Version	License
Qt5Core.dll	Qt5Core	5.12.2	LGPL, Vers. 3
Qt5GUI.dll	Qt5Gui	5.12.2	LGPL, Vers. 3
Qt5Svg.dll	Qt5Svg	5.12.2	LGPL, Vers. 3
Qt5Widgets.dll	Qt5Widgets	5.12.2	LGPL, Vers. 3

Appendix Licence text

10 Appendix Licence text

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Version 3, 29 June 2007

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