CONTACT INFORMATION

- Website: www.supermicro.com
- General Information: marketing@supermicro.com
- Technical Support: support@supermicro.com
 Phone: +1 (408) 503-8000, Fax: +1 (408) 503-8008
- DRIVERS/IMAGES/USER'S MANUAL FROM THE LINKS BELOW:

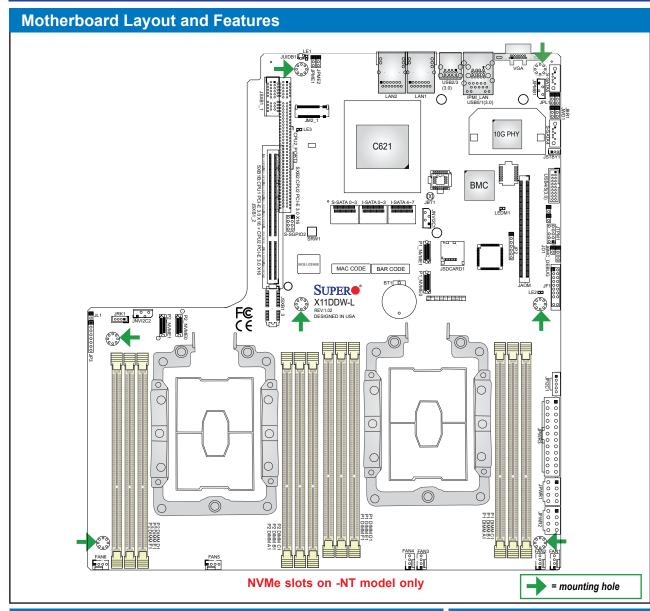
 - Manuals: http://www.supermicro.com/support/manuals
 Drivers & Utilities: http://www.supermicro.com/wftp
 Safety: http://www.supermicro.com/about/policies/safety_information.cfm

FOR YOUR SYSTEM TO WORK PROPERLY, PLEASE DOWNLOAD APPROPRIATE

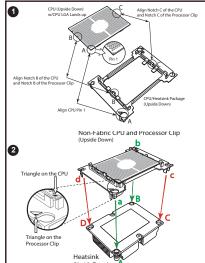
PACKAGE CONTENTS

- One (1) Supermicro Motherboard
- Two (2) SATA CablesOne (1) Quick Reference Guide

WARNING: This product can expose you to chemicals including lead, known to the State of California to cause cancer and birth defects or other reproductive harm. For more information, go to www.P65Warnings.ca.gov.



CPU/Heatsink Installation Installing Processor/Heatsink Module



Notes: 1.Please refer to Chapter 2 of the user's manual fo detailed instructions of CPU/Heatsink and memory Installation. 2. Graphics shown in this quick reference guide are for illustration only. Your components may or may not look exactly the same as drawings shown in this guide.

Front Panel Control (JF1)

	1	2	-
Power	0	0	Ground
Reset	0	0	Ground
3.3V	0	0	Power Fail LED
UID LED	0	0	OH/Fan Fail LED
3.3V Stby	0	0	NIC2 Active LED
3.3V Stby	0	0	NIC1 Active LED
3.3V Stby	0	0	HDD LED
3.3V	0	0	PWR LED
x	0	0) x
NMI	0	0	Ground
	19	20	

Jumpers, Connectors and LED Indicators

	01105 5	Jumpers
JBT1	CMOS Clear	Open (Normal)
JPL1	LAN1/LAN2 Enable	· /
JPME1	ME Recovery	Pins 1-2 (Normal)
JPME2	Manufacturing Mod	e (ME) Select Pins 1-2 (Normal)
JWD1	Watch Dog Timer E	
Battery (BT1) Onboard C	Connectors MOS battery
FAN1~6		oling fan headers
IPMI LAN	•	PMI LAN port
JAOM		x16 SAS3 AOM controller slot
JD1		zzer header (optional) (Note1)
JF1	•	ol panel header
JIPMB1		nal BMC I ² C header (for an IPMI card)
JL1		rusion header (Note 2)
JM2 1	PCIe M.2 fi	
JIVIZ_I		oni POn Bus (I²C) headers used for PCI-E hot-plug SMBus clock
JNVI ² C1/JN\	/I ² C2	nections (Note 3)
JPI ² C1		ply SMBbus I ² C header
JPWR1/JPW		power supply connectors
JPWR3	- 1 1	main power supply connector
JRK1	•	RAID Key for NVMe SSD
JSDCARD1	Micro SD C	•
JSTBY1		wer header
JTPM1		nnector for Trusted Platform Module (TPM)
JUIDB1		er (UID) switch
LAN1/LAN2		N (GLAN) ethernet ports on the back panel
P1 NVME0/	-	
_	` ,	I Express PCI-E 3.0 x4 ports (Note 4)
P2_NVME0/	, ,	1 Express PCI-E 3.0 x4 ports (Note 4)
(I-)SATA0~3		connectors supported by the PCH
(S-)SATA0~3		connectors supported by the SCU
(S-)SATA4/S	-SATA5 Powered S	-SATA connectors w/SuperDOM support
SXB1	PCI-E 3.0 (x16 + x16) Left Riser Card slot
SXB2	PCI-E 3.0 >	t16 Right Riser Card supported by CPU2
S-SGPIO2	Serial Gene	eral Purpose I/O port
USB0/1	Back panel	USB 3.0 ports
USB2/3	Back panel	USB 3.0 ports
USB4/5	USB 3.0 he	eaders
VGA	Back panel	•
LE1	UID (Unit Identifier) L	LED Indicators ED Solid Blue: Unit Identified
LE2	Onboard Power LED	
	M.2 LED	Blinking Green: Device Working

/Note: 1.This feature is available when an external speaker/buzzer is used. 2. Please connect a cable from the Chassis Intrusion header at JL1 to the chassis to receive an alert via IPMI. 3. An SMCI-proprietary NVMe add-on card and cable are required; available for a Supermicro complete system only. 4. When installing an NVMe device on a motherboard, please be sure to connect the first NVMe port (P1_NVME0) first for your system to work properly.

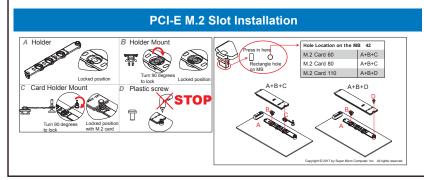
CPU Support

This motherboard supports dual Intel Xeon Scalable-SP or 2nd Gen Intel Xeon Scalable-SP series processors (Socket P); each processor supports dual full-width Intel QuickPath Interconnect (QPI) links of up to 10.4GT/s one direction per QPI.

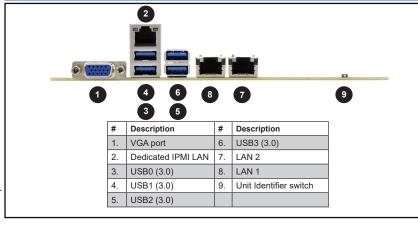
Memory Support

This motherboard supports up to 3TB of 3DS LRDIMM, LRDIMM, 3DS RDIMM, RDIMM, NV-DIMM DDR4 (288-pin) ECC 2933/2666/2400/2133 MHz memory modules in 12 slots. (Notes: 1. Up to 4TB is supported with (L)RDIMM and DCPMM populated in a balanced memory configuration. 2. 2933 MHz memory is supported b 2nd Gen Intel Xeon Scalable-SP(82xx/62xx) series processors only. 3. Unbalanced memory configuration decreases memory performance and is not recommended.)

Memory Population Table			
When 1 CPU is used:	Memory Population Sequence		
1 CPU & 1 DIMM	CPU1: P1-DIMMA1		
1 CPU & 2 DIMMs	CPU1: P1-DIMMA1/P1-DIMMD1		
1 CPU & 3 DIMMs	CPU1: P1-DIMMC1/P1-DIMMB1/P1-DIMMA1		
1 CPU & 4 DIMMs	CPU1: P1-DIMMB1/P1-DIMMA1/P1-DIMMD1/P1-DIMME1		
1 CPU & 5 DIMMs (Unbalanced: not recomended)	CPU1: P1-DIMMC1/P1-DIMMB1/P1-DIMMA1/P1-DIMMD1/P1-DIMME1		
1 CPU & 6 DIMM	CPU1: P1-DIMMC1/P1-DIMMB1/P1-DIMMA1/P1-DIMMD1/P1-DIMME1/P1-DIMMF1		
When 2 CPUs are used:	Memory Population Sequence		
2 CPUs & 2 DIMMs	CPU1: P1-DIMMA1 CPU2: P2-DIMMA1		
2 CPUs & 4 DIMMs	CPU1: P1-DIMMA1/P1-DIMMD1 CPU2: P2-DIMMA1/P2-DIMMD1		
2 CPUs & 6 DIMMs	CPU1: P1-DIMMC1/P1-DIMMB1/P1-DIMMA1 CPU2: P2-DIMMC1/P2-DIMMB1/P2-DIMMA1		
2 CPUs & 8 DIMMs	CPU1: P1-DIMMB1/P1-DIMMA1/P1-DIMMD1/P1-DIMME1 CPU2: P2-DIMMB1/P2-DIMMA1/P2-DIMMD1/P2-DIMME1		
2 CPUs & 10 DIMMs	CPU1: P1-DIMMC1/P1-DIMMB1/P1-DIMMA1/P1-DIMMD1/P1-DIMME1/P1-DIMMF1 CPU2: P2-DIMMB1/P2-DIMMA1/P2-DIMMD1/P2-DIMME1		
2 CPUs & 12 DIMMs	CPU1: P1-DIMMC1/P1-DIMMB1/P1-DIMMA1/P1-DIMMD1/P1-DIMME1/P1-DIMMF1 CPU2: P2-DIMMC1/P2-DIMMB1/P2-DIMMA1/P2-DIMMD1/P2-DIMME1/P2-DIMMF1		







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