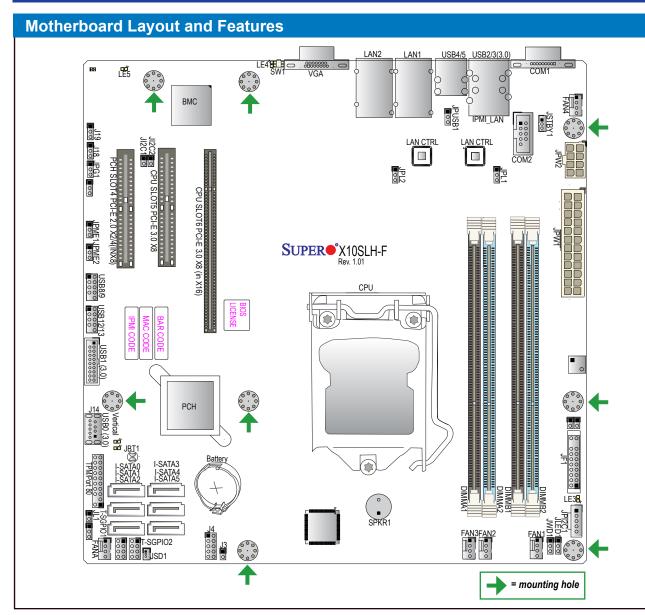
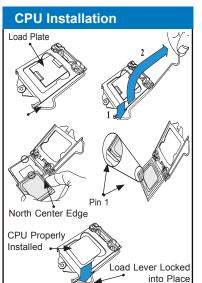
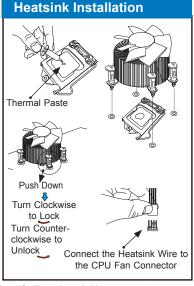
MNL-1464-QRG-10c

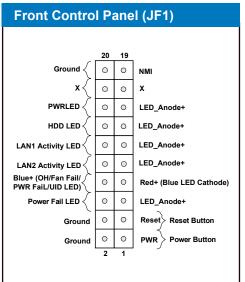
in whole is strictly prohibited without Supermicro's written rate at the time of printing; however, it is not guaranteed.

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## **Jumpers, Connectors and LED Indicators** Jumper Description Default JBT1 CMOS Clear Short: Clear CMOS Open: Normal Operation (Default) SMB to PCI Slots JI2C1/JI2C2 Off (Disabled) VGA Enable JPG1 Pins 1-2 (Enabled) JPL1/JPL2 LAN1/LAN2 Enable Pins 1-2 (Enabled) ME Recovery JPME1 Pins 1-2 (Normal) JPME2 Manufacturing Mode Pins 1-2 (Normal) JPUSB1 USB4/5 Wake Up Enable Pins 1-2 (Enabled) JWD1 Watch Dog Enable Pins (1-2) (Reset) Connectors Connector Description Battery Onboard Battery COM1/COM2 COM1: Port, COM2: Header

l	FAN1-FAN4, FANA	System/CPU Fan Headers	
١	J14	Vertical USB 1 (3.0) Connector	
	JF1	Front Control Panel Header	
	JL1	Chassis Intrusion Header	
	JLED1	Power LED Indicator Header	
	JPI <sup>2</sup> C1	Power System Management Bus (Power SMB)	
	JPW1	24-pin ATX Main Power Connector (Required)	
	JPW2	+12V 8-pin CPU power Connector (Required)	
	JSD1	SATA DOM (Device On Module) Power Connector	
	JSTBY1	Standby Power Header	
	JTPM1	Trusted Platform Module/Port 80 Connector	
	LAN1/LAN2	Gigabit (RJ45) Ports	
	SPKR1	Internal Speaker/Buzzer	
	I-SATA0-3	(Intel PCH) Serial ATA (SATA 3.0) (6Gb/sec)	
	I-SATA4/5	(Intel PCH) Serial ATA (SATA 3.0) (6Gb/sec)	
	(CPU) Slot 6	PCI-Express 3.0 x8 in x16 Slot	
l	(CPU) Slot 5	PCI-Express 3.0 x8 Slot	
l	(PCH) Slot 4	PCI-Express 2.0 x4 in x8 Slot	
l	SW1	Unit Identifier (UID) Switch	
	T-SGPIO 1/2	Serial Link General Purpose I/O Headers	
	USB 0	USB 3.0 Port Type A	
	USB 1	USB 3.0 Header	
	USB 2/3	Back panel USB 3.0 Ports	
	USB 4/5	Back panel USB 2.0 Ports	
	USB 8/9, USB 12/13	Front Panel Accessible USB 2.0 Headers	
	VGA	Back panel VGA Port	

LED Indicators					
LED	Description	Color/State	Status		
LE3	Onboard Standy PWR LED	Green: Solid On	Power On		
LE4	Unit Indetifier (UID) LED	Blue: On	Unit Identified		
LE5	BMC Heartbeat LED	Green: Blinking BMC Normal			

### / Note: Refer to Chapter 1 of the User Manual for detailed information on jumpers, connectors, and LED indicators.

# **Memory Support**

The X10SLH-F supports up to 32GB of Unbuffered (UDIMM) DDR3 ECC 1600/1333 MT/s (1.35V/1.5V) memory in four slots.

/ Note: Use memory modules of the same type and speed. Do not mix

### **DIMM Memory Installation**

### Memory Population Guidelines

	Unbuffered (UDIMM) DDR3 ECC Memory					
DIMM Slots per Channel	DIMMs Populat- ed per Channel	DIMM Type	POR Speeds	Ranks per DIMM (any combination)		
2	1	Unbuffered DDR3	1333, 1600	Single Rank, Dual Rank		
2	2	Unbuffered DDR3	1333, 1600	Single Rank, Dual Rank		

Populating these DIMM modules with a pair of memory modules of the same type and size will result in interleaved memory, which will improve memory performance.

