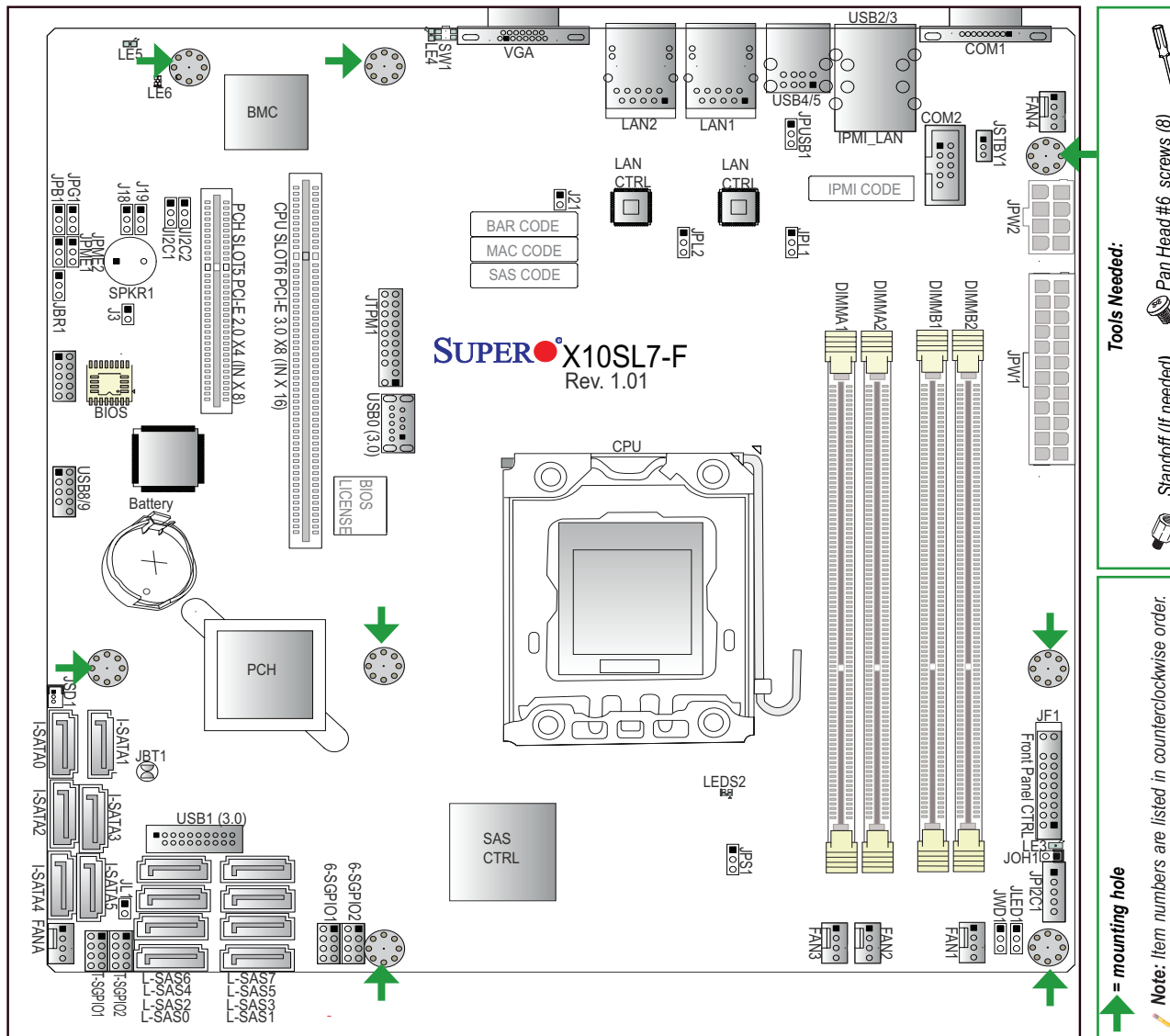


**Motherboard Layout and Features**



**Jumpers, Connectors and LED Indicators**

Jumpers		
Jumper	Description	Default
JBT1	CMOS Clear	(See Chpt. 2)
JPC1/JPC2	SMB to PCI-E Slots	Pins 2-3 (Disabled)
JPG1	VGA Enable	Pins 1-2 (Enabled)
JPL1/JPL2	LAN1/LAN2 Enable	Pins 1-2 (Enabled)
JPME1	ME Recovery	Pins 1-2 (Normal)
JPME2	Manufacture Mode Select	Pins 1-2 (Normal)
JPS1	SAS Enable	Pins 1-2 (Enabled)
JUSB1	USB 4/5 Wake_up Enable	Pins 1-2 (Enabled)
JWD1	Watch Dog Enable	Pins 1-2 (Reset)

Connectors	
Connector	Description
Battery	Onboard Battery
COM1/COM2	COM1 (Port)/COM2 (Header)
Fan1 - Fan4, FanA	System/CPU Fan Headers
JF1	Front Panel Control Header
JL1	Chassis Intrusion Header
JLED1	Power LED Indicator Header
JPC1	Power SMB (System Management Bus)
JPW1	24-pin ATX Main Power Connector (Required)
JPW2	+12V 8-pin CPU power Connector (Required)
JSD1	SATA DOM (Device_On_Module) Power Connector
JSTBY1	Standby Power Header
JTPM1	Trusted Platform Module/Port 80 Connector
LAN1/LAN2	Gigabit (RJ45) Ports (LAN1/2)
IPMI_LAN	IPMI_Dedicated LAN
I-SATA0/I-SATA1	(Intel PCH) Serial ATA (SATA 3.0) Ports 0/1 (6Gb/sec)
I-SATA 2 - I-SATA5	(Intel PCH) Serial ATA (SATA 2.0) Ports 2~5 (3Gb/sec)
L-SAS 0-7	SAS Connectors 0-7 (supported by the LSI 2308 SAS controller)
(PCH) Slot 5	PCI-Express 2.0 x4 in x8 Slot
(CPU) Slot 6	PCI-Express 3.0 x8 in x16 Slot
SPKR1	Internal Speaker/Buzzer
SW1	UID (Unit Identifier) Switch
6-SGPIO 1/2	Serial_Link General Purpose I/O Headers 1/2 for SAS Connections
T-SGPIO 1/2	Serial_Link General Purpose I/O Headers 1/2 for SATA Connections
USB 2/3, 4/5	Backpanel USB 2.0 Ports 2/3, 4/5
USB 0,1 (3.0)	USB 3.0 Ports 0, 1 (USB 0: Type A Connector)
USB 8/9	Front Panel Accessible USB 2.0 Headers 8/9
VGA	Backpanel VGA Port

LED Indicators			
LED	Description	Color/State	Status
LE3	Standby Power LED	On: Power On	
LE4	UID LED	Blue (On): Unit Identified	
LE5	BMC Heartbeat LED	Green (Blinking): BMC Normal	
LE6	Power Status LED	Red: PWR Fail/Yellow: Standby PWR Active/Green: PWR On Normal	
LEDS2	SAS LED	Green (Blinking): SAS Active/Red (Solid On): SAS Error	

**Memory Support**

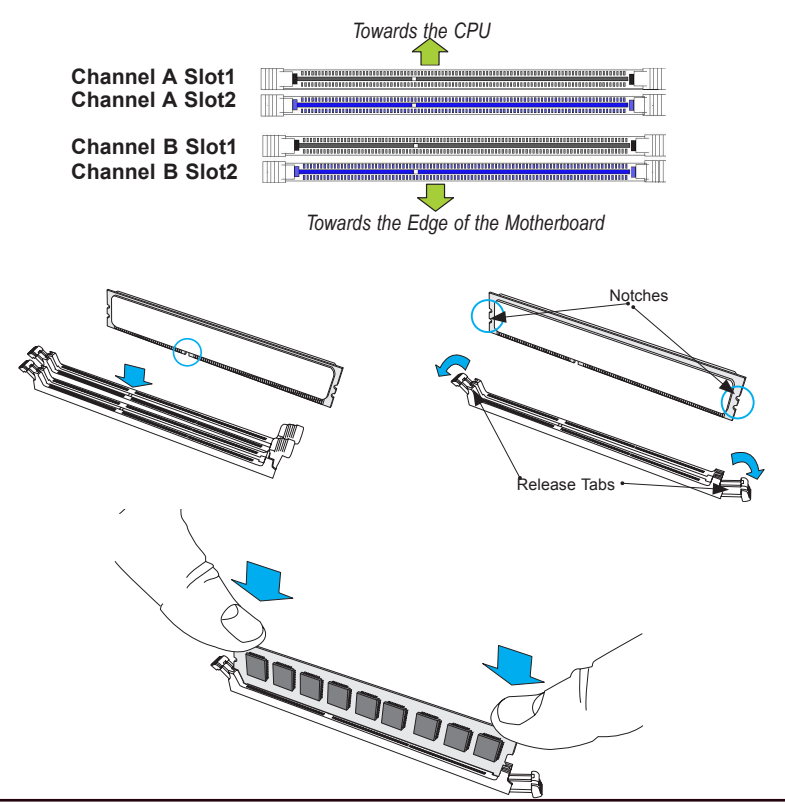
The X10SL7-F supports up to 32GB of Unbuffered (UDIMM) DDR3 ECC 1600/1333 MHz in 4 memory slots. Populating these DIMM modules with a pair of memory modules of the same type and same size will result in interleaved memory, which will improve memory performance.

**Memory Population Guidelines**

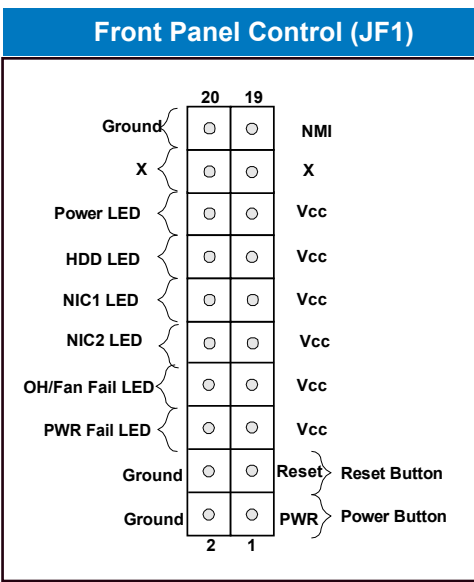
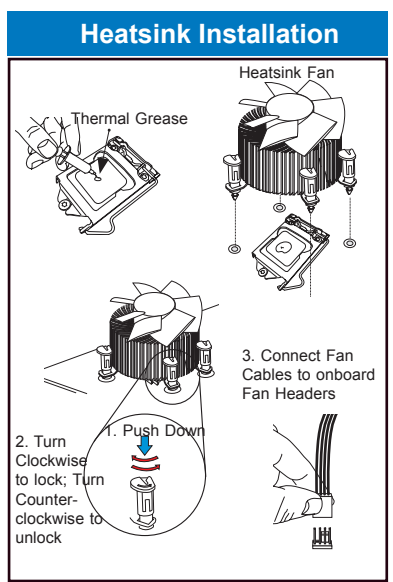
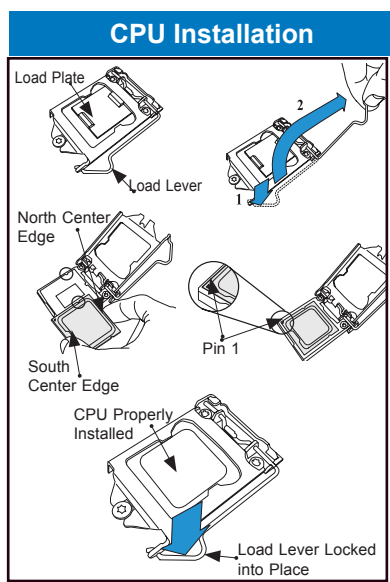
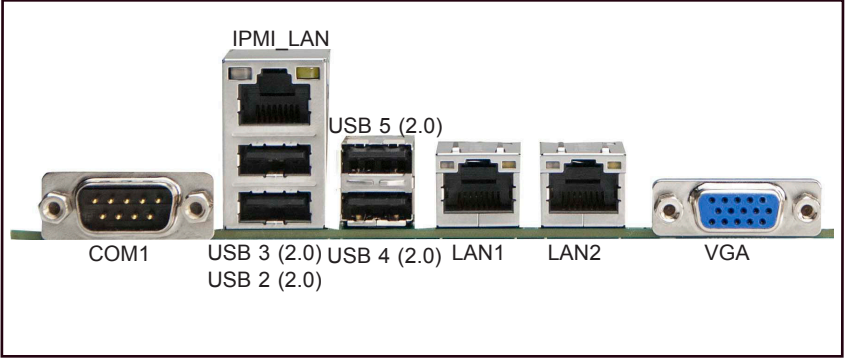
Please follow the table below when populating the X10SL7-F.

DDR3 Unbuffered ECC (UDIMM) Memory				
DIMM Slots per Channel	DIMMs Populated per Channel	DIMM Type	POR Speeds	Ranks per DIMM (any combination)
2	1	Unbuffered DDR3	1333, 1600	Single Rank, Dual Rank
2	2	Unbuffered DDR3	1333, 1600	Single Rank, Dual Rank

**DIMM Memory Installation**



**Back Panel I/O Connectors**



**Note:** Graphics shown in this quick reference guide are for illustration only. Your components may or may not look exactly the same as drawings shown in this guide.

**Note:** Refer to Chapter 1 of the User Manual for detailed information on jumpers, connectors, and LED indicators.

**Note:** Refer to Chapter 2 of the User Manual for detailed information on memory support and CPU/motherboard installation instructions.