



X10DRFF-iG
X10DRFF-iTG
X10DRFF-CG
X10DRFF-CTG

USER'S MANUAL

Revision 1.1b

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Manual Revision 1.1b

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Preface

This manual is written for system integrators, IT professionals, and knowledgeable end users. It provides information for the installation and use of the X10DRFF-iG/iTG/CG/CTG motherboard.

About This Motherboard

The Super X10DRFF-iG/iTG/CG/CTG motherboard supports dual Intel E5-2600v3/v4 Series processors (Socket R3) that offer new Intel Microarchitecture 22nm (E5-2600v3)/14nm (E5-2600v4) processing technology, delivering system performance, power efficiency, and feature sets required for next-generation cloud computing. With the PCH C612 built in, the X10DRFF-iG/iTG/CG/CTG motherboard supports Intel® Node Manager 3.0, Intel MCTP Protocol, and Management Engine (ME). This motherboard is ideal for 4U Fat_Twin and 1U Hadrop server platforms. Please refer to our website (<http://www.supermicro.com>) for CPU and memory support updates.

Manual Organization

Chapter 1 describes the features, specifications, and performance of the motherboard. It also provides detailed information about the Intel PCH C612 chipset.

Chapter 2 provides hardware installation instructions. Read this chapter when installing the processor, memory modules, and other hardware components into the system. If you encounter any problems, see **Chapter 3**, which describes troubleshooting procedures for video, memory, and system setup stored in the CMOS.

Chapter 4 includes an introduction to BIOS and provides detailed information on running the BIOS setup utility.


Appendix A lists software installation instructions.

Appendix B contains UEFI BIOS recovery instructions.

Conventions Used in the Manual

Pay special attention to the following symbols for proper system installation:

Warning: Important information given to ensure proper system installation or to prevent damage to the components or injury to yourself;

 **Note:** Additional information given to differentiate between models or instructions provided for proper system setup.

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Chapter 1


Overview

1-1 Overview


Checklist

Congratulations on purchasing your computer motherboard from an acknowledged leader in the industry. Supermicro boards are designed with the utmost care and attention to detail to provide you with the highest standards in quality and performance.

 **Note 1:** This motherboard was designed to be used with a Supermicro-proprietary chassis as an integrated server platform. It is not to be used as a stand-alone product and will not be shipped independently in a retail box. No motherboard shipping package will be provided in your shipment.


 **Note 2:** For your system to work properly, please follow the links below to download all necessary drivers/utilities and the user's manual for your system.

- Supermicro product manuals: <http://www.supermicro.com/support/manuals/>
- Product Drivers and utilities: <http://www.supermicro.com/wftp>

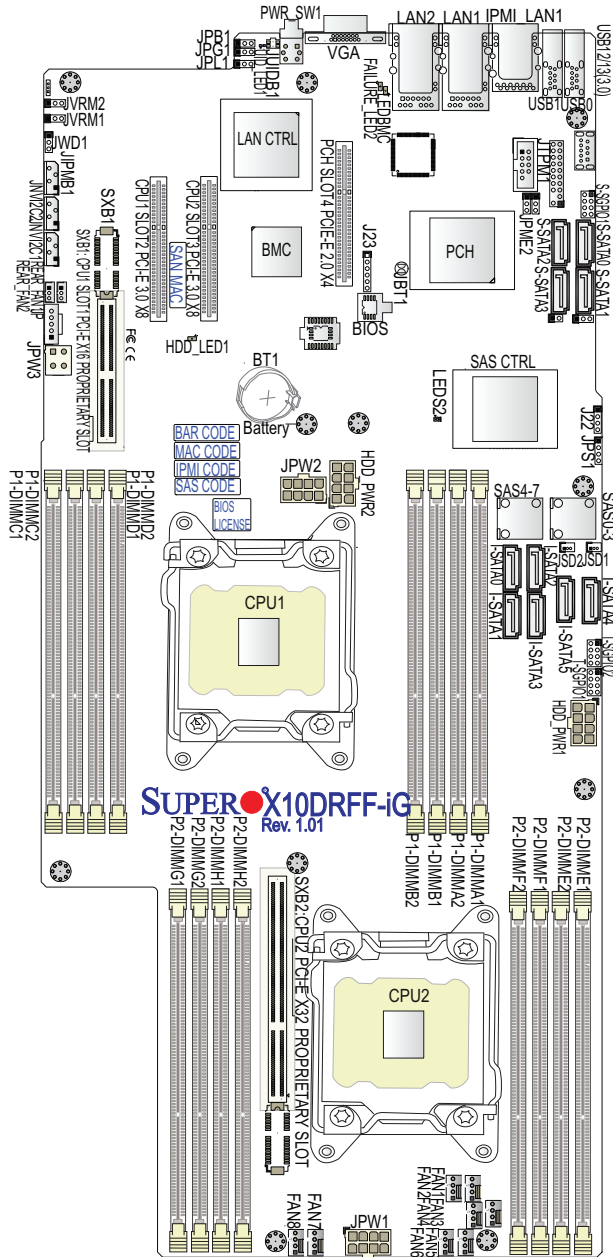
 **Note 3:** For safety considerations, please refer to the complete list of safety warnings posted on the Supermicro website at http://www.supermicro.com/about/policies/safety_information.cfm.

Motherboard Image



 **Note:** All graphics shown in this manual were based upon the latest PCB revision available at the time of publication of the manual. The motherboard you received may or may not look exactly the same as the graphics shown in this manual.

Motherboard Layout



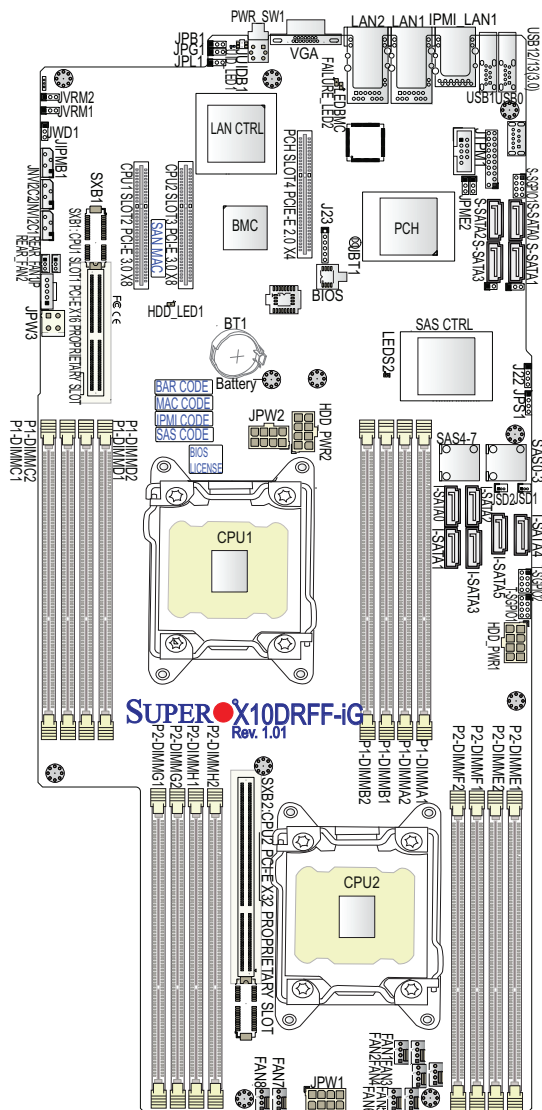
Differences between X10DRFF-iG/-iTG/-CG/-CTG

	X10DRFF-iG	X10DRFF-iTG	X10DRFF-CG	X10DRFF-CTG
SAS Connections & LSI 3008 SAS Controller	No	No	Yes	Yes
10G-LAN (TLAN) (w/X540 LAN Controller)	No	Yes	No	Yes
GLAN (w/i350 LANController)	Yes	No	Yes	No



Note: For the latest CPU/Memory updates, please refer to our website at <http://www.supermicro.com/products/motherboard/> for details.

X10DRFF-iG/iTG/CG/CTG Quick Reference



Notes:

- See Chapter 2 for detailed information on jumpers, I/O ports, and JF1 front panel connections.
- "■" indicates the location of Pin 1.
- Jumpers/LED indicators not indicated are used for testing only.
- Please refer to the table on Page 1-3 to see model variations.
- Use only the correct type of onboard CMOS battery as specified by the manufacturer. Do not install the onboard battery upside down to avoid possible explosion.

X10DRFF-iG/iTG/CG/CTG Jumpers

Jumper	Description	Default Setting
JBT1	Reset BIOS Configuration	See Chapter 2
JPB1	BMC Enable	Pins 1-2 (Enabled)
JPG1	VGA Enable	Pins 1-2 (Enabled)
JPL1	GLAN1/GLAN2 Enable (X10DRFF-iG/CG) (10G) TLAN1/TLAN2 Enable (X10DRFF-iTG/CTG)	Pins 1-2 (Enabled)
JPME2	Manufacture (ME) Mode Select	Pins 1-2 (Normal)
JPS1	SAS Enable (X10DRFF-CG/CTG Only)	Pins 1-2 (Enabled)
JWD1	Watch Dog Timer Enable	Pins 1-2 (Reset)

X10DRFF-iG/iTG/CG/CTG Connectors



Connectors	Description
BT1 (Battery)	Onboard CMOS battery (See Chpt. 3 for Used Battery Disposal)
FAN1-8, Rear FAN1/2	CPU/system fan headers (Fan 1-Fan 8, Rear Fan1/Rear Fan2) (Note: Rear Fan 1/Rear Fan 2 are proprietary fans.)
JIPMB1	4-pin external BMC I ² C header (for an IPMI card)
JNVI ² C1/2	System management bus (SMBbus) (I ² C) for NVME
JPI ² C1	Power supply SMBbus I ² C header
JPW1/JPW2	12V 8-pin power connectors 1/2
JPW3	4-pin (PS_ON_N) power connector
HDD PWR1/2	8-pin power connectors (1/2) for HDD devices
JSD1/JSD2	SATA DOM (Device on Module) power connectors 1/2
JTPM1	TPM (Trusted Platform Module)/Port 80 header
JUIDB1	UID (Unit Identifier) switch
LAN1/LAN2	Gigabit Ethernet (GLAN) ports 1/2 (X10DRFF-iG/CG) 10G-bit Ethernet (TLAN) ports 1/2 (X10DRFF-iTG/CTG)
IPMI_LAN	IPMI_dedicated LAN support by the ASpeed controller
PWR_SW1	Power switch
I-SATA 0-5	SATA 3.0 connectors supported by Intel PCH (I-SATA 0-5), (I-SATA4/I-SATA5: can be used as Supermicro SuperDOM (Disk-on-Module) with built-in power connectors)
S-SATA 0-3	SATA 3.0 connectors (0-3) supported by Intel PCH
SAS (0-3,4-7)	SAS 3.0 connections (0-3, 4-7) supported by the LSI 3008 (for X10DRFF-CG/CTG)
(CPU1) Slot2	PCI-Express 3.0 x8 slot from CPU1
(CPU2) Slot3	PCI-Express 3.0 x8 slot from CPU2
(PCH) Slot4	PCI-Express 2.0 x4 slot from PCH
SXB1	CPU1 SLOT1 PCI-E x16 Proprietary Slot
SXB2	CPU2 SLOT2 PCI-E x32 Proprietary Slot

T-SGPIO1/2	Serial_Link General Purpose I/O headers 1/2 for SATA ports (I-SGPIO1 for I-SATA0-3, I-SGPIO2 for I-SATA4/5)
S-SGPIO1	Serial_Link General Purpose I/O header for S-SATA ports (S-SATA0-3)
(BP) USB 12/13 (3.0)	Backpanel USB 3.0 ports 12/13 (USB0/1)
VGA	Backpanel VGA port

X10DRFF-iG/iTG/CG/CTG LED Indicators


LED	Description	State
HDD_LED1	HDD Activity LED	Green: Blinking (HDD Active) Solid: On (Overheating)
Failure_LED2	Motherboard Failure LED	Blinking at 1Hz (Fan Failure) Blinking at 0.25Hz (Power Supply Failure)
LEDBMC	BMC Heartbeat LED	Green: Blinking (BMC Normal)
LEDS2 (For -CG/-CTG)	SAS Heartbeat LED	Green: Blinking (SAS Normal)
UID_LED1	Rear UID LED	Blue: On (Unit Identified)

Motherboard Features

CPU	<ul style="list-style-type: none"> Dual Intel® E5-2600v3/v4 Series processors (Socket R3-LGA 2011); each processor supports Intel Quick-Path Interconnect (QPI) links (of up to 9.6 GT/s one direction per QPI) <p> Note 1: E5-2600v4 requires Revision 2.0 BIOS (or higher).</p> <p>Note 2: Both CPUs need to be installed for full access to the PCI-E slots, DIMM slots, and on-board controllers. Refer to the block diagram on page 1-10 to determine which slots or devices may be affected.</p>
Memory	<ul style="list-style-type: none"> Integrated memory controller supports up to 2048 GB of Load Reduced (LRDIMM) or 512 GB of Registered (RDIMM) DDR4 (288-pin) ECC 2400/2133/1866/1600 MHz modules in 16 slots <p> Note 1: Memory speed support depends on the processors used in the system.</p> <p>Note 2: For the latest CPU/memory updates, please refer to our website at http://www.super-micro.com/products/motherboard.</p>
	DIMM Sizes
	<ul style="list-style-type: none"> DIMM Up to 128GB @ 1.2V
Chipset	<ul style="list-style-type: none"> Intel® PCH C612
Expansion Slots	<ul style="list-style-type: none"> Two (2) PCI Express 3.0 x8 slots (CPU1 Slot2, CPU2 Slot3) One (1) PCI-Express 2.0 x4 slot (PCH Slot4) One (1) SMC-proprietary PCI Express x16 slot (SXB1) One (1) SMC-proprietary PCI Express x32 slot (SXB2)
Graphics	<ul style="list-style-type: none"> Graphics controller via ASpeed 2400 BMC

Network	<ul style="list-style-type: none"> Intel i350 Gigabit (10/100/1000 Mb/s) Ethernet controller for LAN 1/LAN 2 ports (X10DRFF-iG/CG) Intel X540 10-Gigabit (T) Ethernet controller for LAN 1/LAN 2 ports (X10DRFF-iTG/CTG) 	
	<ul style="list-style-type: none"> ASpeed AST 2400 Baseboard Controller (BMC) supports IPMI 2.0 	
I/O Devices	SATA/SAS Connections	
	<ul style="list-style-type: none"> SATA 3.0 	<p>Six (6) SATA 3.0 ports supported by Intel PCH (I-SATA 0-5)</p> <p>Four (4) SATA 3.0 with 4 connections supported by the PCH (S-SATA 0-3)</p>
	<ul style="list-style-type: none"> RAID (PCH) 	RAID 0, 1, 10, 5
	<ul style="list-style-type: none"> S A S 3 . 0 (X10DRFF-CG / CTG Only) 	Eight (8) SAS/SATA 3.0 connections (SAS 0-3, 4-7)
	<ul style="list-style-type: none"> R A I D (LSI3008) 	RAID 0, 1, 10 Software RAID from the LSI 3008 SAS controller
	IPMI 2.0	
	<ul style="list-style-type: none"> IPMI 2.0 supported by ASpeed AST 2400 	
Peripheral Devices	USB Devices	
	<ul style="list-style-type: none"> Two (2) USB 3.0 ports on the rear I/O panel (USB 12/13) 	
BIOS	<ul style="list-style-type: none"> 16 MB SPI AMI BIOS® SM Flash UEFI BIOS 	
	<ul style="list-style-type: none"> ACPI 2.0/3.0/4.0, USB keyboard, Plug-and-Play (PnP), SPI quad speed support, and SMBIOS 2.3 	
Power Management	<ul style="list-style-type: none"> ACPI Power Management 	
	<ul style="list-style-type: none"> Main switch override mechanism 	
	<ul style="list-style-type: none"> Power-on mode for AC power recovery 	
	<ul style="list-style-type: none"> Intel® Intelligent Power Node Manager 3.0 (available when the Supermicro Power Manager [SPM] is installed and a special power supply is used. See the note on Page 1-14.) 	
	<ul style="list-style-type: none"> Management Engine (ME) 	

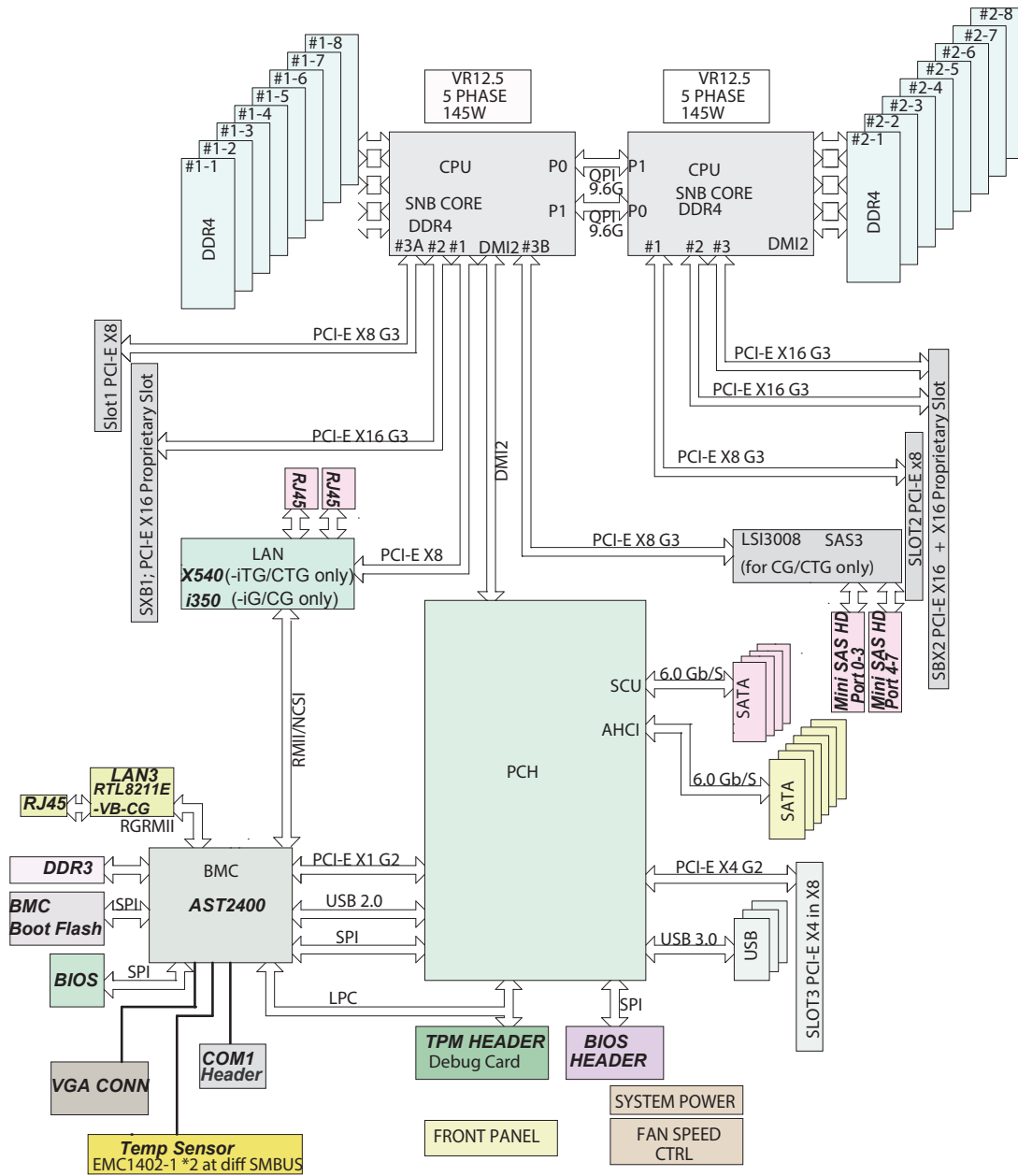
System Health Monitoring	System Health/CPU Monitoring
	<ul style="list-style-type: none"> Onboard voltage monitoring for +3.3V, 3.3V standby, +5V, +5V standby, +12V, CPU Vcore, memory, BMC, PCH, and VBAT voltages
	<ul style="list-style-type: none"> CPU/system overheat LED and control
	<ul style="list-style-type: none"> CPU Thermal Trip support
	<ul style="list-style-type: none"> Status monitor for speed control
	<ul style="list-style-type: none"> Status monitor for on/off control
	<ul style="list-style-type: none"> CPU Thermal Design Power (TDP) support of up to 145W (See Note 1 below.)
	Fan Control
	<ul style="list-style-type: none"> Fan status monitoring via IPMI connections
	<ul style="list-style-type: none"> Low-noise fan speed control
System Management	<ul style="list-style-type: none"> PECI (Platform Environment Control Interface) 2.0 support
	<ul style="list-style-type: none"> UID (Unit Identifier)/Remote UID
	<ul style="list-style-type: none"> System resource alert via SuperDoctor® 5
	<ul style="list-style-type: none"> SuperDoctor 5, Watch Dog, NMI
Dimensions	<ul style="list-style-type: none"> 18.73" (L) x 8.54" (W) (475.74 mm x 216.92 mm)

 **Note 1:** The CPU maximum thermal design power (TDP) is subject to chassis and heatsink cooling restrictions. For proper thermal management, please check the chassis and heatsink specifications for proper CPU TDP sizing.


Note 2: For IPMI configuration instructions, please refer to the Embedded IPMI Configuration User's Guide available @ <http://www.supermicro.com/support/manuals/>.

Note 3: It is strongly recommended that you change BMC log-in information upon initial system power-on. The manufacturer default username is ADMIN and the password is ADMIN. For proper BMC configuration, please refer to http://www.supermicro.com/products/info/files/IPMI/Best_Practices_BMC_Security.pdf

Note 4: To avoid causing interference with other components, please be sure to use an add-on card that is fully compliant with the PCI-standard on a PCI slot.



System Block Diagram

 **Note 1:** This is a general block diagram and may not exactly represent the features on your motherboard. See the Motherboard Features pages for the actual specifications of each motherboard.

Note 2: Both CPUs need to be installed for full access to the PCI-E slots, DIMM slots, and onboard controllers. Refer to the block diagram above to determine which slots or devices may be affected.

1-2 Processor and Chipset Overview

Built upon the functionality and capability of the Intel E5-2600v3/v4 Series processors (Socket R3) and the Intel C612 PCH, the X10DRFF-iG/iTG/CG/CTG motherboard provides system performance, power efficiency, and feature sets to address the needs of next-generation cloud computing.

With support of the new Intel microarchitecture 22nm (E5-2600v3)/14nm (E5-2600v4) process technology, the X10DRFF-iG/iTG/CG/CTG drastically increases system performance for a multitude of server applications.

The PCH C612 chip provides Enterprise SMBus support, including the following features:

- DDR4 288-pin memory support on Socket R3
- Support for MCTP Protocol
- Support for Management Engine (ME)
- Support of SMBus speeds of up to 400KHz for BMC connectivity
- Improved I/O capabilities to high-storage-capacity configurations
- SPI enhancements
- Intel® Node Manager 3.0 for advanced power monitoring, capping, and management for BMC enhancement
- The BMC supports remote management, virtualization, and the security package for enterprise platforms



Note 1: E5-2600v4 requires Revision 2.0 BIOS (or higher).

Note 2: Node Manager 3.0 support is dependent on the power supply used in the system.

1-3 Special Features

Recovery from AC Power Loss

The Basic I/O System (BIOS) provides a setting that determines how the system will respond when AC power is lost and then restored to the system. You can choose for the system to remain powered off (in which case you must press the power switch to turn it back on) or for it to automatically return to the power-on state. See the Advanced BIOS Setup section for this setting. The default setting is **Last State**.

1-4 System Health Monitoring


This section describes the features of system health monitoring of the motherboard. This motherboard has an onboard Baseboard Management Controller (BMC) that supports system health monitoring. An onboard voltage monitor will scan the following onboard voltages continuously: +3.3V, 3.3V standby, +5V, +5V standby, +12V, CPU Vcore, memory, BMC, PCH, and VBAT voltages. Once a voltage becomes unstable, a warning is given or an error message is sent to the screen. The user can adjust the voltage thresholds to define the sensitivity of the voltage monitor.

Fan Status Monitor with Firmware Control

System health monitoring support provided by the BMC controller can check the RPM status of a cooling fan. The onboard CPU and chassis fans are controlled by IPMI Thermal Management.

Environmental Temperature Control

System health sensors in the BMC monitors temperatures and voltage settings of onboard processors and the system in real time via the IPMI interface. Whenever the temperature of the CPU or the system exceeds a user-defined threshold, system/CPU cooling fans will be turned on to prevent the CPU or system from overheating.

 **Note:** To avoid possible system overheating, please be sure to provide adequate airflow to your system.

System Resource Alert

This feature is available when used with SuperDoctor 5, which is the system health monitoring software used to notify the user of certain system events. For example, you can configure SuperDoctor 5 to warn you when the system temperature, CPU temperatures, voltages, or fan speeds go beyond a predefined range.

1-5 ACPI Features

ACPI stands for *Advanced Configuration and Power Interface*. The ACPI specification defines a flexible and abstract hardware interface that provides a standard way to integrate power management features throughout a system, including its hardware, operating system, and application software. This enables the system to automatically turn on and off peripherals such as network cards, hard-disk drives, and printers.

In addition to power management directed by the operating system, the ACPI also provides a generic system event mechanism for Plug-and-Play and an interface (independent of the operating system) for configuration control. ACPI leverages the Plug-and-Play BIOS data structures, while providing an implementation that is independent of the processor architecture and compatible with Windows 8/R2 and Windows 2012/R2 Operating Systems.

1-6 Power Supply

As with all computer products, a stable power source is necessary for proper and reliable operation. This is even more important for processors that have high CPU clock rates.

As a part of the Fat Twin server platform, this motherboard requires a special power supply unit along with an appropriate backplane and/or adaptor recommended by the manufacturer to provide adequate power to the system.


It is strongly recommended that you use a high-quality power supply that meets the ATX power supply specification 2.02 or above. It must also be SSI-compliant. (For more information, please refer to the website at <http://www.ssiforum.org/>.) Additionally, in areas where noisy power transmission is present, you may choose to install a line filter to shield the computer from noise. It is recommended that you also install a power surge protector to help avoid problems caused by power surges.

1-7 Advanced Power Management

The following new advanced power management features are supported by this motherboard:

Intel® Intelligent Power Node Manager (NM) (Available when the Supermicro Power Manager [SPM] is installed)

The Intel® Intelligent Power Node Manager 3.0 (IPNM) provides your system with real-time thermal control and power management for maximum energy efficiency. Although IPNM Specification Version 2.0/3.0 is supported by the BMC (Baseboard Management Controller), your system must also have IPNM-compatible Management Engine (ME) firmware installed to use this feature.

 **Note:** Support for IPNM Specification Version 2.0 or Version 3.0 is dependent on the power supply used in the system.

Management Engine (ME)

The Management Engine, which is an ARC controller embedded in the PCH, provides Server Platform Services (SPS) to your system. The services provided by SPS are different from those provided by the ME on client platforms.

Chapter 2

Installation

2-1 Standardized Warning Statements

The following statements are industry-standard warnings, provided to warn the user of situations which have the potential for bodily injury. Should you have questions or experience difficulty, contact Supermicro's Technical Support Department for assistance. Only certified technicians should attempt to install or configure components.

Read this section in its entirety before installing or configuring components in the Supermicro chassis.

Battery Handling



Warning!

There is a danger of explosion if the battery is replaced incorrectly. Replace the battery only with the same or an equivalent type recommended by the manufacturer. Dispose of used batteries according to the manufacturer's instructions.

電池の取り扱い

電池交換が正しく行われなかった場合、破裂の危険性があります。交換する電池はメーカーが推奨する型、または同等のものを使用下さい。使用済電池は製造元の指示に従って処分して下さい。

警告

電池更換不當會有爆炸危險。請只使用同類電池或制造商推薦的功能相當的電池更換原有電池。請按製造商的說明處理廢舊電池。

警告

電池更換不當會有爆炸危險。請使用製造商建議之相同或功能相當的電池更換原有電池。請按照製造商的說明指示處理廢棄舊電池。

Warnung

Bei Einsetzen einer falschen Batterie besteht Explosionsgefahr. Ersetzen Sie die Batterie nur durch den gleichen oder vom Hersteller empfohlenen Batterietyp. Entsorgen Sie die benutzten Batterien nach den Anweisungen des Herstellers.

Attention

Danger d'explosion si la pile n'est pas remplacée correctement. Ne la remplacer que par une pile de type semblable ou équivalent, recommandée par le fabricant. Jeter les piles usagées conformément aux instructions du fabricant.

¡Advertencia!

Existe peligro de explosión si la batería se reemplaza de manera incorrecta. Reemplazar la batería exclusivamente con el mismo tipo o el equivalente recomendado por el fabricante. Desechar las baterías gastadas según las instrucciones del fabricante.

אזהרה !

קיימת סכנת פיצוץ של הסוללה במידה והוחלפה בדרך לא תקינה. יש להחליף את הסוללה בסוג התואם מחברת יצרן מומלצת.

סילוק הסוללות המשומשות יש לבצע לפי הוראות היצרן.

هناك خطر من التيار الكهربائي أو الطاقة الموجودة على اللوحة عندما يكون النظام يعمل كن حذرا عند خدمة هذا الجهاز

경고!

배터리가 올바르게 교체되지 않으면 폭발의 위험이 있습니다. 기존 배터리와 동일하거나 제조사에서 권장하는 동등한 종류의 배터리로만 교체해야 합니다. 제조사의 안내에 따라 사용된 배터리를 처리하여 주십시오.

Waarschuwing

Er is ontploffingsgevaar indien de batterij verkeerd vervangen wordt. Vervang de batterij slechts met hetzelfde of een equivalent type die door de fabrikant aanbevolen wordt. Gebruikte batterijen dienen overeenkomstig fabrieksvoorschriften afgevoerd te worden.

Product Disposal



Warning!

Ultimate disposal of this product should be handled according to all national laws and regulations.

製品の廃棄

この製品を廃棄処分する場合、国の関係する全ての法律・条例に従い処理する必要があります。

警告

本产品的废弃处理应根据所有国家的法律和规章进行。

警告

本產品的廢棄處理應根據所有國家的法律和規章進行。

Warnung

Die Entsorgung dieses Produkts sollte gemäß allen Bestimmungen und Gesetzen des Landes erfolgen.

¡Advertencia!

Al deshacerse por completo de este producto debe seguir todas las leyes y reglamentos nacionales.

Attention

La mise au rebut ou le recyclage de ce produit sont généralement soumis à des lois et/ou directives de respect de l'environnement. Renseignez-vous auprès de l'organisme compétent.

סילוק המוצר

אזהרה!

סילוק סופי של מוצר זה חייב להיות בהתאם להנחיות וחוקי המדינה.

عند التخلص النهائي من هذا المنتج ينبغي التعامل معه وفقا لجميع القوانين واللوائح الوطنية

경고!

이 제품은 해당 국가의 관련 법규 및 규정에 따라 폐기되어야 합니다.

Waarschuwing

De uiteindelijke verwijdering van dit product dient te geschieden in overeenstemming met alle nationale wetten en reglementen.

2-2 Static-Sensitive Devices

Electrostatic Discharge (ESD) can damage electronic components. To avoid damaging your system board, it is important to handle it very carefully. The following measures are generally sufficient to protect your equipment from ESD.

Precautions

- Use a grounded wrist strap designed to prevent static discharge.
- Touch a grounded metal object before removing the board from the antistatic bag.
- Handle the motherboard by its edges only; do not touch its components, peripheral chips, memory modules, or gold contacts.
- When handling chips or modules, avoid touching their pins.
- Put the motherboard and peripherals back into their antistatic bags when not in use.
- For grounding purposes, make sure that your system chassis provides excellent conductivity between the power supply, case, mounting fasteners, and the motherboard.

Unpacking

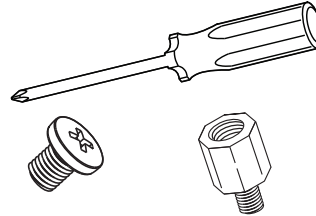
The motherboard is shipped in antistatic packaging to avoid static damage. When unpacking the motherboard, make sure that the person handling it is static-protected.

2-3 Motherboard Installation

All motherboards have standard mounting holes to fit different types of chassis. Make sure that the locations of all mounting holes for both motherboard and chassis match. Although a chassis may have both plastic and metal mounting fasteners, metal ones are recommended because they ground the motherboard to the chassis. Make sure that the metal standoffs click in or are screwed in tightly. Then use a screwdriver to secure the motherboard onto the motherboard tray.

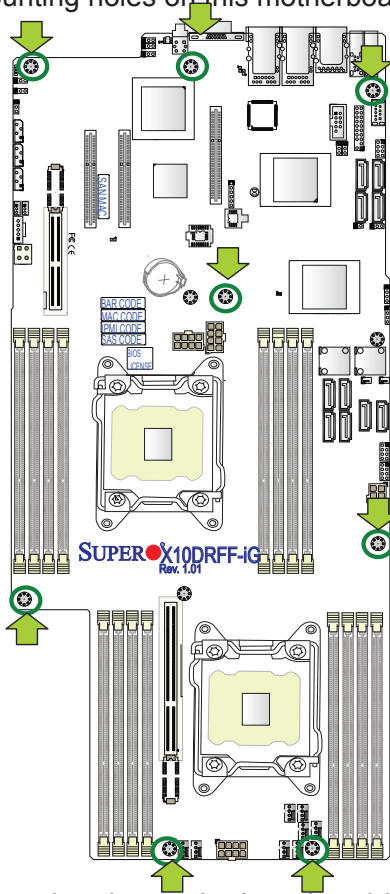
Tools Needed

- Phillips screwdriver
- Panhead screws (8 pieces)
- Standoffs (8 pieces, if needed)




Location of Mounting Holes

There are eight (8) mounting holes on this motherboard as indicated by the arrows.

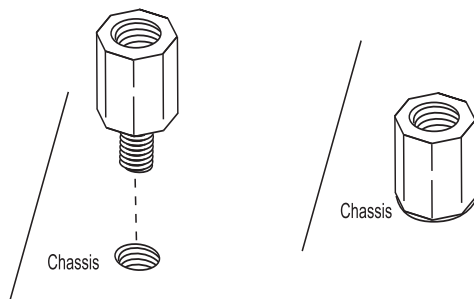


Caution: 1) To avoid damaging the motherboard and its components, do not use a force greater than 8 lb/inch on each mounting screw during motherboard installation. 2) Some components are very close to the mounting holes. Please use caution to prevent damage to these components when installing the motherboard to the chassis.

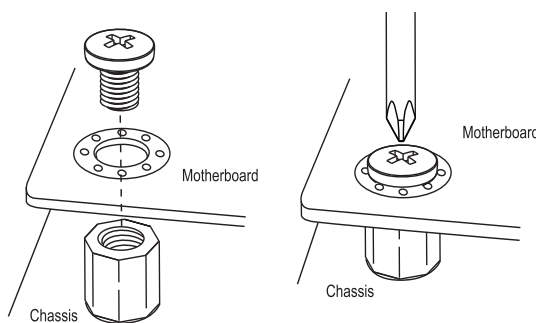
Installing the Motherboard

 **Note:** Always connect the power cord last, and always remove it before adding, removing, or changing any hardware components.


1. Locate the mounting holes on the motherboard.
2. Locate the matching mounting holes on the chassis. Align the mounting holes on the motherboard against the mounting holes on the chassis.
3. Install standoffs in the chassis if needed.



4. Install the motherboard into the chassis carefully to avoid damaging motherboard components.
5. Using the Phillips screwdriver, insert a panhead #6 screw into a mounting hole on the motherboard and its matching mounting hole on the chassis.
6. Repeat Step 5 to insert #6 screws into all mounting holes.



7. Make sure that the motherboard is securely placed in the chassis.

 **Note:** Images displayed are for illustration only. Your chassis or components might look different from those shown in this manual.

2-4 Processor and Heatsink Installation

Warning: When handling the processor package, avoid placing direct pressure on the label area. Also, improper CPU installation and socket/pin misalignment may cause serious damage to the CPU or the motherboard that will require RMA repairs. Be sure to read and follow all instructions thoroughly before installing your CPU and heatsink.



Notes:

Always connect the power cord last, and always remove it before adding, removing, or changing any hardware components. Make sure that you install the processor into the CPU socket before you install the CPU heatsink.

If you buy a CPU separately, make sure that you use an Intel-certified multidirectional heatsink only.

Make sure to install the motherboard into the chassis before you install the CPU heatsink.

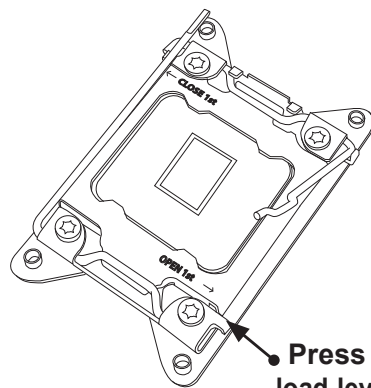
If you receive a motherboard without a processor preinstalled, make sure that the plastic CPU socket cap is in place and that none of the socket pins are bent; otherwise, contact your retailer immediately.

Refer to the Supermicro website for updates on CPU support.

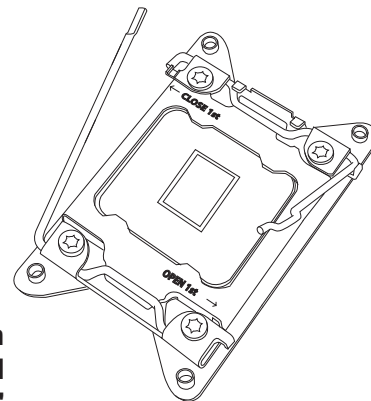
Installing the LGA2011 Processor

1. There are two load levers on the LGA2011 socket. To open the socket cover, first press and release the load lever labeled "Open 1st."

1



2

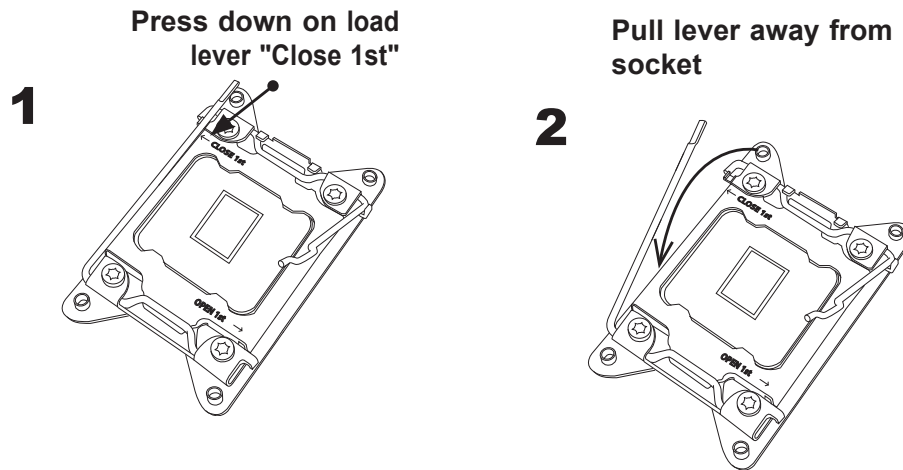


Press down on load lever labeled "Open 1st"

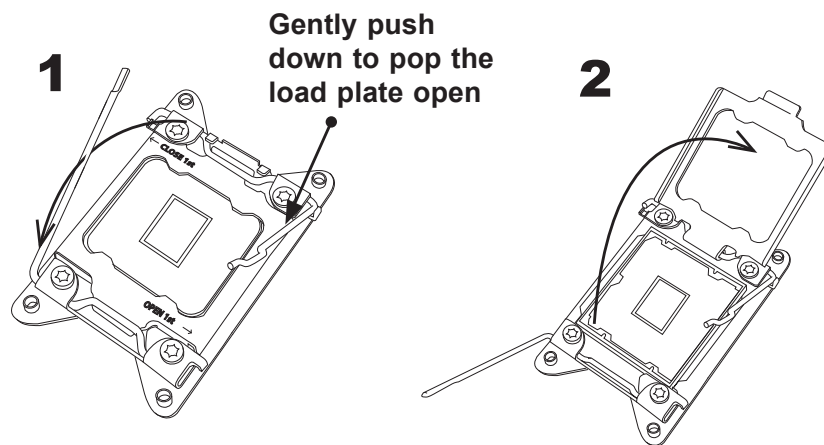



Note: All graphics, drawings, and pictures shown in this manual are for illustration only. The components that came with your machine may or may not look exactly the same as those shown in this manual.

2. Press the second load lever labeled "Close 1st" to release the load plate that covers the CPU socket from its locking position.

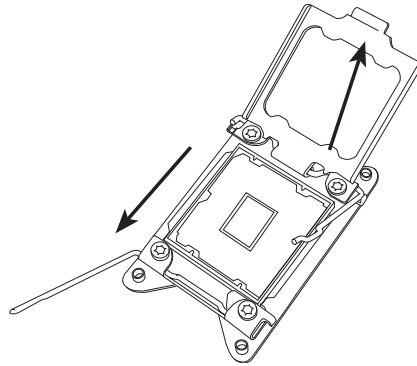


3. With the lever labeled "Close 1st" fully retracted, gently push down on the lever labeled "Open 1st" to open the load plate. Lift the load plate to open it completely.

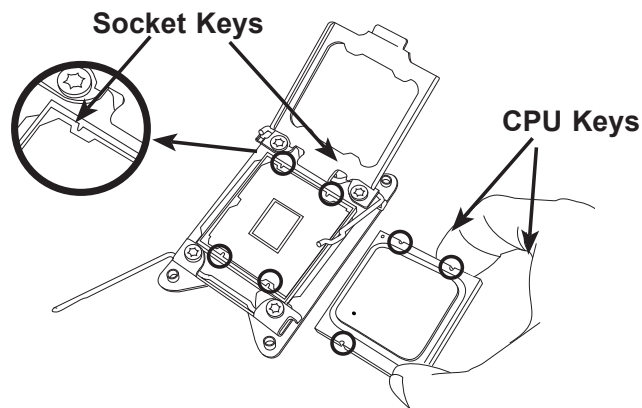


 **Note:** All graphics, drawings and pictures shown in this manual are for illustration only. The components that came with your machine may or may not look exactly the same as those shown in this manual.

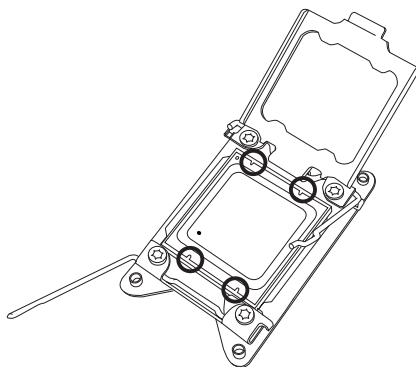
- Use your thumb and index finger to loosen the lever and open the load plate.



- Using your thumb and index finger, hold the CPU by its edges. Align the CPU keys, which are semicircle notches, against the socket keys.

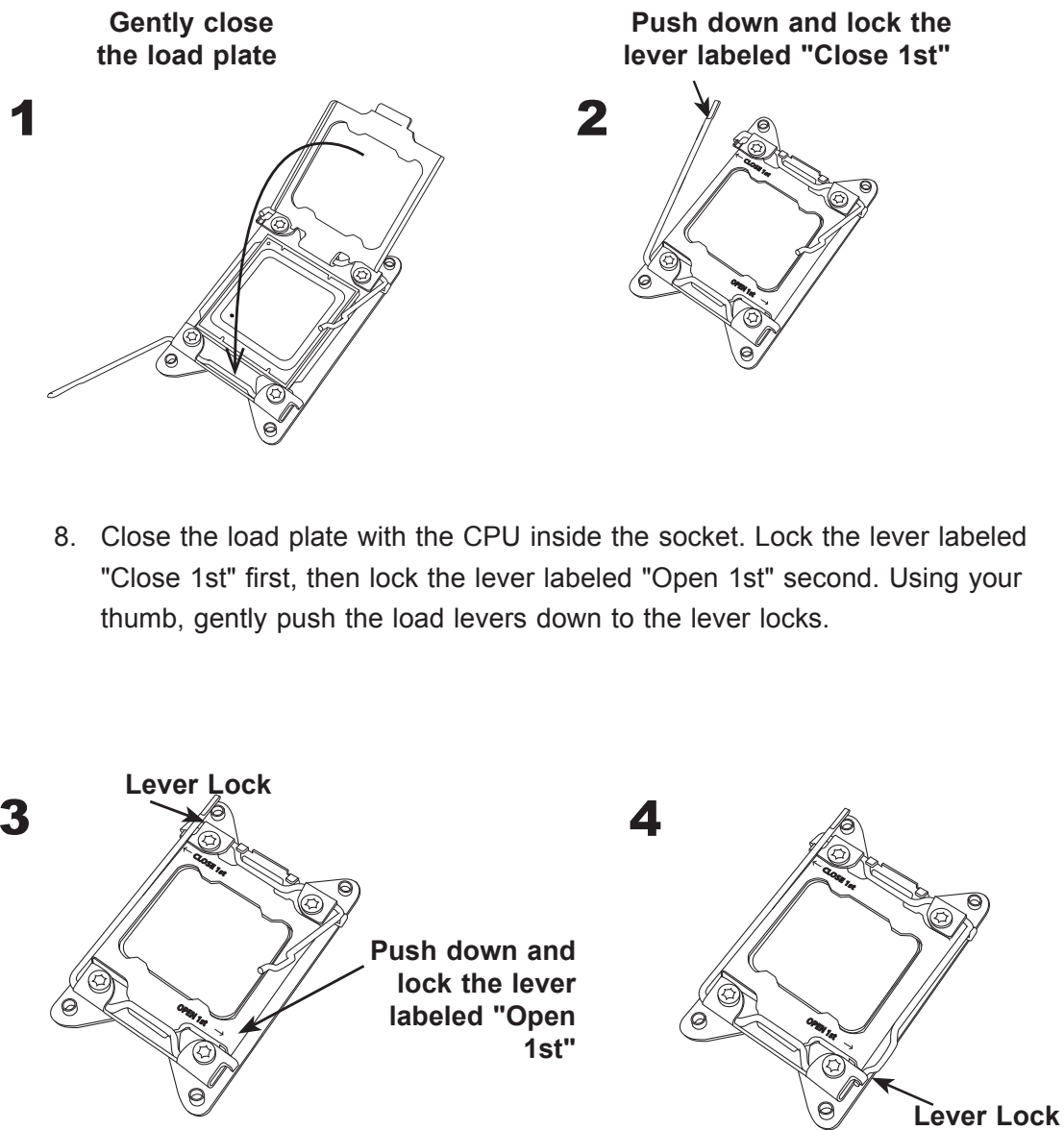


- Once they are aligned, carefully lower the CPU straight down into the socket. Do not drop the CPU on the socket or move the CPU horizontally or vertically. Do not rub the CPU against the surface or against any of the socket pins to avoid damaging the CPU or the socket.



Warning: You can only install the CPU inside the socket in one direction. Make sure that it is properly inserted into the CPU socket before closing the load plate. If it doesn't close properly, do not force it as this may damage your CPU. Instead, open the load plate again to make sure that the CPU is aligned properly.

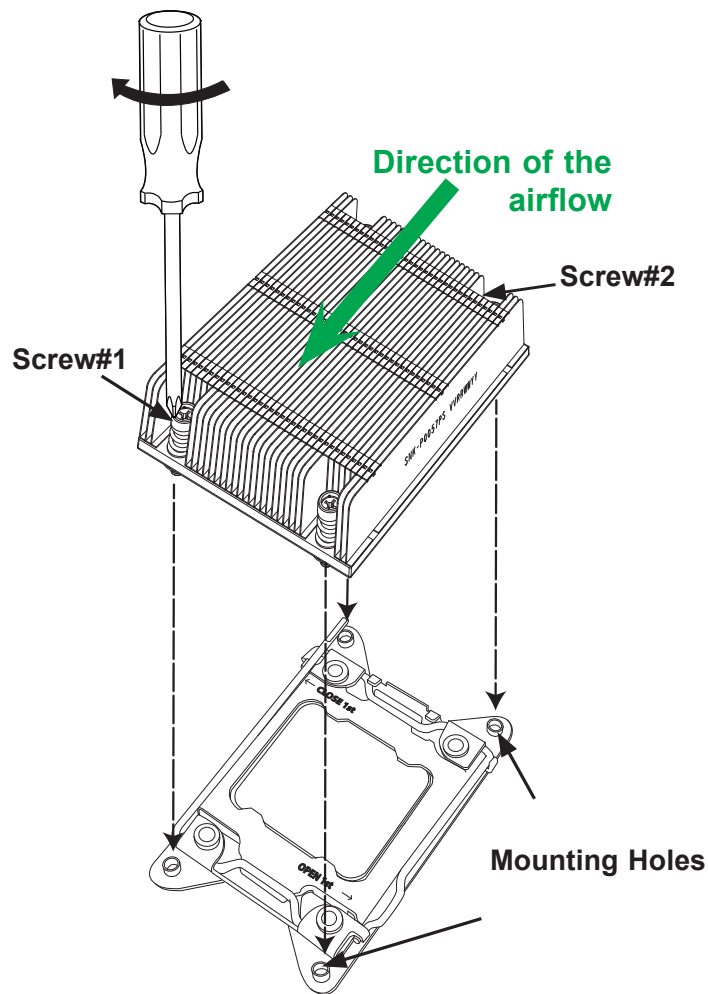
7. With the CPU inside the socket, inspect the four corners of the CPU to make sure that the CPU is properly installed.




8. Close the load plate with the CPU inside the socket. Lock the lever labeled "Close 1st" first, then lock the lever labeled "Open 1st" second. Using your thumb, gently push the load levers down to the lever locks.

Installing a Passive CPU Heatsink

1. Do not apply any thermal grease to the heatsink or the CPU die -- the required amount has already been applied.
2. Place the heatsink on top of the CPU so that the four mounting holes are aligned with those on the motherboard and the heatsink bracket underneath.
3. Screw in two diagonal screws (e.g., the #1 and the #2 screws) until just snug. Do not over-tighten the screws to avoid damaging the CPU and the motherboard.
4. Finish the installation by fully tightening all four screws.

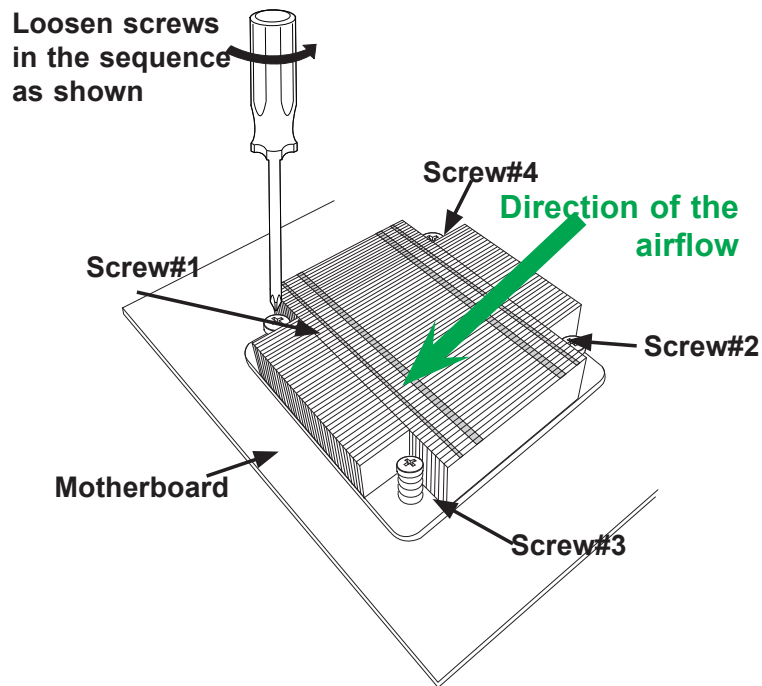



 **Note:** For optimal airflow, please follow your chassis airflow direction to correctly install the CPU heatsink. Graphic drawings included in this manual are for reference only. They might look different from the components installed in your system.

Removing the CPU and the Heatsink

Warning: We do not recommend that the CPU or the heatsink be removed. However, if you do need to uninstall the CPU or the heatsink, please follow the instructions below to uninstall the heatsink to avoid damaging the CPU or the motherboard.

1. Unscrew the heatsink screws from the motherboard in the sequence as shown in the illustration below.
2. Gently wriggle the heatsink to loosen it from the CPU. Do not use excessive force when loosening the heatsink!
3. Once the heatsink is loosened from the socket, remove the heatsink from the CPU socket. Once the heatsink is removed, remove the CPU from the socket as needed.
4. Remove the used thermal grease and clean the surface of the CPU and the heatsink. Reapply the proper amount of thermal grease on the surface before reinstalling the CPU and the heatsink as needed.



 **Note 1:** For optimized airflow, please follow your chassis airflow direction to properly install the heatsink.

Note 2: Graphics shown in this manual are for reference only. They may or may not look the same as the components installed in your system.

2-5 Installing and Removing the Memory Modules

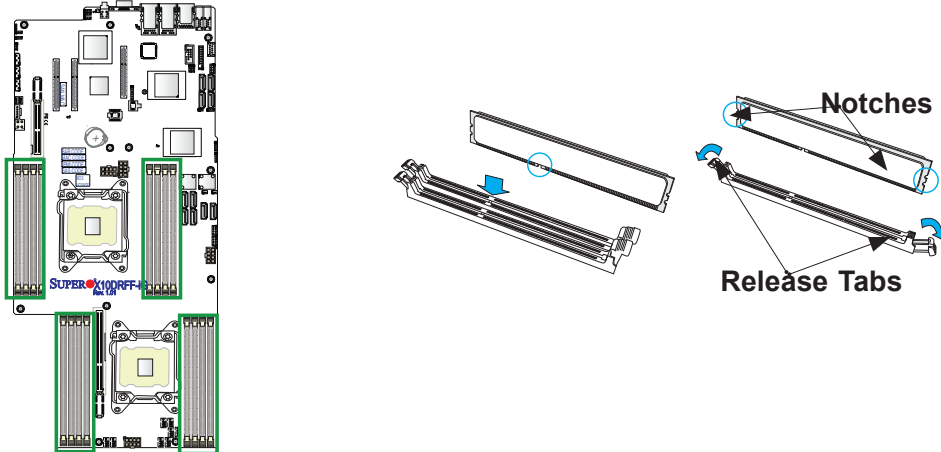
Note: Check Supermicro's website for a list of recommended memory modules.

CAUTION

Exercise extreme care when installing or removing DIMM modules to avoid damaging the DIMM modules or the motherboard.

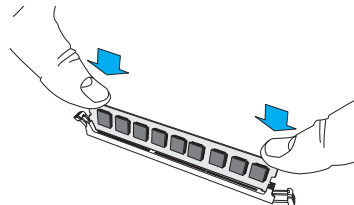
Installing & Removing DIMMs

1. Insert the desired number of DIMMs into the memory slots, starting with P1-DIMMA1. For best performance, please use DIMM modules of the same type and speed in the same bank.
2. Push the release tabs outwards on both ends of the DIMM slot to unlock it.



3. Align the key of the DIMM module with the receptive point on the memory slot.
4. Align the notches on both ends of the module against the receptive points at each side of the slot.
5. With your thumbs, press the notches on both ends of the module straight down into the slot until the module snaps into place.
6. Press the release tabs to their locking positions to secure the DIMM into the slot.

Press both notches straight down into the memory slot at the same time.



Removing Memory Modules

Press both release tabs on the ends of the DIMM to unlock it. Once the DIMM is loosened, remove it from the memory slot.

Memory Support for the X10DRFF-iG/iTG/CG/CTG Motherboard

The X10DRFF-iG/iTG/CG/CTG motherboard supports up to 2048 GB of Load Reduced (LRDIMM) or 512 GB of Registered (RDIMM) DDR4 (288-pin) ECC 2400/2133/1866/1600 MHz modules in 16 slots. Memory speed support depends on the CPUs installed in the motherboard. For the latest memory updates, please refer to our website at <http://www.supermicro.com/products/motherboard>.

Processor & Memory Module Population Configuration

For the memory to work properly, follow the tables below for memory installation.

Processors and their Corresponding Memory Modules								
CPU#	Corresponding DIMM Modules							
CPU 1	P1-DIMMA1	P1-DIMMB1	P1-DIMMC1	P1-DIMMD1	P1-DIMMA2	P1-DIMMB2	P1-DIMMC2	P1-DIMMD2
CPU2	P2-DIMME1	P2-DIMMF1	P2-DIMMG1	P2-DIMMH1	P2-DIMME2	P2-DIMM F2	P2-DIMMG2	P2-DIMMH2

Populating RDIMM/LRDIMM DDR4 Memory Modules for the E5-2600v3-based Motherboard

Type	Ranks Per DIMM and Data Width	DIMM Capacity (GB)		Speed (MT/s); Voltage (V); Slot Per Channel (SPC) and DIMM Per Channel (DPC)		
				1 Slot Per Channel	2 Slots Per Channel	
		4Gb	8Gb	1DPC	1DPC	2DPC
				1.2V	1.2V	1.2V
RDIMM	SRx4	8GB	16GB	2133	2133	1866
RDIMM	SRx8	4GB	8GB	2133	2133	1866
RDIMM	DRx8	8GB	16GB	2133	2133	1866
RDIMM	DRx4	16GB	32GB	2133	2133	1866
LRDIMM	QRx4	32GB	64GB	2133	2133	2133
LRDIMM 3DS[†]	8Rx4	64GB	128GB	2133	2133	2133

Populating RDIMM/LRDIMM DDR4 Memory Modules for the E5-2600v4-based Motherboard

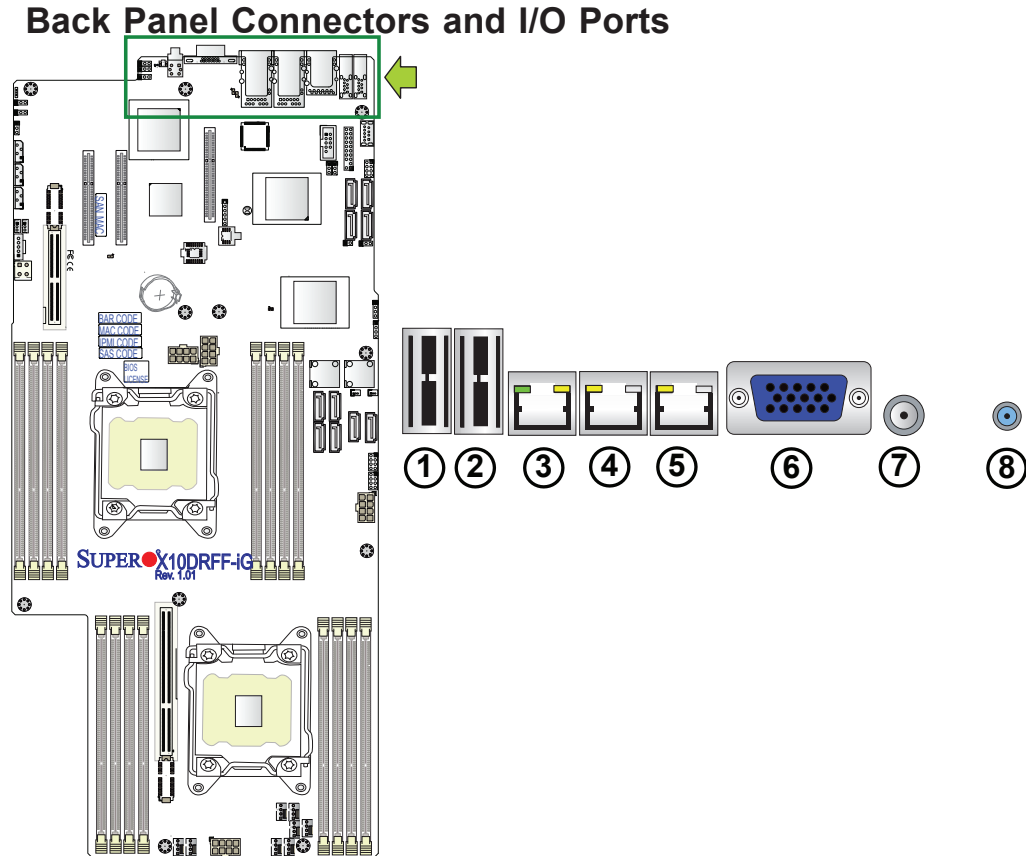
Type	Ranks Per DIMM and Data Width	DIMM Capacity (GB)		Speed (MT/s); Voltage (V); Slot Per Channel (SPC) and DIMM Per Channel (DPC)		
				1 Slot Per Channel	2 Slots Per Channel	
		4Gb	8Gb	1DPC	1DPC	2DPC
RDIMM	SRx4	8GB	16GB	2400	2400	2133
RDIMM	SRx8	4GB	8GB	2400	2400	2133
RDIMM	DRx8	8GB	16GB	2400	2400	2133
RDIMM	DRx4	16GB	32GB	2400	2400	2133
LRDIMM	QRx4	32GB	64GB	2400	2400	2400
LRDIMM 3DS	8Rx4	64GB	128GB	2400	2400	2400

An Important Note:

- For the memory modules to work properly, please install DIMM modules of the same type, same speed and same operating frequency in the motherboard. Mixing of DIMMs of different types or different speeds is not allowed.

2-6 Control Panel Connectors and I/O Ports

The I/O ports are color-coded in conformance with the industry standards. See the picture below for the colors and locations of the various I/O ports.



Back Panel I/O Port Locations and Definitions

1.	Backpanel USB 3.0 Port 12
2.	Backpanel USB 3.0 Port 13
3.	IPMI-Dedicated LAN
4.	Gigabit LAN 1 (X10DRFF-iG/CG), 10GbE LAN 1 (X10DRFF-iTG/CTG)
5.	Gigabit LAN 2 (X10DRFF-iG/CG), 10GbE LAN 2 (X10DRFF-iTG/CTG)
6.	VGA (Blue)
7.	Power Switch
8.	UID Button/UID LED (UID_LED1)

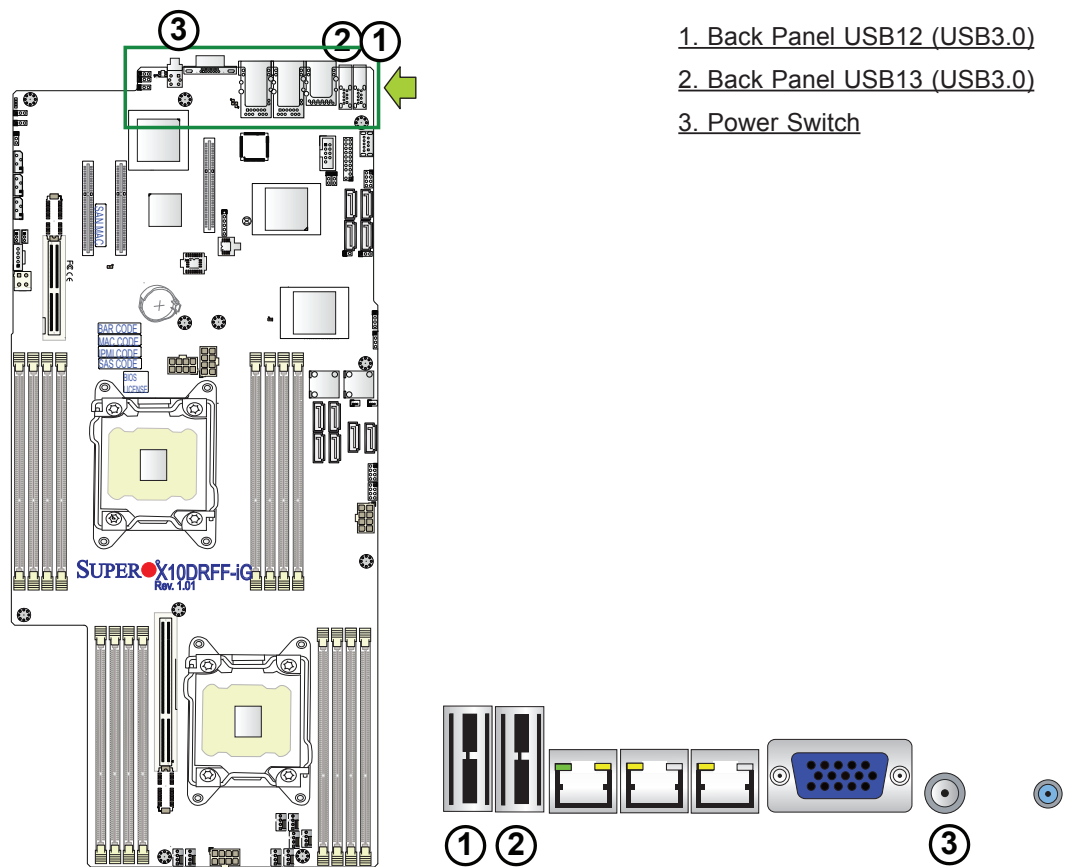
Universal Serial Bus (USB)

Two USB ports (USB 12/13) are located on the I/O back panel to provide USB 3.0 connections to the system. (Cables are not included.) See the tables on the right and below for pin definitions.

Back Panel USB 12/13 (3.0) Pin Definitions	
Pin#	Description
1	VBUS
2	SSRX-
3	SSRX+
4	Ground
5	SSTX-
6	SSTX+
7	GND_DRAIN
8	D-
9	D+

Power Switch

A power switch is located next to the VGA port on the IO back panel. Press this switch to turn on or turn off the system power. See the layout below for the location of the power switch.



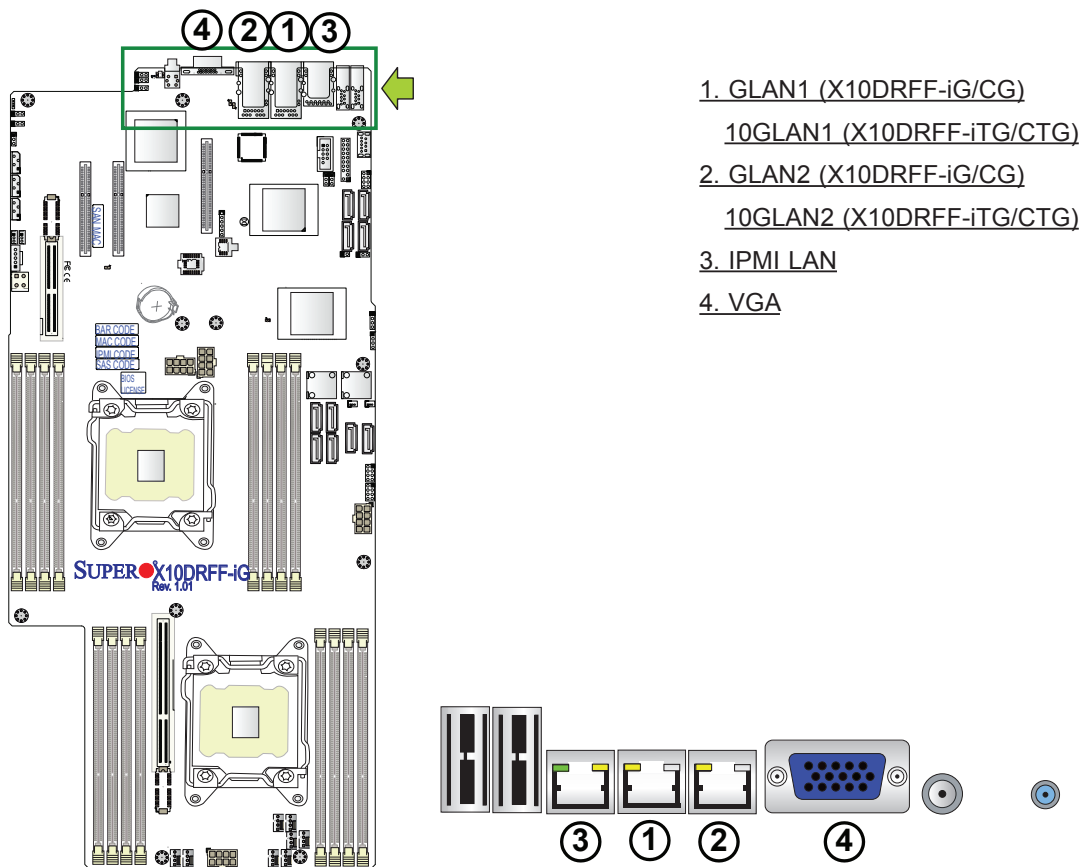
1. Back Panel USB12 (USB3.0)
2. Back Panel USB13 (USB3.0)
3. Power Switch

Ethernet Ports

Two Ethernet ports (LAN1 and LAN2) are located on the I/O back panel on the motherboard. These Ethernet ports support 1_Gigabit LANs on the X10DRFF-iG/CG, and 10_Gigabit LANs on the X10DRFF-iTG/CTG. In addition, an IPMI-dedicated LAN is located next to USB ports on the back panel. All of these ports accept RJ45 cables. Please refer to the LED Indicator section for LAN LED information.


Video Connection

A Video (VGA) port is located next to LAN Port 2 on the I/O back panel. Refer to the board layout below for its location.



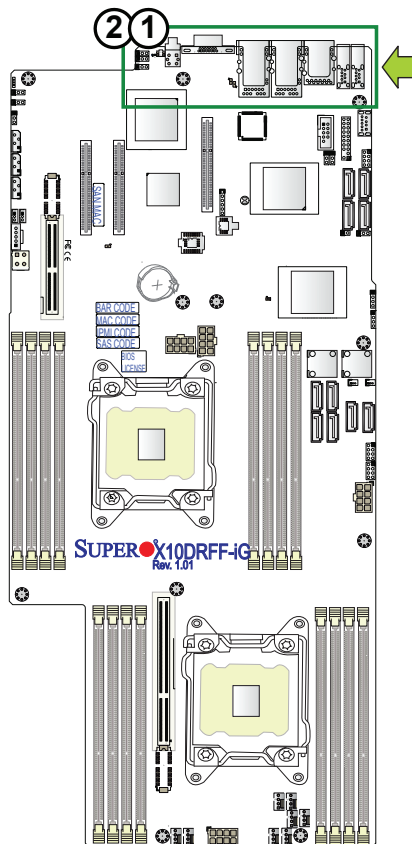
Unit Identifier Button/UID LED Indicators

A rear Unit Identifier (JUIDB1) button and a UID LED (UID_LED1) are located next to the VGA port on the IO back panel. When you press the rear UID button, rear UID LED will be turned on. Press the UID button again to turn off the LED indicator. The UID indicator provides easy identification of a system unit that may be in need of service.

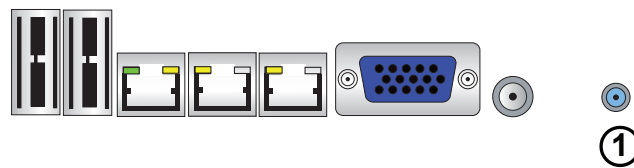
 **Note:** UID can also be triggered via IPMI on the motherboard. For more information on IPMI, please refer to the IPMI user's guide posted on our website @ <http://www.supermicro.com>.

UID Switch	
Pin#	Definition
1	Ground
2	Ground
3	Button In
4	Button In

UID LED Status	
Color/State	Status
Blue: On	Unit Identified



1. UID Switch
2. Rear UID LED



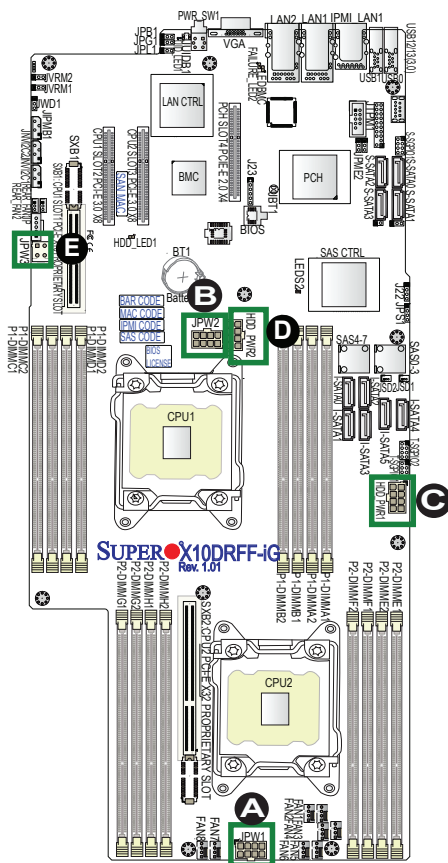
2-7 Connecting Cables

Power Connectors

Two 8-pin power connectors (JPW1/ JPW2) provide main power to the motherboard, while other two 8-pin power connectors (HDD_PWR1/ HDD_PWR2) are used to supply power to HDD devices. The 4-pin power connector (JPW3) is used as auxiliary power. These power connectors to provide adequate power to the system. See the tables on the right for pin definitions.

12V 8-pin Power Connector (JPW1/JPW2) Pin Definitions	
Pins	Definition
1 through 4	Ground
5 through 8	+12V

12V 8-pin HDD PWR Connector (HDD PWR1/2) Pin Definitions	
Pins	Definition
1-4	Ground
5 /6	+12V
7/8	+5V

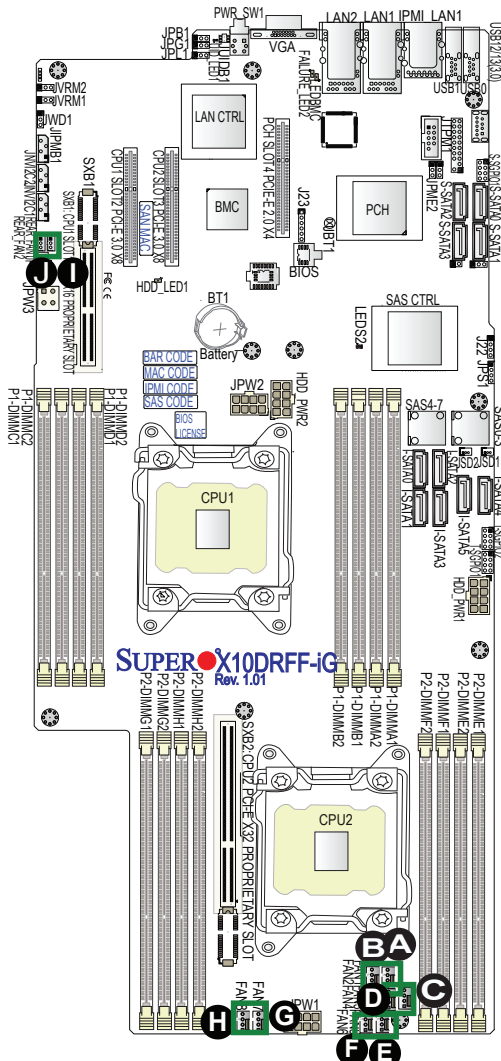


- A. JPW1 8-pin Main PWR (required)
- B. JPW2 8-pin Main PWR (required)
- C. HDD_PWR1: 8-Pin HDD PWR (required)
- D. HDD_PWR2: 8-Pin HDD PWR (required)
- E. JPW3 4-pin PWR (required)

Fan Headers

This motherboard has ten system/CPU fan headers (Fans 1-8, Rear Fan 1, and Rear Fan 2) on the motherboard. All these 4-pin fans headers are backward-compatible with the traditional 3-pin fans. However, fan speed control is available for 4-pin fans only by Thermal Management via the IPMI 2.0 interface. See the table on the right for pin definitions.

Fan Headers 1-8 Pin Definitions	
Pin#	Definition
1	Ground
2	+12V
3	Tachometer
4	PWR Modulation



- A. Fan 1
- B. Fan 2
- C. Fan 3
- D. Fan 4
- E. Fan 5
- F. Fan 6
- G. Fan 7
- H. Fan 8
- I. Rear Fan 1 (Proprietary)
- J. Rear Fan 2 (Proprietary)

TPM/Port 80 Header

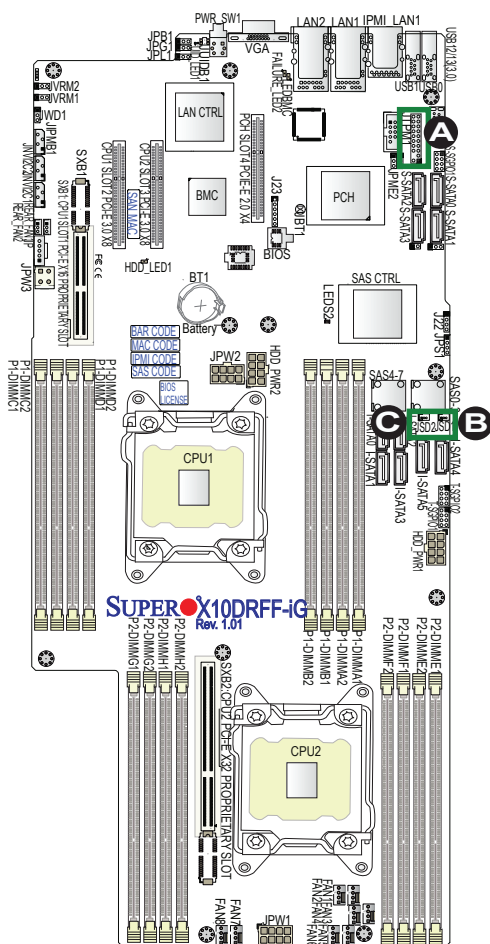
A Trusted Platform Module (TPM)/Port 80 header is located at JTPM1 to provide TPM support and Port 80 connections. Use this header to enhance system performance and data security. See the table on the right for pin definitions.

TPM/Port 80 Header Pin Definitions			
Pin #	Definition	Pin #	Definition
1	LCLK	2	GND
3	LFRAME#	4	<(KEY)>
5	LRESET#	6	No Connection
7	LAD 3	8	LAD 2
9	+3.3V	10	LAD1
11	LAD0	12	GND
13	No Connection	14	No Connection
15	+3V STBY	16	SERIRQ
17	GND	18	CLKRUN#
19	LPCPD#	20	No Connection

DOM Power Connectors

Two power connectors for SATA DOM (Disk_On_Module) devices are located at JSD1/JSD2. Connect appropriate cables here to provide power support for your Serial Link DOM devices.

DOM PWR Pin Definitions	
Pin#	Definition
1	+5V
2	Ground
3	Ground



A. TPM/Port 80 Header

B. JSD1

C. JSD2

Power SMB (I²C) Connector

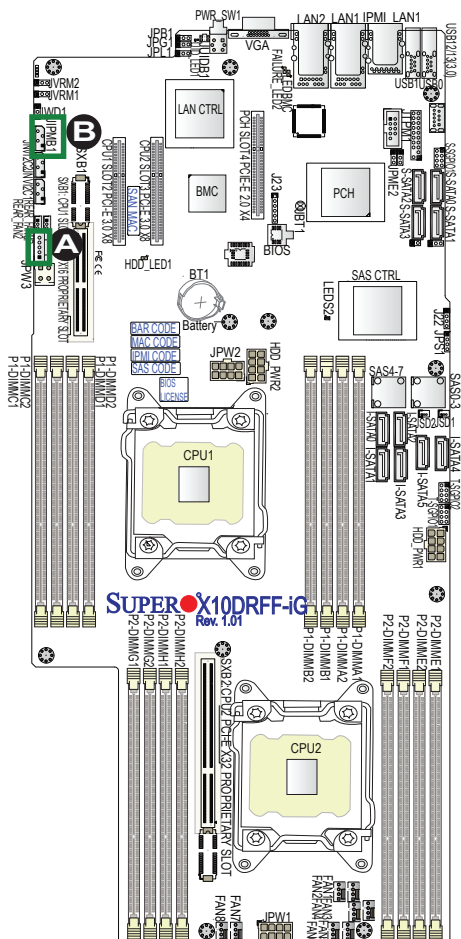
The Power System Management Bus (I²C) connector (JPI²C1) monitors power supply, fan, and system temperatures. See the table on the right for pin definitions.

PWR SMB Pin Definitions	
Pin#	Definition
1	SMBCLK_PMB
2	SMBDAT_PMB
3	PMBus_Alert
4	SMBCLK_HS
5	SMBDAT_HS

IPMB

A System Management Bus header for IPMI 2.0 is located at JIPMB1. Connect the appropriate cable here to use the IPMB I²C connection on your system.

IPMB Header Pin Definitions	
Pin#	Definition
1	Data
2	Ground
3	Clock
4	No Connection



A. JPI²C1

B. JIPMB1

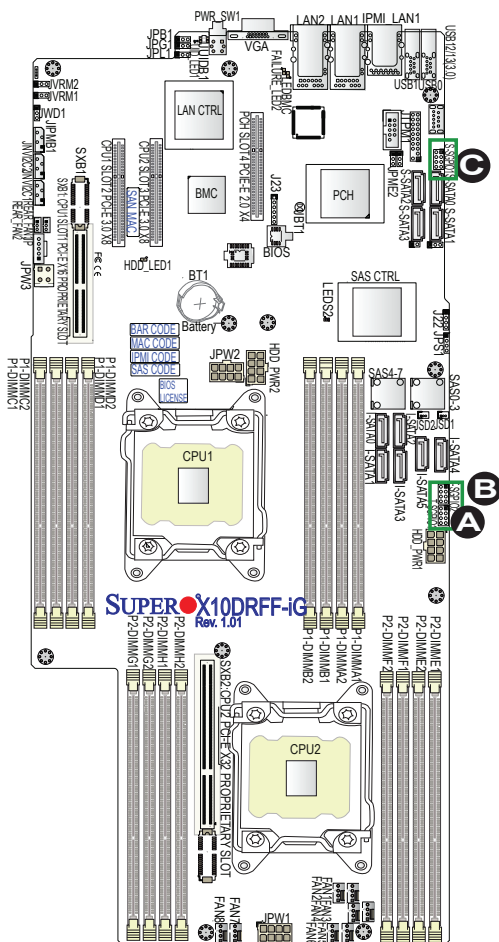
T-SGPIO 1/2 & S-SGPIO 1 Headers

Three SGPIO (Serial Link General Purpose Input/Output) headers are located on the motherboard. T-SGPIO1/2 support onboard I-SATA 0-5, while S-SGPIO1 supports S-SATA 0-3 connections. See the tables on the right for SGPIO support and for pin definitions of the connectors.

T-SGPIO1/2 & S-SGPIO2 Headers Pin Definitions			
Pin#	Definition	Pin#	Definition
2	NC	1	NC
4	Ground	3	Data
6	Load	5	Ground
8	Clock	7	NC

Note: NC= No Connection

I-SGPIO 0/1	
T-SGPIO1	I-SATA 3.0 ports 0-3 Supported
T-SGPIO2	I-SATA 3.0 ports 4/5 Supported
S-SGPIO1	S-SATA 3.0 ports 0-3 Supported

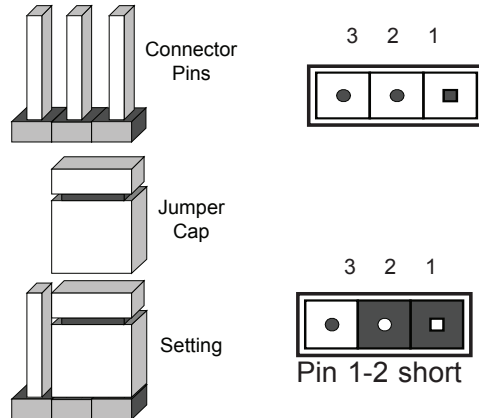



- A. T-SGPIO1
- B. T-SGPIO2
- C. S-SGPIO1

2-8 Jumper Settings

Explanation of Jumpers

To modify the operation of the motherboard, jumpers can be used to choose between optional settings. Jumpers create shorts between two pins to change the function of the connector. Pin 1 is identified with a square solder pad on the printed circuit board. See the motherboard layout pages for jumper locations.

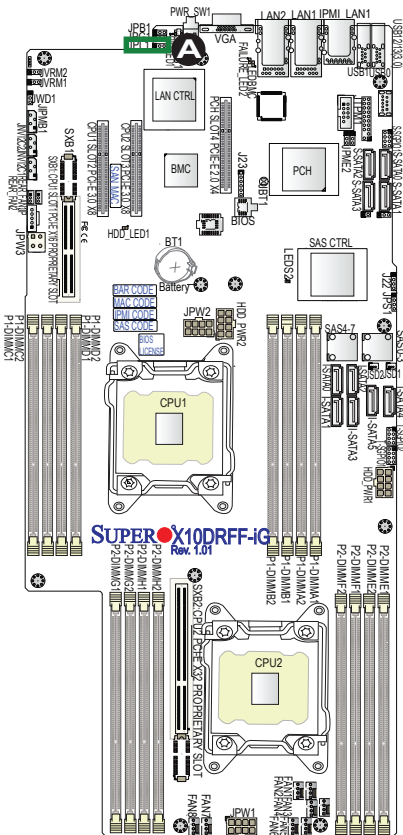


 **Note:** On two pin jumpers, "closed" means the jumper is on the pins, and "open" means the jumper is off them.

LAN Enable/Disable

JPL1 enables or disables GbE LAN ports 1/2 on the X10DRFF-iG/CG and 10GbE LAN ports 1/2 on the X10DRFF-iTG/CTG. See the table on the right for jumper settings. The default setting is Enabled.

LAN Enable Jumper Settings	
Jumper Setting	Definition
Pins 1-2	Enabled (default)
Pins 2-3	Disabled



- A. GbE LAN1/2 Enable (X10DRFF-CG/i)
- A. 10GbE LAN (TLAN) 1/2 Enable (X10DRFF-iTG/CTG)

CMOS Clear

JBT1 is used to clear the CMOS. Instead of pins, this "jumper" consists of contact pads to prevent accidental clearing of the CMOS. To clear the CMOS, use a metal object such as a small screwdriver to touch both pads at the same time to short the connection.



Note: Be sure to completely shut down the system, and then short JBT1 to clear the CMOS.

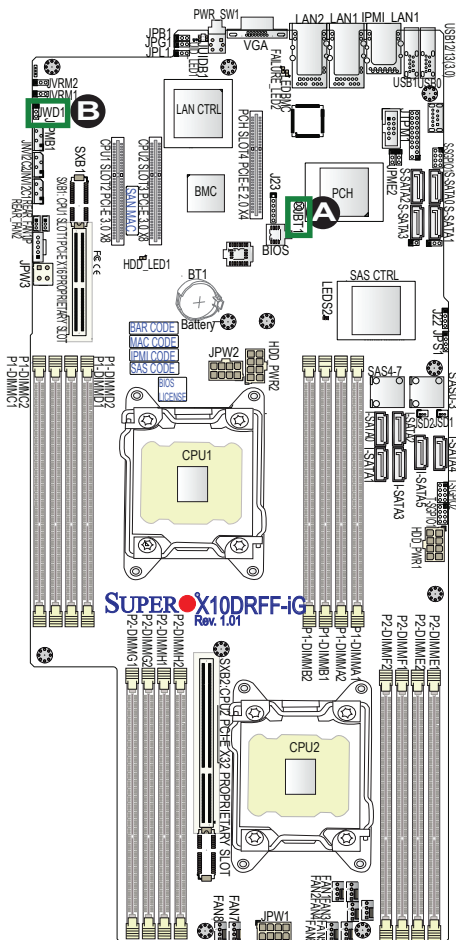
Watch Dog Enable/Disable

The Watch Dog (JWD1) is a system monitor that will reboot the system when a software application hangs. Close pins 1-2 to reset the system if an application hangs. Close pins 2-3 to generate a non-maskable interrupt signal for the application that hangs. See the table on the right for jumper settings. The Watch Dog must also be enabled in the BIOS.

Watch Dog Jumper Settings	
Jumper Setting	Definition
Pins 1-2	Reset (default)
Pins 2-3	NMI
Open	Disabled

A. Clear CMOS

B. Watch Dog Enable



VGA Enable

Jumper JPG1 allows the user to enable the onboard VGA connector. The default setting is on pins 1-2 to enable the connection. See the table on the right for jumper settings.

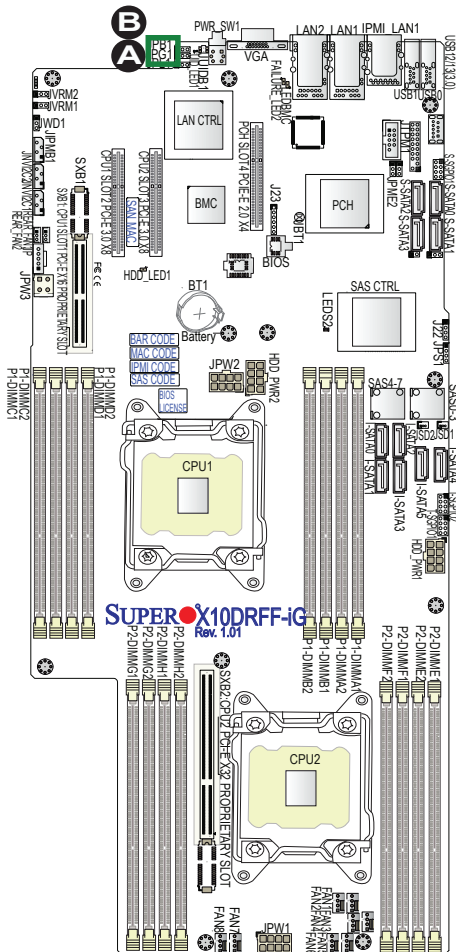
VGA Enable Jumper Settings	
Jumper Setting	Definition
Pins 1-2	Enabled (Default)
Pins 2-3	Disabled

BMC Enable

Jumper JPB1 allows you to enable the embedded ASpeed AST2400 Baseboard Management Controller (BMC) to provide IPMI 2.0/KVM support on the motherboard. The manufacturer default setting is on pins 1-2 to enable BMC. For proper hardware monitoring and fan speed control, do not disable JPB1.

BMC Enable Jumper Settings	
Jumper Setting	Definition
Pins 1-2	BMC Enable (Manufacture Default)
Pins 2-3	Disable (Do not use this setting!)

Warning! Jumper JPB1 (BMC Enable) is for engineering debugging only. The manufacturer default setting is on pins 1-2 to enable BMC. Do not change the default setting for your hardware monitoring and system health management to work properly.



- A. [VGA Enabled](#)
- B. [BMC Enabled](#)

Manufacturer Mode Select

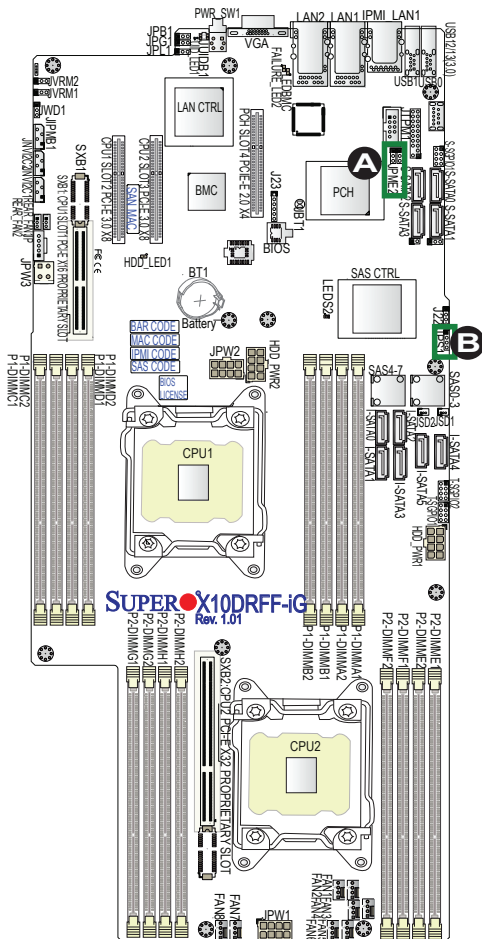
Close pin 2 and pin 3 of Jumper JPME2 to bypass SPI flash security and force the system to operate in the manufacturer mode, which will allow the user to flash the system firmware from a host server for system setting modifications. See the table on the right for jumper settings.

ME Mode Select Jumper Settings	
Jumper Setting	Definition
Pins 1-2	Normal (Default)
Pins 2-3	Manufacture Mode

SAS Enable (X10DRFF-CG/CTG only)

Jumper JPS1 allows the user to enable the onboard SAS connections. The default setting is 1-2 (enabled). See the table on the right for jumper settings.

SAS Enable Jumper Settings	
Jumper Setting	Definition
Pins 1-2	Enabled (Default)
Pins 2-3	Disabled



- A. ME Select
- B. SAS Enable

2-9 Onboard LED Indicators

GLAN LEDs

Two LAN ports (LAN 1 and LAN 2) are located on the IO back panel of the motherboard. Each Ethernet LAN port has two LEDs. The green LED indicates activity, while the other Link LED may be green, amber, or off to indicate the speed of the connection. See the tables at right for more information.

IPMI-Dedicated LAN LEDs

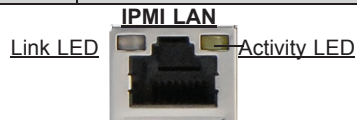
In addition to LAN 1 and LAN 2, an IPMI LAN is also located on the I/O back panel. The amber LED on the right indicates activity, while the green LED on the left indicates the speed of the connection. See the table at right for more information.



Rear View (when facing the rear side of the chassis)

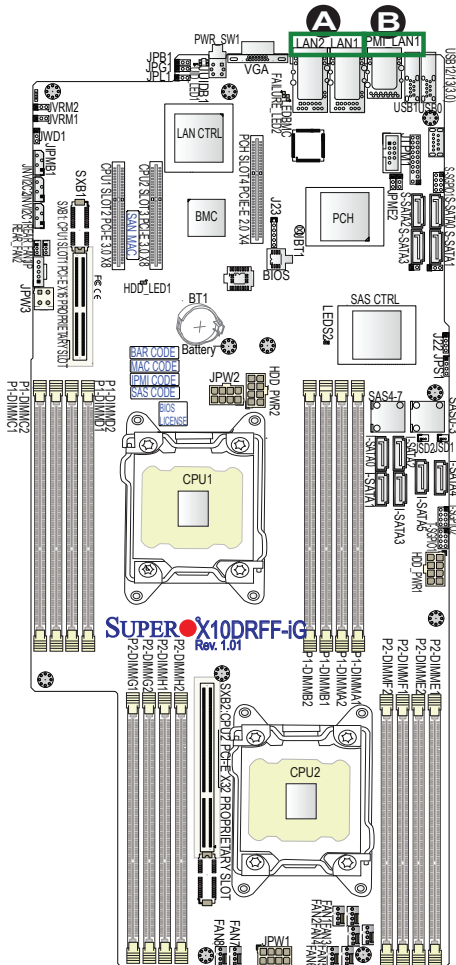
LAN 1/LAN 2 Activity LED (Right) LED State		
Color	Status	Definition
Green	Flashing	Active

LAN 1/LAN 2 Link LED (Left) LED State	
LED Color	Definition
Off	10 Mbps, 100 Mbps, or No Connection
Green	10 Gbps (X10DRFF-iTG/CTG only)
Amber	1 Gbps



IPMI LAN Link LED (Left) & Activity LED (Right)		
	Color/State	Definition
Link (Left)	Green: Solid Amber: Solid	100 Mbps 1 Gbps
Activity (Right)	Amber: Blinking	Active

- A. LAN1/2 LEDs
- B. IPMI LAN LEDs



HDD Activity LED

The HDD Activity LED is located at HDD_LED1 on the motherboard. When HDD_LED1 is blinking, HDD is active. See the table on the right for more information.

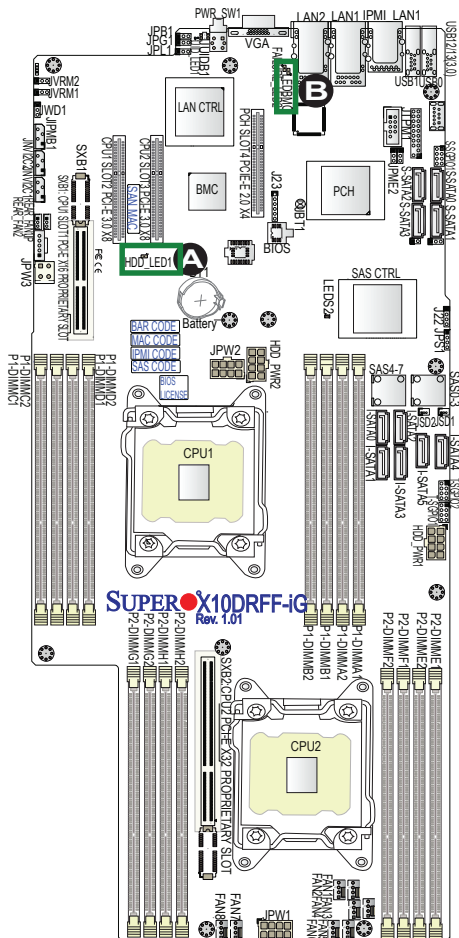
HDD Activity LED Status	
Color/State	Definition
Green: Blinking	HDD: Active

BMC Heartbeat LED

A BMC Heartbeat LED is located at LED-BMC on the motherboard. When LED-BMC is blinking, the BMC is normal. See the table at right for more information.

BMC Heartbeat LED States	
Color/State	Definition
Green: Blinking	BMC: Normal

 **Note:** Refer to Page 2-19 for information on the rear UID LED (LE1).



A. HDD_Activity_LED

B. BMC_LED

SAS Heartbeat LED (For X10DRFF-CG/CTG)

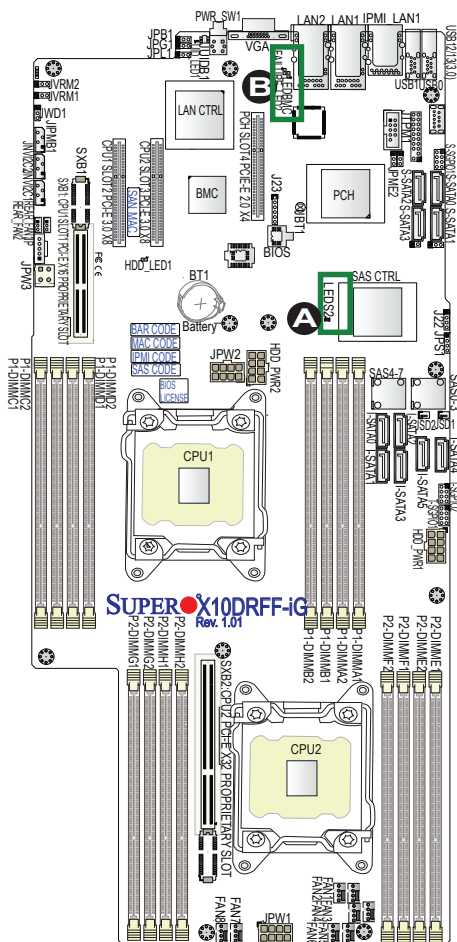
The SAS Heartbeat LED is located at LEDS2 on the motherboard. When LEDS2 is blinking, the SAS drive supported by the LSI 3008 controller works properly. See the table on the right for more information.

SAS Heartbeat LED Status	
Color/State	Definition
Green: Blinking	SAS: Normal

Motherboard Fault LED

The motherboard Fault LED is located at Failure_LED2 on the motherboard. When Failure_LED2 is on or blinking, an error has occurred to the motherboard. See the table on the right for more information.

Motherboard Fault LED Status	
State	Definition
Solid: On	MB overheating
Blinking@1Hz	Fan Failure
Blinking@0.25Hz	Power Supply Failure




- A. SAS Heartbeat LED
- B. Motherboard Fault LED

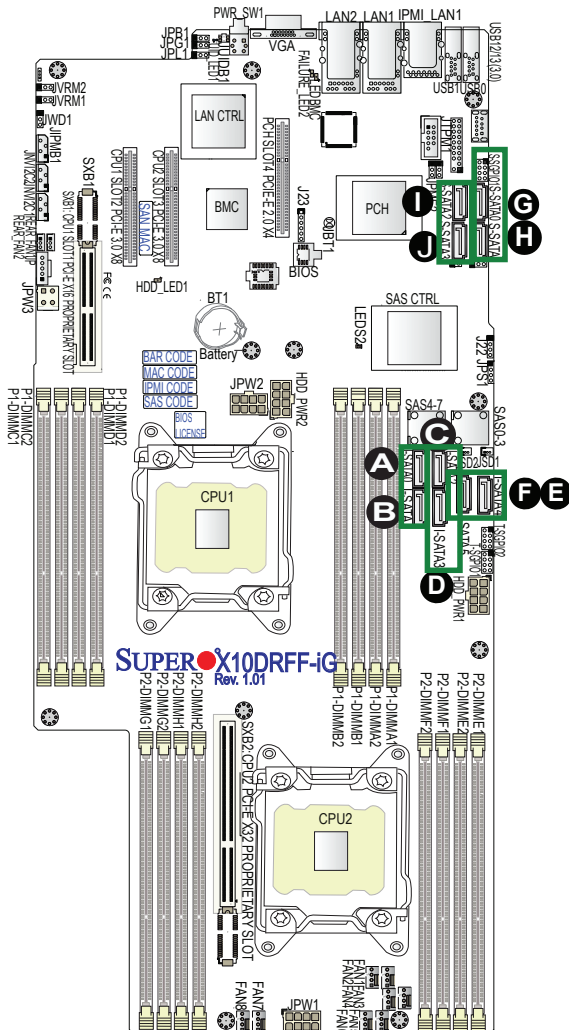
2-10 SATA/SAS Connections

SATA 3.0 Connections

Ten SATA 3.0 connections (I-SATA0-5, S-SATA0-3) are located on the motherboard. All these SATA 3.0 ports are supported by the Intel PCH C612. I-SATA4/5, the yellow connectors with power pins built-in, are used with Supermicro SuperDOM (Disk-on-Module) connectors, and do not require external power cables. SuperDOMs are backward-compatible with regular SATA HDDs and SATA DOMs that require external power cables. All SATA ports provide serial-link signal connections, which are faster than the connections of Parallel ATA. See the table on the right for pin definitions.. See the table on the right for pin definitions.

SATA Pin Definitions	
Pin#	Definition
1	Ground
2	TX_P
3	TX_N
4	Ground
5	RX_N
6	RX_P
7	Ground

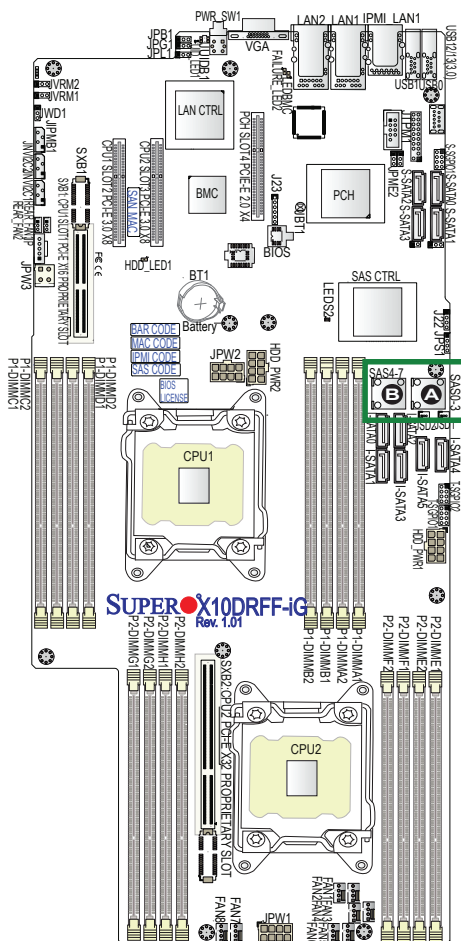
 **Note:** For more information on the SATA HostRAID configuration, please refer to the Intel SATA HostRAID user's guide posted on our website at <http://www.supermicro.com>..



- A. I-SATA0
- B. I-SATA1
- C. I-SATA2
- D. I-SATA3
- E. I-SATA4
- F. I-SATA5
- G. S-SATA0
- H. S-SATA1
- I. S-SATA2
- J. S-SATA3

SAS Ports (X10DRFF-CG/CTG Only)

Eight SAS ports, supported by the LSI 3008 SAS controller, are located at X10DRFF-CG/CTG. SAS 0-3 and SAS 4-7 support RAID 0, 1,10 from the LSI 3008 SAS controller. See the layout below for the locations of SAS connectors.



A. SAS0-3

B. SAS4-7

Chapter 3

Troubleshooting

3-1 Troubleshooting Procedures

Use the following procedures to troubleshoot your system. If you have followed all of the procedures below and still need assistance, refer to the "Technical Support Procedures" and/or "Returning Merchandise for Service" section(s) in this chapter.

Note: Always disconnect the power cord before adding, changing, or installing any hardware components.

Before Power On

1. Make sure that there are no short circuits between the motherboard and chassis.
2. Disconnect all ribbon/wire cables from the motherboard, including those for the keyboard and mouse.
3. Remove all add-on cards.
4. Install CPU 1 first (making sure it is fully seated) and connect the front panel connectors to the motherboard.

No Power

1. Make sure that there are no short circuits between the motherboard and the chassis.
2. Make sure that the ATX power connectors are properly connected.
3. Check that the 115V/230V switch, if available, on the power supply is properly set.
4. Turn the power switch on and off to test the system, if applicable.
5. The battery on your motherboard may be old. Check to verify that it still supplies ~3VDC. If it does not, replace it with a new one.

No Video

If the power is on but you have no video, remove all add-on cards and cables.

System Boot Failure

If the system does not display POST or does not respond after the power is turned on, check the following:

1. Clear the CMOS settings by unplugging the power cord and contacting both pads on the CMOS Clear Jumper (JBT1). (Refer to Section 2-8 in Chapter 2.)
2. Turn on the system with only one DIMM module installed. If the system boots, check for bad DIMM modules or slots by following the Memory Errors Troubleshooting procedure in this Chapter.

Losing the System's Setup Configuration

1. Make sure that you are using a high-quality power supply. A poor-quality power supply may cause the system to lose the CMOS setup information. Refer to Section 2-7 for details on recommended power supplies.
2. The battery on your motherboard may be old. Check to verify that it still supplies ~3VDC. If it does not, replace it with a new one.
3. If the above steps do not fix the setup configuration problem, contact your vendor for repairs.

Memory Errors

When a no-memory beep code is issued by the system, check the following:


1. Make sure that the memory modules are compatible with the system and that the DIMMs are properly and fully installed. (For memory compatibility, refer to the memory compatibility chart posted on our website @ <http://www.supermicro.com>.)
2. Check if different speeds of DIMMs have been installed. It is strongly recommended that you use the same RAM type and speed for all DIMMs in the system.

3. Make sure that you are using the correct type of Load Reduction (LRDIMM)/Registered (RDIMM) ECC DDR4 DIMM modules recommended by the manufacturer.
4. Check for bad DIMM modules or slots by swapping a single module among all memory slots and check the results.
5. Make sure that all memory modules are fully seated in their slots. Follow the instructions given in Section 2-5 in Chapter 2.
6. Please follow the instructions given in the DIMM population tables listed in Section 2-5 to install your memory modules.

When the System Becomes Unstable

A. If the system becomes unstable during or after OS installation, check the following:

1. CPU/BIOS support: Make sure that your CPU is supported and that you have the latest BIOS installed in your system.
2. Memory support: Make sure that the memory modules are supported by testing the modules using memtest86 or a similar utility.

 **Note:** Refer to the product page on our website @ <http://www.supermicro.com> for memory and CPU support and updates.
3. HDD support: Make sure that all hard disk drives (HDDs) work properly. Replace the bad HDDs with good ones.
4. System cooling: Check the system cooling to make sure that all heatsink fans and CPU/system fans, etc., work properly. Check the hardware monitoring settings in the BIOS to make sure that the CPU and system temperatures are within the normal range. Also check the front panel Overheat LED and make sure that it is not on.
5. Adequate power supply: Make sure that the power supply provides adequate power to the system. Make sure that all power connectors are connected. Please refer to our website for more information on the minimum power requirements.
6. Proper software support: Make sure that the correct drivers are used.

B. If the system becomes unstable before or during OS installation, check the following:

1. Source of installation: Make sure that the devices used for installation are working properly, including boot devices such as CD/DVD.
2. Cable connection: Check to make sure that all cables are connected and working properly.
3. Using the minimum configuration for troubleshooting: Remove all unnecessary components (starting with add-on cards first), and use the minimum configuration (but with a CPU and a memory module installed) to identify the trouble areas. Refer to the steps listed in Section A above for proper troubleshooting procedures.
4. Identifying bad components by isolating them: If necessary, remove a component in question from the chassis, and test it in isolation to make sure that it works properly. Replace a bad component with a good one.
5. Check and change one component at a time instead of changing several items at the same time. This will help isolate and identify the problem.
6. To find out if a component is good, swap this component with a new one to see if the system will work properly. If so, then the old component is bad. You can also install the component in question in another system. If the new system works, the component is good and the old system has problems.

3-2 Technical Support Procedures

Before contacting Technical Support, please take the following steps. Also, please note that as a motherboard manufacturer, Supermicro also sells motherboards through its channels, so it is best to first check with your distributor or reseller for troubleshooting services. They should know of any possible problems with the specific system configuration that was sold to you.

1. Please go through the Troubleshooting Procedures and Frequently Asked Questions (FAQ) sections in this chapter or see the FAQs on our website (<http://www.supermicro.com/>) before contacting Technical Support.
2. BIOS upgrades can be downloaded from our website (<http://www.supermicro.com>).
3. If you still cannot resolve the problem, include the following information when contacting Supermicro for technical support:

- Motherboard model and PCB revision number
 - BIOS release date/version (This can be seen on the initial display when your system first boots up.)
 - System configuration
4. An example of a Technical Support form is on our website at <http://www.supermicro.com/RmaForm/>.
- Distributors: For immediate assistance, please have your account number ready when placing a call to our Technical Support department. We can be reached by email at support@supermicro.com.

3-3 Battery Removal and Installation

Battery Removal

To remove the onboard battery, follow the steps below:

1. Power off your system and unplug your power cable.
2. Locate the onboard battery as shown below.
3. Using a tool such as a pen or a small screwdriver, push the battery lock outwards to unlock it. Once unlocked, the battery will pop out from the holder.
4. Remove the battery.

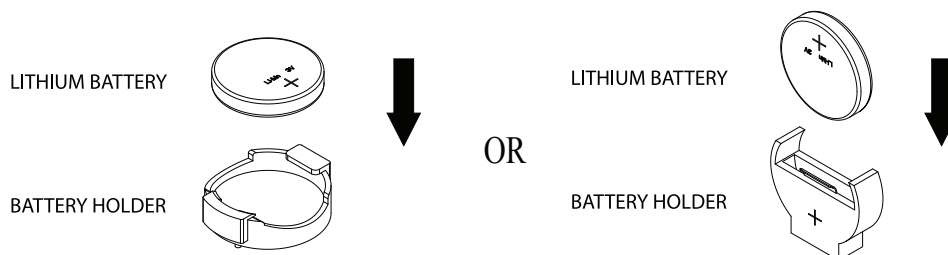
Proper Battery Disposal


Warning! Please handle used batteries carefully. Do not damage the battery in any way; a damaged battery may release hazardous materials into the environment. Do not discard a used battery in the garbage or a public landfill. Please comply with the regulations set up by your local hazardous waste management agency to dispose of your used battery properly.

Battery Installation

To install an onboard battery, follow the steps below:

1. Power off your system and unplug your power cable.
2. Locate the onboard battery as shown below.
3. Identify the battery's polarity. The positive (+) side should be facing up.
4. Insert the battery into the battery holder and push it down until you hear a click to ensure that the battery is securely locked.



 **Note:** When replacing a battery, be sure to only replace it with the same type.

3-4 Frequently Asked Questions

Question: What are the various types of memory that my motherboard can support?

Answer: The motherboard supports Load Reduction (LRDIMM)/Registered (RDIMM) ECC DDR4 DIMM modules. To enhance memory performance, do not mix memory modules of different speeds and sizes. Please follow all memory installation instructions given on Section 2-5 in Chapter 2.

Question: How do I handle the used battery?

Answer: Please handle used batteries carefully. Do not damage the battery in any way; a damaged battery may release hazardous materials into the environment. Do not discard a used battery in the garbage or a public landfill. Please comply with the regulations set up by your local hazardous waste management agency to dispose of your used battery properly.

Question: How do I update my BIOS under DOS?


It is recommended that you **do not** upgrade your BIOS if you are not experiencing any problems with your system. Updated BIOS files are located on our website at <http://www.supermicro.com>. Please check our BIOS warning message and the information on how to update your BIOS on our website. Select your motherboard model and download the BIOS file to your computer. Also, check the current BIOS revision to make sure that it is newer than your BIOS before downloading. You can choose from the zip file and the .exe file. If you choose the zip BIOS file, please unzip the BIOS file onto a bootable USB device. Run the batch file using the format Flash.bat filename.rom from your bootable USB device to flash the BIOS. Then, your system will automatically reboot.

Warning: Do not shut down or reset the system while updating the BIOS to prevent possible system boot failure!)




Note: The SPI BIOS chip used on this motherboard cannot be removed. Send your motherboard back to our RMA Department at Supermicro for repair. For BIOS Recovery instructions, please refer to the AMI BIOS Recovery Instructions posted at <http://www.supermicro.com>.

Question: How do I update my BIOS under UEFI Shell?

 **Note:** We do not recommend that you update your BIOS if you are not experiencing a BIOS-related problem. If you need to update your BIOS, please follow the steps below to properly update your BIOS under UEFI Shell.

1. Download and save the BIOS update package to your computer
2. Extract the files from the UEFI folder of the BIOS package to a USB stick.

 **Note:** The USB stick doesn't have to be bootable; however, it has to be formatted with the FAT/FAT32 file system.

3. Insert the USB stick into a USB port, boot to the UEFI Built-In Shell, and type the following commands to start the BIOS update:

```
Shell> fs0:
```

```
fs0:\> cd UEFI
```

```
fs0:\UEFI> flash.nsh BIOSname#.###
```

4. The FLASH.NSH script will compare the Flash Descriptor Table (FDT) code in the new BIOS with the existing one in the motherboard:

a. If a different FDT is found

- A new file, STARTUP.NSH, will be created, and the system will automatically reboot in 10 seconds without you pressing any key. BIOS will be updated after the system reboots.
- You can also press <Y> to force an immediate system reboot to shorten the process. During system reboot, press the <F11> key to invoke the boot menu and boot into the build-in UEFI Shell. Your BIOS will be updated automatically.

b. If the FDT is the same

- BIOS update will be immediately performed without a system reboot initiated.

Warning: Do not shut down or reset the system while updating the BIOS to prevent possible system boot failure!)

5. Perform an A/C power cycle after the message indicating the BIOS update has completed.
6. Go to the BIOS setup utility, and restore the BIOS settings.

3-5 Returning Merchandise for Service

A receipt or copy of your invoice marked with the date of purchase is required before any warranty service will be rendered. You can obtain service by calling your vendor for a Returned Merchandise Authorization (RMA) number. When returning the motherboard to the manufacturer, the RMA number should be prominently displayed on the outside of the shipping carton, and the shipping package is mailed prepaid or hand-carried. Shipping and handling charges will be applied for all orders that must be mailed when service is complete. For faster service, You can also request a RMA authorization online (<http://www.supermicro.com/RmaForm/>).

This warranty only covers normal consumer use and does not cover damages incurred in shipping or from failure due to the alternation, misuse, abuse or improper maintenance of products.


During the warranty period, contact your distributor first for any product problems.

Chapter 4

BIOS


4-1 Introduction

This chapter describes the AMI BIOS setup utility for the X10DRFF-iG/iTG/CG/CTG. The ROM BIOS is stored in a Flash EEPROM and can be easily updated. This chapter describes the basic navigation of the AMI BIOS setup utility screens.


 **Note:** For AMI BIOS recovery, please refer to the UEFI BIOS Recovery Instructions in Appendix C.

Starting BIOS Setup Utility


To enter the AMI BIOS setup utility screens, press the <Delete> key while the system is booting up.

 **Note:** In most cases, the <Delete> key is used to invoke the AMI BIOS setup screen. There are a few cases when other keys are used, such as <F1>, <F2>, etc.

Each main BIOS menu option is described in this manual. The AMI BIOS setup menu screen has two main frames. The left frame displays all the options that can be configured. Grayed-out options cannot be configured. Options in blue can be configured by the user. The right frame displays the key legend. Above the key legend is an area reserved for a text message. When an option is selected in the left frame, it is highlighted in white. Often a text message will accompany it.

 **Note:** the AMI BIOS has default text messages built in. Supermicro retains the option to include, omit, or change any of these text messages.

The AMI BIOS setup utility uses a key-based navigation system called "hot keys." Most of the AMI BIOS setup utility "hot keys" can be used at any time during the setup navigation process. These keys include <F1>, <F4>, <Enter>, <Esc>, arrow keys, etc.

 **Note:** Options printed in **Bold** are default settings.

How To Change the Configuration Data

The configuration data that determines the system parameters may be changed by entering the AMI BIOS setup utility. This setup utility can be accessed by pressing at the appropriate time during system boot.

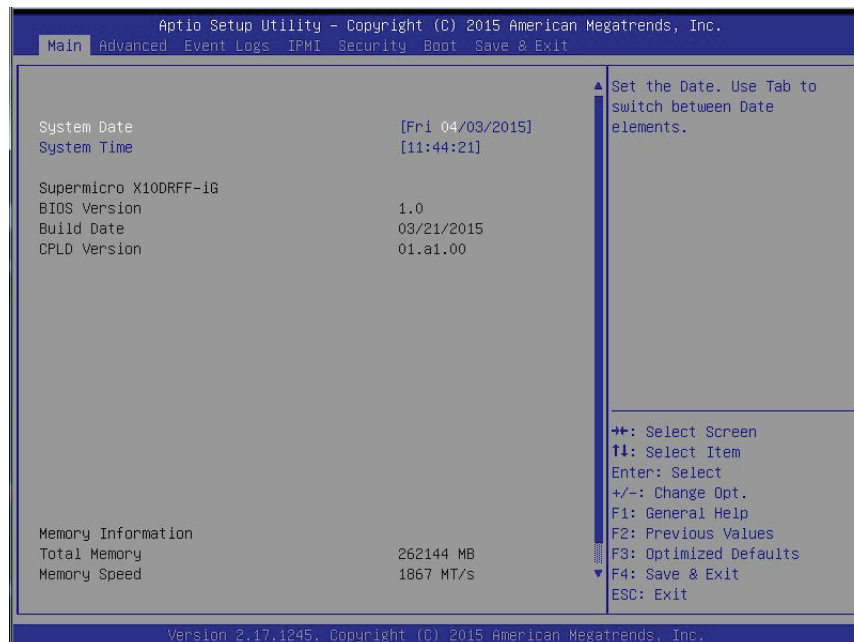
How to Start the Setup Utility

Normally, the only visible Power-On Self-Test (POST) routine is the memory test. As the memory is being tested, press the <Delete> key to enter the main menu of the AMI BIOS setup utility. From the main menu, you can access the other setup screens. An AMI BIOS identification string is displayed at the left bottom corner of the screen, below the copyright message.

Warning: Do not upgrade the BIOS unless your system has a BIOS-related issue. Flashing the wrong BIOS can cause irreparable damage to the system. In no event shall Supermicro be liable for direct, indirect, special, incidental, or consequential damages arising from a BIOS update. If you have to update the BIOS, do not shut down or reset the system while the BIOS is updating to avoid possible boot failure.

4-2 Main Setup

When you first enter the AMI BIOS setup utility, you will enter the Main setup screen. You can always return to the Main setup screen by selecting the Main tab on the top of the screen. The Main BIOS setup screen is shown below.



The following Main menu items will be displayed:

System Date/System Time

Use this option to change the system date and time. Highlight *System Date* or *System Time* using the arrow keys. Enter new values using the keyboard. Press the <Tab> key or the arrow keys to move between fields. The date must be entered in Day MM/DD/YYYY format. The time is entered in HH:MM:SS format.



Note: The time is in the 24-hour format. For example, 5:30 P.M. appears as 17:30:00.

Supermicro X10DRFF-iG

BIOS Version: This item displays the version of the BIOS ROM used in the system.

Build Date: This item displays the date when the version of the BIOS ROM used in the system was built.

CPLD Version: This item displays the version of the complex programmable logic device (CPLD) utility used in the system.

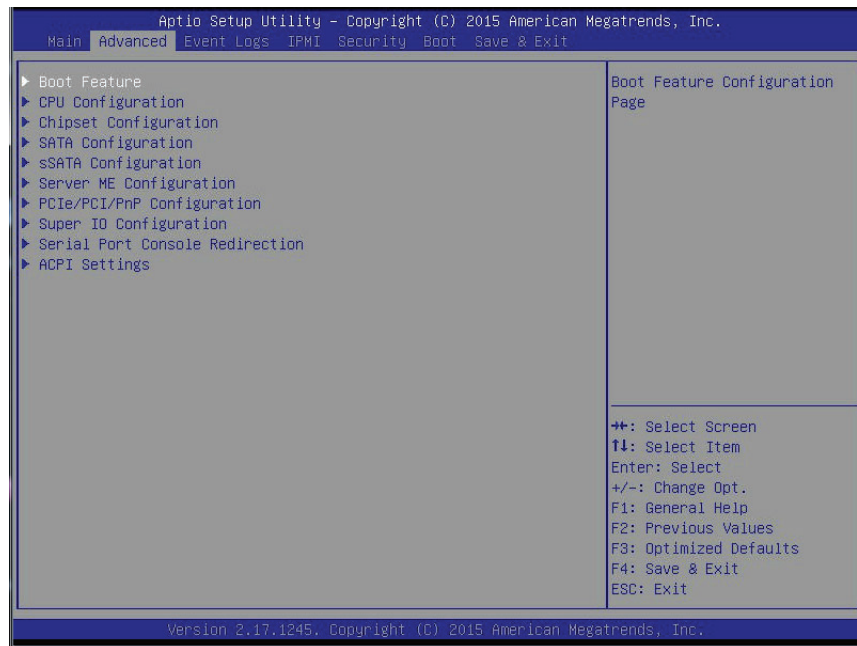
Memory Information

Total Memory: This item displays the total size of memory available in the system.

Memory Speed: This item displays the default speed of the memory modules installed in the system.

4-3 Advanced Setup Configurations

Use the arrow keys to select Advanced setup and press <Enter> to access the submenu items:



Warning: Take Caution when changing the Advanced settings. An incorrect value, an improper DRAM frequency, or a wrong BIOS timing setting may cause the system to malfunction. When this occurs, restore the setting to the manufacturer default setting.

►Boot Feature

Quiet Boot

Use this item to select the screen display between POST messages or the OEM logo at bootup. Select Disabled to display the POST messages. Select Enabled to display the OEM logo instead of the normal POST messages. The options are **Enabled** and Disabled.

AddOn ROM Display Mode

Use this item to set the display mode for the Option ROM. Select Keep Current to use the current AddOn ROM display setting. Select Force BIOS to use the Option ROM display mode set by the system BIOS. The options are **Force BIOS** and Keep Current.

Bootup Num-Lock State

Use this item to set the Power-on state for the Numlock key. The options are **On** and Off.

Wait For 'F1' If Error

Select Enabled to force the system to wait until the 'F1' key is pressed if an error occurs. The options are Disabled and **Enabled**.

INT19 (Interrupt 19) Trap Response

Interrupt 19 is the software interrupt that handles the boot disk function. When this item is set to Immediate, the ROM BIOS of the host adaptors will "capture" Interrupt 19 at bootup immediately and allow the drives that are attached to these host adaptors to function as bootable disks. If this item is set to Postponed, the ROM BIOS of the host adaptors will not capture Interrupt 19 immediately and allow the drives attached to these adaptors to function as bootable devices at bootup. The options are **Immediate** and Postponed.

Re-try Boot

When EFI Boot is selected, the system BIOS will automatically reboot the system from an EFI boot device after its initial boot failure. Select Legacy Boot to allow the BIOS to automatically reboot the system from a Legacy boot device after its initial boot failure. The options are **Disabled**, Legacy Boot, and EFI Boot.

Power Configuration**Watch Dog Function**

Select Enabled to allow the Watch Dog timer to reboot the system when it is inactive for more than 5 minutes. The options are Enabled and **Disabled**.

Power Button Function

This feature controls how the system shuts down when the power button is pressed. Select 4 Seconds Override for the user to power off the system after pressing and holding the power button for 4 seconds or longer. Select Instant Off to instantly power off the system as soon as the user presses the power button. The options are 4 Seconds Override and **Instant Off**.

Restore on AC Power Loss

Use this item to set the power state after a power outage. Select Power-Off for the system power to remain off after a power loss. Select Power-On for the system power to be turned on after a power loss. Select Last State to allow the system to resume its last power state before a power loss. The options are Power-On, Stay-Off and **Last State**.

► CPU Configuration

This submenu displays the following CPU information as detected by the BIOS. It also allows the user to configure CPU settings.

- Processor Socket
- Processor ID
- Processor Frequency
- Processor Max Ratio
- Processor Min Ratio
- Microcode Revision
- L1 Cache RAM
- L2 Cache RAM
- L3 Cache RAM
- CPU 1 Version
- CPU 2 Version

Clock Spread Spectrum

Select Enabled to allow the BIOS to monitor and attempt to reduce the level of Electromagnetic Interference caused by the components whenever needed. The options are **Disabled** and Enabled.

Hyper-Threading (All)

Select Enable to support Intel's Hyper-threading Technology to enhance CPU performance. The options are **Enable** and Disable.

Cores Enabled

This feature allows the user to set the number of CPU cores to enable. Enter "0" to enable all cores. There are 14 cores available in the system. The default setting is **0**.

Execute-Disable Bit (Available if supported by the OS & the CPU)

Select Enable for Execute Disable Bit Technology support, which will allow the processor to designate areas in the system memory where an application code can execute and where it cannot, thus preventing a worm or a virus from flooding illegal

codes to overwhelm the processor to damage the system during an attack. This feature is used in conjunction with the items: "Clear MCA," "VMX," "Enable SMX," and "Lock Chipset" for Virtualization media support. The options are **Enable** and Disable. (Refer to Intel and Microsoft websites for more information.)

PPIN Control

Select Unlock/Enable to use the Protected-Processor Inventory Number (PPIN) in the system. The options are **Unlock/Enable** and Unlock/Disable.

Hardware Prefetcher (Available when supported by the CPU)

If set to Enable, the hardware prefetcher will prefetch streams of data and instructions from the main memory to the L2 cache to improve CPU performance. The options are Disable and **Enable**.

Adjacent Cache Prefetch (Available when supported by the CPU)

Select Enable for the CPU to prefetch both cache lines for 128 bytes as comprised. Select Disable for the CPU to prefetch both cache lines for 64 bytes. The options are Disable and **Enable**.



Note: Please reboot the system for changes on this setting to take effect. Please refer to Intel's website for detailed information.

DCU (Data Cache Unit) Streamer Prefetcher (Available when supported by the CPU)

If set to Enable, the DCU Streamer Prefetcher will prefetch data streams from the cache memory to the DCU (Data Cache Unit) to speed up data accessing and processing to enhance CPU performance. The options are Disable and **Enable**.

DCU IP Prefetcher

If set to Enable, the IP prefetcher in the DCU (Data Cache Unit) will prefetch IP addresses to improve network connectivity and system performance. The options are **Enable** and Disable.

Direct Cache Access (DCA)

Select Enable to use Intel DCA (Direct Cache Access) Technology to improve the efficiency of data transferring and accessing. The options are **Auto**, Enable, and Disable.

X2APIC (Advanced Programmable Interrupt Controller)

Based on Intel's Hyper-Threading architecture, each logical processor (thread) is assigned 256 APIC IDs (APIDs) in 8-bit bandwidth. When this feature is set to Enable, the APIC ID will be expanded from 8 bits (X2) to 16 bits to provide 512 APIDs to each thread to enhance CPU performance. The options are **Disable** and Enable.

AES-NI

Select Enable to use the Intel Advanced Encryption Standard (AES) New Instructions (NI) to ensure data security. The options are Enable and **Disable**.

Intel Virtualization Technology

Select Enable to use Intel Virtualization Technology support for Direct I/O VT-d support by reporting the I/O device assignments to the VMM (Virtual Machine Monitor) through the DMAR ACPI tables. This feature offers fully-protected I/O resource sharing across Intel platforms, providing greater reliability, security and availability in networking and data-sharing. The options are **Enable** and Disable.

► Advanced Power Management Configuration

Advanced Power Management Configuration

Power Technology

Select Energy Efficient to support power-saving mode. Select Custom to customize system power settings. Select Disabled to disable power-saving settings. The options are Disable, **Energy Efficient**, and Custom.

If the option is set to Custom, the following items will display:

Energy Performance Tuning

Select Enable to support energy-performance tuning to enhance energy efficiency. The options are Enable and **Disable**.

Energy Performance Bias Setting

Use this item to select an appropriate fan setting to achieve maximum system performance (with maximum cooling) or maximum energy efficiency (with maximum power saving). The fan speeds are controlled by the firmware management via IPMI 2.0. The options are Performance, **Balanced Performance**, Balanced Power, and Power.

Energy Efficiency Turbo

Select Enable for Energy Efficiency Turbo Support to tune up CPU core frequency to improve CPU performance without compromising energy efficiency. The options are **Enable**, and Disable.

► CPU P State Control (Available when Power Technology is set to Custom)

EIST (P-states)

EIST (Enhanced Intel SpeedStep Technology) allows the system to automatically adjust processor voltage and core frequency to reduce power consumption and heat dissipation. The options are Disable and **Enable**.

Turbo Mode

Select Enable to use the Turbo Mode to boost system performance. The options are **Enable** and Disable.

P-state Coordination

Use this item to change the P-state (Power-Performance State) coordination type. P-state is also known as "SpeedStep" for Intel processors. Select HW_ALL to change the P-state coordination type for hardware components only. Select SW_ALL to change the P-state coordination type for all software installed in the system. Select SW_ANY to change the P-state coordination type for a software program in the system. The options are **HW_All**, SW_ALL, and SW_ANY.

► CPU C State Control (Available when Power Technology is set to Custom)

Package C State limit

Use this item to set the limit on the C-State package register. The options are C0/1 state, C2 state, C6 (non-Retention) state, and **C6 (Retention) state**.

CPU C3 Report

Select Enable to allow the BIOS to report the CPU C3 State (ACPI C2) to the operating system. During the CPU C3 State, the CPU clock generator is turned off. The options are Enable and **Disable**.

CPU C6 Report (Available when Power Technology is set to Custom)

Select Enable to allow the BIOS to report the CPU C6 state (ACPI C3) to the operating system. During the CPU C6 state, power to all cache is turned off. The options are **Enable** and Disable.

Enhanced Halt State (C1E)

Select Enabled to use Enhanced Halt-State technology, which will significantly reduce the CPU's power consumption by reducing the CPU's clock cycle and voltage during a Halt-state. You will need to reboot the system for the change of this setting to take effect. The options are Disable and **Enable**.

► CPU T State Control (Available when Power Technology is set to Custom)

ACPI (Advanced Configuration Power Interface) T-States

Select Enable to support CPU throttling by the operating system to reduce power consumption. The options are **Enable** and Disable.

► Chipset Configuration

Warning! Please set the correct settings for the items below. A wrong configuration setting may cause the system to malfunction.

► North Bridge

This feature allows the user to configure the settings for the Intel North Bridge.

► IIO Configuration

EV DFX (Device Function On-Hide) Features

When this item is set to Enable, the EV_DFX Lock Bits that are located on a processor will always remain clear during electric tuning. The options are **Disable** and Enable.

► IIO1 Configuration

IOU2 (IIO1 PCIe Port 1)

This item configures the PCI-E port Bifuraction setting for a PCI-E port specified by the user. The options are x4x4, x8, and **Auto**.

IIO1 PORT 1A Link Speed

This item configures the link speed of a PCI-E port specified by the user. The options are Gen 1 (Generation 1) (2.5 GT/s), Gen 2 (Generation 2) (5 GT/s), and **Gen 3 (Generation 3) (8 GT/s)**.

IOU0 (IIO1 PCIe Port 2)

This item configures the PCI-E port Bifuraction setting for a PCI-E port specified by the user. The options are x4x4x4x4, x4x4x8, x8x4x4, x8x8, x16, and **Auto**.

RSC-R4UFF-A2E16A SLOT1 Link Speed

This item configures the link speed of a PCI-E port specified by the user. The options are Gen 1 (Generation 1) (2.5 GT/s), Gen 2 (Generation 2) (5 GT/s), and **Gen 3 (Generation 3) (8 GT/s)**.

IOU1 (II0 PCIe Port 3)

This item configures the PCI-E port Bifuraction setting for a PCI-E port specified by the user. The options are x4x4x4x4, x4x4x8, x8x4x4, x8x8, x16, and **Auto**.

II01 PORT 3A Link Speed

This item configures the link speed of a PCI-E port specified by the user. The options are Gen 1 (Generation 1) (2.5 GT/s), Gen 2 (Generation 2) (5 GT/s), and **Gen 3 (Generation 3) (8 GT/s)**.

II01 PORT 3C Link Speed

This item configures the link speed of a PCI-E port specified by the user. The options are Gen 1 (Generation 1) (2.5 GT/s), Gen 2 (Generation 2) (5 GT/s), and **Gen 3 (Generation 3) (8 GT/s)**.

►II02 Configuration**IOU2 (II02 PCIe Port 1)**

This item configures the PCI-E port Bifuraction setting for a PCI-E port specified by the user. The options are x4x4, x8, and **Auto**.

II02 PORT 1A Link Speed

This item configures the link speed of a PCI-E port specified by the user. The options are Gen 1 (Generation 1) (2.5 GT/s), Gen 2 (Generation 2) (5 GT/s), and **Gen 3 (Generation 3) (8 GT/s)**.

IOU0 (II02 PCIe Port 2)

This item configures the PCI-E port Bifuraction setting for a PCI-E port specified by the user. The options are x4x4x4x4, x4x4x8, x8x4x4, x8x8, x16, and **Auto**.

IOU1 (II02 PCIe Port 3)

This item configures the PCI-E port Bifuraction setting for a PCI-E port specified by the user. The options are x4x4x4x4, x4x4x8, x8x4x4, x8x8, x16, and **Auto**.

►IOAT (Intel® IO Acceleration) Configuration**Enable IOAT**

Select Enable to enable Intel I/OAT (I/O Acceleration Technology) support, which will significantly reduce CPU overhead by leveraging CPU architectural improvements and freeing the system resource for other tasks. The options are **Enable** and Disable.

No Snoop

Select Enable to support no-snoop mode for each CB device. The options are **Disable** and Enable.

Relaxed Ordering

Select Enable to enable Relaxed Ordering support which will allow certain transactions to violate the strict-ordering rules of PCI bus for a transaction to be completed prior to other transactions that have already been enqueued earlier. The options are **Disable** and Enable.

► **Intel VT for Directed I/O (VT-d)**

Intel VT for Direct I/O (VT-d)

Intel® VT for Directed I/O (VT-d)

Select Enable to use Intel Virtualization Technology support for Direct I/O VT-d support by reporting the I/O device assignments to the VMM (Virtual Machine Monitor) through the DMAR ACPI Tables. This feature offers fully-protected I/O resource sharing across Intel platforms, providing greater reliability, security and availability in networking and data-sharing. The options are **Enable** and Disable.

Interrupt Remapping

Select Enable for Interrupt Remapping support to enhance system performance. The options are **Enable** and Disable.

► **QPI (Quick Path Interconnect) Configuration**

QPI Status

The following information will display:

- Number of CPU
- Number of IIO
- Current QPI Link Speed
- Current QPI Link Frequency
- QPI Global MMIO Low Base/Limit
- QPI Global MMIO High Base/Limit
- QPI PCIe Configuration Base/Size

Link Frequency Select

Use this item to select the desired frequency for QPI Link connections. The options are 6.4GB/s, 8.0GB/s, 9.6GB/s, **Auto**, and Auto Limited.

Link L0p Enable

Select Enable for the QPI link to enter the L0p state for power saving. The options are **Enable** and Disable.

Link L1 Enable

Select Enable for the QPI link to enter the L1 state for power saving. The options are **Enable** and Disable.

COD Enable (Available when the OS and the CPU support this feature)

Select Enabled for Cluster-On-Die support to enhance system performance in cloud computing. The options are Enable and **Disable**.

Early Snoop (Available when the OS and the CPU support this feature)

Select Enable for Early Snoop support to enhance system performance. The options are Enable, Disable, and **Auto**.

Isoc Mode

Select Enable for Isochronous support to meet QoS (Quality of Service) requirements. This feature is especially important for Virtualization Technology. The options are Enable and **Disable**.

►Memory Configuration**Enforce POR**

Select Enable to enforce POR restrictions on DDR4 frequency and voltage programming. The options are **Enabled** and Disabled.

Memory Frequency

Use this item to set the maximum memory frequency for onboard memory modules. The options are **Auto**, 1333, 1400, 1600, 1800, 1867, 2000, 2133, 2200, and 2400.

Data Scrambling

Select Enabled to enable data scrambling to enhance system performance and data integrity. The options are **Auto**, Disabled and Enabled.

Enable ADR

Use this item to configure Automatic Diagnostic Repository (ADR) settings to enhance memory performance. The options are **Disabled**, ADR + Batterybacked DIMMs, and ADR + NVDIMMs.

DRAM RAPL (Running Average Power Limit) Baseline

Use this item to set the run-time power-limit baseline for the DRAM modules. The options are Disable, DRAM RAPL Mode 0, and **DRAM RAPL Mode 1**.

Set Throttling Mode

Throttling improves reliability and reduces power consumption in processors via automatic voltage control during processor idle states. The options are Disabled and **CLTT** (Closed Loop Thermal Throttling).

A7 Mode

Select Enabled to support the A7 (Addressing) mode to improve memory performance. The options are **Enable** and Disable.

► DIMM Information

This item displays the status of a DIMM module as detected by the BIOS.

P1-DIMMA1-A2/P1-DIMMB1-B2/P1-DIMMC1-C2/P1-DIMMD1-D2/P2-DIMME1-E2/P2-DIMMF1-F2/P2-DIMMG1-G2/P2-DIMMH1-H2

► Memory RAS (Reliability_Availability_Serviceability) Configuration

Use this submenu to configure the following Memory RAS settings.

RAS Mode

When Disable is selected, RAS is not supported. When Mirror is selected, the motherboard maintains two identical copies of all data in memory for data backup. When Lockstep is selected, the motherboard uses two areas of memory to run the same set of operations in parallel to boost performance. The options are **Disable**, Mirror, and Lockstep Mode.

Memory Rank Sparing

Select Enable to enable memory-sparing support for memory ranks to improve memory performance. The options are **Disabled** and Enabled.

Patrol Scrub

Patrol Scrubbing is a process that allows the CPU to correct correctable memory errors detected on a memory module and send the correction to the requestor (the original source). When this item is set to Enable, the PCH (Platform Control Hub) will read and write-back one cache line every 16K cycles if there is no delay caused by internal processing. By using this method, roughly 64 GB of memory behind the PCH will be scrubbed every day. The options are **Enable** and Disable.

Patrol Scrub Interval

This feature allows you to decide how many hours the system should wait before the next complete patrol scrub is performed. Use the keyboard to enter a value from 0-24. The Default setting is **24**.

Demand Scrub

Demand Scrubbing is a process that allows the CPU to correct correctable memory errors found on a memory module. When the CPU or I/O issues a demand-read command, and the read data from memory turns out to be a correctable error, the error is corrected and sent to the requestor (the original source). Memory is updated as well. Select Enable to use Demand Scrubbing for ECC memory correction. The options are **Enable** and Disable.

Device Tagging

Select Enable to support device tagging. The options are **Disable** and Enable.

►South Bridge Configuration

The following South Bridge information will display:

►USB Configuration

- USB Module Version
- USB Devices

Legacy USB Support

Select Enabled to support onboard legacy USB devices. Select Auto to disable legacy support if there are no legacy USB devices present. Select Disable to have all USB devices available for EFI applications only. The options are **Enabled**, Disabled and Auto.

XHCI (Extensible Host Controller Interface) Hand-Off

This is a work-around solution for operating systems that do not support XHCI (Extensible Host Controller Interface) hand-off. The XHCI ownership change should be claimed by the XHCI driver. The settings are **Enabled** and Disabled.

EHCI (Enhanced Host Controller Interface) Hand-Off

This item is for operating systems that do not support Enhanced Host Controller Interface (EHCI) hand-off. When this item is enabled, EHCI ownership change will be claimed by the EHCI driver. The settings are Enabled and **Disabled**.

Port 60/64 Emulation

Select Enabled for I/O port 60h/64h emulation support, which in turn, will provide complete legacy USB keyboard support for the operating systems that do not support legacy USB devices. The options are Disabled and **Enabled**.

USB 3.0 Support

Select Enabled for USB 3.0 support. The options are Smart Auto, **Auto**, Enabled, Disabled and Manual.

EHCI1

Select Enabled to enable EHCI (Enhanced Host Controller Interface) support on USB 2.0 connector #1 (-at least one USB 2.0 connector should be enabled for EHCI support.) The options are Disabled and **Enabled**.

EHCI2

Select Enabled to enable EHCI (Enhanced Host Controller Interface) support on USB 2.0 connector #2 (-at least one USB 2.0 connector should be enabled for EHCI support.) The options are Disabled and **Enabled**.

XHCI (Extensible Host Controller Interface) Pre-Boot Drive

Select Enabled to enable XHCI (Extensible Host Controller Interface) support on a pre-boot drive specified by the user. The options are Enabled and **Disabled**.

►SATA Configuration

When this submenu is selected, AMI BIOS automatically detects the presence of the SATA devices that are supported by the Intel PCH chip and displays the following items:

SATA Controller

This item enables or disables the onboard SATA controller supported by the Intel PCH chip. The options are **Enabled** and Disabled.

Configure SATA as

Select IDE to configure a SATA drive specified by the user as an IDE drive. Select AHCI to configure a SATA drive specified by the user as an AHCI drive. Select RAID to configure a SATA drive specified by the user as a RAID drive. The options are IDE, **AHCI**, and RAID.

**If the item above "Configure SATA as" is set to AHCI, the following items will display:*

Support Aggressive Link Power Management

When this item is set to Enabled, the SATA AHCI controller manages the power usage of the SATA link. The controller will put the link to a low power state when the I/O is inactive for an extended period of time, and the power state will return to normal when the I/O becomes active. The options are **Enabled** and Disabled.

SATA Port 0~ Port 5

This item displays the information detected on the installed SATA drive on the particular SATA port.

- Model number of drive and capacity
- Software Preserve Support

Port 0~ Port 5

Select Enabled to enable a SATA port specified by the user. The options are Disabled and **Enabled**.

Port 0 ~ Port 5 Hot Plug

Select Enabled to enable hot-plugging support for a port specified by the user, which will allow the user to replace a SATA disk drive installed on this port without shutting down the system. The options are **Enabled** and Disabled.

Port 0 ~ Port 5 Spin Up Device

On an edge detect from 0 to 1, select Enabled to allow the PCH to initialize the device. The options are Enabled and **Disabled**.

Port 0 ~ Port 5 SATA Device Type

Use this item to specify if the SATA port specified by the user should be connected to a Solid State drive or a Hard Disk Drive. The options are **Hard Disk Drive** and Solid State Drive.

**If the item above "Configure SATA as" is set to IDE, the following items will display:*

Serial ATA Port 0~ Port 5

This item indicates that a SATA port specified by the user is installed (present) or not.

Port 0 ~ Port 5 SATA Device Type (Available when a SATA port is detected)

Use this item to specify if the SATA port specified by the user should be connected to a Solid State drive or a Hard Disk Drive. The options are **Hard Disk Drive** and Solid State Drive.

**If the item above "Configure SATA as" is set to RAID, the following items will display:*

Support Aggressive Link Power Management

When this item is set to Enabled, the SATA AHCI controller manages the power usage of the SATA link. The controller will put the link to a low power state when the I/O is inactive for an extended period of time, and the power state will return to normal when the I/O becomes active. The options are **Enabled** and Disabled.

SATA RAID Option ROM/UEFI Driver

Select EFI to load the EFI driver for system boot. Select Legacy to load a legacy driver for system boot. The options are Disabled, EFI, and **Legacy**.

SATA/sSATA RAID Boot Select

Select SATA Controller to boot the system from a SATA RAID device. Select sSATA Controller to boot the system from a S-SATA RAID device. Select Both to boot the system either from a SATA RAID device or from an sSATA RAID device. Please note that the option-Both is not supported by the Windows Server 2012/R2 OS. The options are Both, SATA Controller, and **sSATA Controller**.

Serial ATA Port 0~ Port 5

This item displays the information detected on the installed SATA drives on the particular SATA port.

- Model number of drive and capacity
- Software Preserve Support

Port 0~ Port 5

Select Enabled to enable a SATA port specified by the user. The options are Disabled and **Enabled**.

Port 0 ~ Port 5 Hot Plug

Select Enabled to enable hot-plugging support for a port specified by the user, which will allow the user to replace a SATA disk drive installed on this port without shutting down the system. The options are **Enabled** and Disabled.

Port 0 ~ Port 5 Spin Up Device

On an edge detect from 0 to 1, set this item to allow the PCH to start a COMRESET initialization to the device. The options are Enabled and **Disabled**.

Port 0 ~ Port 5 SATA Device Type

Use this item to specify if the SATA port specified by the user should be connected to a Solid State drive or a Hard Disk Drive. The options are **Hard Disk Drive** and Solid State Drive.

►sSATA Configuration

When this submenu is selected, AMI BIOS automatically detects the presence of the SATA devices that are supported by the PCH-sSATA controller and displays the following items:

sSATA Controller

This item enables or disables the onboard SATA controller supported by the Intel PCH-sSATA controller. The options are **Enabled** and Disabled.

Configure sSATA as

Select IDE to configure an sSATA drive specified by the user as an IDE drive. Select AHCI to configure an sSATA drive specified by the user as an AHCI drive. Select RAID to configure an sSATA drive specified by the user as a RAID drive. The options are IDE, **AHCI**, and RAID.

**If the item above "Configure sSATA as" is set to AHCI, the following items will display:*

Support Aggressive Link Power Management

When this item is set to Enabled, the SATA AHCI controller manages the power usage of the SATA link. The controller will put the link to a low power state when the I/O is inactive for an extended period of time, and the power state will return to normal when the I/O becomes active. The options are **Enabled** and Disabled.

sSATA Port 0~ Port 3

This item displays the information detected on the installed on the sSATA port. specified by the user.

- Model number of drive and capacity
- Software Preserve Support

sSATA Port 0~ Port 3

Select Enabled to enable an sSATA port specified by the user. The options are Disabled and Enabled.

sSATA Port 0 ~ Port 3 Hot Plug

Select Enabled to enable hot-plugging support for a port specified by the user, which will allow the user to replace a sSATA disk drive installed on this port without shutting down the system. The options are **Enabled** and Disabled.

sSATA Port 0 ~ Port 3 Spin Up Device

On an edge detect from 0 to 1, set this item to allow the PCH to start a COMRE-SET initialization to the device. The options are Enabled and **Disabled**.

Port 0 ~ Port 3 sSATA Device Type

Use this item to specify if the sSATA port specified by the user should be connected to a Solid State drive or a Hard Disk Drive. The options are **Hard Disk Drive** and Solid State Drive.

**If the item above "Configure sSATA as" is set to IDE, the following items will display:*

sSATA Port 0~ Port 3

This item indicates that an sSATA port specified by the user is installed (present) or not.

Port 0 ~ Port 3 sSATA Device Type (Available when a SATA port is detected)

Use this item to specify if the sSATA port specified by the user should be connected to a Solid State drive or a Hard Disk Drive. The options are **Hard Disk Drive** and Solid State Drive.

**If the item above "Configure sSATA as" is set to RAID, the following items will display:*

Support Aggressive Link Power Management

When this item is set to Enabled, the SATA AHCI controller manages the power usage of the SATA link. The controller will put the link to a low power state when the I/O is inactive for an extended period of time, and the power state will return to normal when the I/O becomes active. The options are **Enabled** and Disabled.

sSATA RAID Option ROM/UEFI Driver

Select EFI to load the EFI driver for system boot. Select Legacy to load a legacy driver for system boot. The options are Disabled, EFI, and **Legacy**.

SATA/sSATA RAID Boot Select

Select SATA Controller to use a device supported by the SATA connector for system boot. Select sSATA Controller to use a device supported by the sSATA connector for system boot. The options are SATA Controller, **sSATA Controller**, and Both.

sSATA Port 0~ Port 3

This item displays the information detected on the installed sSATA drives on the particular sSATA port.

- Model number of drive and capacity
- Software Preserve Support

sSATA Port 0~ Port 3

Select Enabled to enable an sSATA port specified by the user. The options are Disabled and Enabled.

sSATA Port 0 ~ Port 3 Hot Plug

This feature designates this port for hot plugging. Set this item to Enabled for hot-plugging support, which will allow the user to replace an sSATA drive without shutting down the system. The options are **Enabled** and Disabled.

sSATA Port 0 ~ Port 3 Spin Up Device

On an edge detect from 0 to 1, set this item to allow the PCH to start a COMRESET initialization to the device. The options are Enabled and **Disabled**.

Port 0 ~ Port 3 sSATA Device Type

Use this item to specify if the sSATA port specified by the user should be connected to a Solid State drive or a Hard Disk Drive. The options are **Hard Disk Drive** and Solid State Drive.

► **Server ME (Management Engine) Configuration**

This feature displays the following system ME configuration settings.

- General ME Configuration
- Operational Firmware Version
- Recovery Firmware Version
- ME Firmware Features
- ME Firmware Status #1
- ME Firmware Status #2
 - Current State
 - Error Code

► **PCIe/PCI/PnP Configuration**

The following PCI information will be displayed:

- PCI Bus Driver Version
- PCI Device Common Settings

The following common PCI-device settings will be displayed:

PCI Latency Timer

Use this item to configure the PCI latency timer for a device installed on a PCI bus. Select 32 to set the PCI latency timer to 32 PCI clock cycles. The options are **32**, 64, 96, 128, 160, 192, 224, and 248 (PCI Bus Clocks).

PCI PERR/SERR Support

Select Enabled to allow a PCI device to generate a PERR (PCI/PCI-E Parity Error) or an SERR (System Error) number for a PCI bus error event. The options are Enabled and **Disabled**.

Above 4G Decoding (Available if the system supports 64-bit PCI decoding)

Select Enabled to decode a PCI device that supports 64-bit in the space above 4G Address. The options are Enabled and **Disabled**.

SR-IOV (Available if the system supports Single-Root Virtualization)

Select Enabled for Single-Root IO Virtualization support. The options are Enabled and **Disabled**.

Maximum Payload

Select Auto for the system BIOS to automatically set the maximum payload value for a PCI-E device to enhance system performance. The options are **Auto**, 128 Bytes, and 256 Bytes.

Maximum Read Request

Select Auto for the system BIOS to automatically set the maximum size for a read request for a PCI-E device to enhance system performance. The options are **Auto**, 128 Bytes, 256 Bytes, 512 Bytes, 1024 Bytes, 2048 Bytes, and 4096 Bytes.

ASPM Support

Use this item to set the Active State Power Management (ASPM) level for a PCI-E device. Select Auto for the system BIOS to automatically set the ASPM level based on the system configuration. Select Disabled to disable ASPM support. The options are **Disabled** and Auto.

Warning: Enabling ASPM support may cause some PCI-E devices to fail!

MMIOHBase

Use this item to select the base memory size according to memory-address mapping for the PCH. The base memory size must be between 4032G to 4078G. The options are **56T**, 48T, 24T, 3T, and 2T.

MMIO High Size

Use this item to select the high memory size according to memory-address mapping for the PCH. The options are **256G**, 128G, 512G, and 1024G.

**CPU1 PCI-E 3.0X8 SLOT2 OPROM/CPU2 PCI-E 3.0X8 SLOT3 OPROM/
PCH PCI-E 2.0X4 SLOT4 OPROM/RSC-R4UFF-A2ED16A SLOT1 PCI-E X16
OPROM/Onboard SAS Option ROM**

Select Enabled to enable Option ROM support to boot the computer using a device installed on the slot specified by the user. The options are Disabled, **Legacy** and EFI.

Onboard LAN Option ROM Type

Select Legacy to boot the computer using a Legacy device installed on the motherboard. The options are **Legacy** and EFI.

Onboard LAN1 Option ROM/Onboard LAN2 Option ROM/Onboard Video Option ROM

Use this option to select the type of device installed in LAN Port1, LAN Port2 or the onboard video device used for system boot. The default setting for LAN1 Option ROM is **PXE**, for LAN2 Option ROM is **Disabled**, and for Onboard Video Option ROM is **Legacy**.

VGA Priority

Use this item to select the graphics device to be used as the primary video display for system boot. The options are **Onboard** and **Offboard**.

Network Stack

Select **Enabled** to enable PXE (Preboot Execution Environment) or UEFI (Unified Extensible Firmware Interface) for network stack support. The options are **Enabled** and **Disabled**.

►Super IO Configuration

Super IO Chip AST2400

►Serial Port 1 Configuration/Serial Port 2 Configuration

Serial Port 1 Configuration/Serial Port 2 Configuration

Select **Enabled** to enable the onboard serial port specified by the user. The options are **Enabled** and **Disabled**.

Device Settings

This item displays the base I/O port address and the Interrupt Request address of a serial port specified by the user.

Change Port 1 Settings/Change Port 2 Settings

This feature specifies the base I/O port address and the Interrupt Request address of Serial Port 1 or Serial Port 2. Select **Auto** for the BIOS to automatically assign the base I/O and IRQ address to a serial port specified.

The options for Serial Port 1 are **Auto**, (IO=3F8h; IRQ=4), (IO=3F8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12), (IO=2F8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12); (IO=3E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12), and (IO=2E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12).

The options for Serial Port 2 are **Auto**, (IO=2F8h; IRQ=3), (IO=3F8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12), (IO=2F8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12); (IO=3E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12), and (IO=2E8h; IRQ=3, 4, 5, 6, 7, 9, 10, 11, 12).

Serial Port 2 Attribute

Select SOL to use COM Port 2 as a Serial_Over_LAN (SOL) port for console redirection. The options are COM and **SOL**.

►Serial Port Console Redirection

COM 1 (Available when COM1 port is detected)

COM 1 Console Redirection

Select Enabled to enable COM Port 1 Console Redirection, which will allow a client machine to be connected to a host machine at a remote site for networking. The options are **Disabled** and Enabled.

**If the item above set to Enabled, the following items will become available for configuration:*

►COM1 Console Redirection Settings (Available when COM1 port is detected)

Terminal Type

This feature allows the user to select the target terminal emulation type for Console Redirection. Select VT100 to use the ASCII Character set. Select VT100+ to add color and function key support. Select ANSI to use the Extended ASCII Character Set. Select VT-UTF8 to use UTF8 encoding to map Unicode characters into one or more bytes. The options are ANSI, VT100, **VT100+**, and VT-UTF8.

Bits Per second

Use this item to set the transmission speed for a serial port used in Console Redirection. Make sure that the same speed is used in the host computer and the client computer. A lower transmission speed may be required for long and busy lines. The options are 9600, 19200, 38400, 57600 and **115200** (bits per second).

Data Bits

Use this feature to set the data transmission size for Console Redirection. The options are 7 (Bits) and **8 (Bits)**.

Parity

A parity bit can be sent along with regular data bits to detect data transmission errors. Select Even if the parity bit is set to 0, and the number of 1's in data bits is even. Select Odd if the parity bit is set to 0, and the number of 1's in data bits is odd. Select None if you do not want to send a parity bit with your data bits in transmission. Select Mark to add a mark as a parity bit to be sent along with

the data bits. Select Space to add a Space as a parity bit to be sent with your data bits. The options are **None**, Even, Odd, Mark and Space.

Stop Bits

A stop bit indicates the end of a serial data packet. Select 1 Stop Bit for standard serial data communication. Select 2 Stop Bits if slower devices are used. The options are **1** and **2**.

Flow Control

Use this item to set the flow control for Console Redirection to prevent data loss caused by buffer overflow. Send a "Stop" signal to stop sending data when the receiving buffer is full. Send a "Start" signal to start sending data when the receiving buffer is empty. The options are **None** and Hardware RTS/CTS.

VT-UTF8 Combo Key Support

Select Enabled to enable VT-UTF8 Combination Key support for ANSI/VT100 terminals. The options are **Enabled** and Disabled.

Recorder Mode

Select Enabled to capture the data displayed on a terminal and send it as text messages to a remote server. The options are **Disabled** and Enabled.

Resolution 100x31

Select Enabled for extended-terminal resolution support. The options are Disabled and **Enabled**.

Legacy OS Redirection Resolution

Use this item to select the number of rows and columns used in Console Redirection for legacy OS support. The options are 80x24 and **80x25**.

Putty KeyPad

This item selects Function Keys and KeyPad settings for Putty, which is a terminal emulator designed for the Windows OS. The options are **VT100**, LINUX, XTERMR6, SCO, ESCN, and VT400.

Redirection After BIOS Post

Use this item to enable or disable legacy Console Redirection after BIOS POST. When the option-Bootloader is selected, legacy Console Redirection is disabled before booting the OS. When Always Enable is selected, legacy Console Redirection remains enabled upon OS bootup. The options are **Always Enable** and Bootloader.

SOL/COM2

SOL/COM2 Console Redirection

Select Enabled to use the SOL/COM2 port for Console Redirection. The options are **Enabled** and Disabled.

**If the item above set to Enabled, the following items will become available for user's configuration:*

► SOL/COM2 Console Redirection Settings

Use this feature to specify how the host computer will exchange data with the client computer, which is the remote computer used by the user.

Terminal Type

Use this feature to select the target terminal emulation type for Console Redirection. Select VT100 to use the ASCII Character set. Select VT100+ to add color and function key support. Select ANSI to use the Extended ASCII Character Set. Select VT-UTF8 to use UTF8 encoding to map Unicode characters into one or more bytes. The options are ANSI, VT100, **VT100+**, and VT-UTF8.

Bits Per second

Use this feature to set the transmission speed for a serial port used in Console Redirection. Make sure that the same speed is used in the host computer and the client computer. A lower transmission speed may be required for long and busy lines. The options are 9600, 19200, 38400, 57600 and **115200** (bits per second).

Data Bits

Use this feature to set the data transmission size for Console Redirection. The options are 7 (Bits) and **8 (Bits)**.

Parity

A parity bit can be sent along with regular data bits to detect data transmission errors. Select Even if the parity bit is set to 0, and the number of 1's in data bits is even. Select Odd if the parity bit is set to 0, and the number of 1's in data bits is odd. Select None if you do not want to send a parity bit with your data bits in transmission. Select Mark to add a mark as a parity bit to be sent along with the data bits. Select Space to add a Space as a parity bit to be sent with your data bits. The options are **None**, Even, Odd, Mark and Space.

Stop Bits

A stop bit indicates the end of a serial data packet. Select 1 Stop Bit for standard serial data communication. Select 2 Stop Bits if slower devices are used. The options are **1** and 2.

Flow Control

Use this feature to set the flow control for Console Redirection to prevent data loss caused by buffer overflow. Send a "Stop" signal to stop sending data when the receiving buffer is full. Send a "Start" signal to start data-sending when the receiving buffer is empty. The options are **None** and Hardware RTS/CTS.

VT-UTF8 Combo Key Support

Select Enabled to enable VT-UTF8 Combination Key support for ANSI/VT100 terminals. The options are **Enabled** and Disabled.

Recorder Mode

Select Enabled to capture the data displayed on a terminal and send it as text messages to a remote server. The options are **Disabled** and Enabled.

Resolution 100x31

Select Enabled for extended-terminal resolution support. The options are Disabled and **Enabled**.

Legacy OS Redirection Resolution

Use this feature to select the number of rows and columns used in Console Redirection for legacy OS support. The options are 80x24 and **80x25**.

Putty KeyPad

This feature selects Function Keys and KeyPad settings for Putty, which is a terminal emulator designed for the Windows OS. The options are **VT100**, LINUX, XTERMR6, SCO, ESCN, and VT400.

Redirection After BIOS Post

Use this feature to enable or disable legacy Console Redirection after BIOS POST (Power-On Self-Test). When this feature is set to Bootloader, legacy Console Redirection is disabled before booting the OS. When this feature is set to Always Enable, legacy Console Redirection remains enabled upon OS boot. The options are **Always Enable** and Bootloader.

Serial Port for Out-of-Band Management/Windows Emergency Management Services (EMS)

The submenu allows the user to configure Console Redirection settings to support Out-of-Band Serial Port management.

EMS Console Redirection

Select Enabled to use a COM port selected by the user for EMS Console Redirection. The options are Enabled and **Disabled**.

**If the item above set to Enabled, the following items will become available for user's configuration:*

►EMS Console Redirection Settings (Available when EMS Console Redirection is enabled)

Use this feature to specify how the host computer will exchange data with the client computer, which is the remote computer used by the user.

Out-of-Band Management Port

The feature selects a serial port in a client server to be used by the Windows Emergency Management Services (EMS) to communicate with a remote host server. The options are **COM1 (Console Redirection)** and SOL/COM2 (Console Redirection).

Terminal Type

Use this feature to select the target terminal emulation type for Console Redirection. Select VT100 to use the ASCII character set. Select VT100+ to add color and function key support. Select ANSI to use the extended ASCII character set. Select VT-UTF8 to use UTF8 encoding to map Unicode characters into one or more bytes. The options are ANSI, VT100, VT100+, and **VT-UTF8**.

Bits Per Second

This item sets the transmission speed for a serial port used in Console Redirection. Make sure that the same speed is used in both host computer and the client computer. A lower transmission speed may be required for long and busy lines. The options are 9600, 19200, 57600, and **115200** (bits per second).

Flow Control

Use this item to set the flow control for Console Redirection to prevent data loss caused by buffer overflow. Send a "Stop" signal to stop data-sending when the receiving buffer is full. Send a "Start" signal to start data-sending when the receiving buffer is empty. The options are **None**, Hardware RTS/CTS, and Software Xon/Xoff.

The setting for each these features is displayed: Data Bits, Parity, Stop Bits

►Trusted Computing (Available when a TPM device is installed)

Configuration

Security Device Support

If this feature and the TPM jumper on the motherboard are both set to Enabled, onboard security devices will be enabled for TPM (Trusted Platform Module) sup-


port to enhance data integrity and network security. Please reboot the system for a change on this setting to take effect. The options are Enabled and **Disabled**.

TPM State

Select Enabled to use TPM (Trusted Platform Module) settings to enhance system data security. Please reboot your system for any change on the TPM state to take effect. The options are Disabled and **Enabled**.


Pending Operation

Use this item to schedule a TPM-related operation to be performed by a security device for system data integrity. Your system will reboot to carry out a pending TPM operation. The options are **0**, Enable Take Ownership, Disable Take Ownership, and TPM Clear.

 **Note:** Your system will reboot to carry out a pending TPM operation.

Current Status Information

This item displays the status of the TPM support on this motherboard.

 **Note:** For more information on TPM, please refer to the TPM manual at <http://www.supermicro.com/manuals/other/TPM.pdf>.

►ACPI Settings

WHEA Support

Select Enabled to support the Windows Hardware Error Architecture (WHEA) platform and provide a common infrastructure for the system to handle hardware errors within the Windows OS environment to reduce system crashes and to enhance system recovery and health monitoring. The options are **Enabled** and Disabled.

High Precision Timer

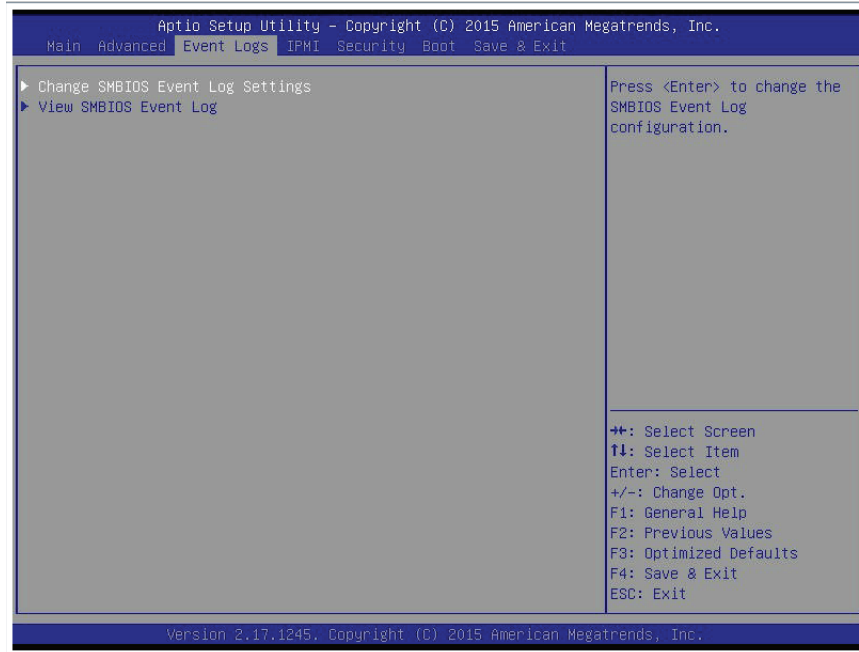
Select Enabled to activate the High Precision Event Timer (HPET) that produces periodic interrupts at a much higher frequency than a Real-time Clock (RTC) does in synchronizing multimedia streams, providing smooth playback and reducing the dependency on other timestamp calculation devices, such as an x86 RDTSC Instruction embedded in the CPU. The High Performance Event Timer is used to replace the 8254 Programmable Interval Timer. The options are **Enabled** and Disabled.

NUMA (Available when the OS supports this feature)

Select Enabled to enable Non-Uniform Memory Access support to enhance system performance. The options are **Enabled** and Disabled.

4-4 Event Logs

Use this feature to configure Event Log settings.



► Change SMBIOS Event Log Settings

This feature allows the user to configure SMBIOS Event settings.

Enabling/Disabling Options

SMBIOS Event Log

Select Enabled to enable SMBIOS (System Management BIOS) Event Logging during system boot. The options are **Enabled** and Disabled.

Runtime Error Logging Support

Select Enable to support Runtime Error Logging. The options are **Enable** and Disable.

Erasing Settings

Erase Event Log

Select Enabled to erase all error events in the SMBIOS (System Management BIOS) log before an event logging is initialized at bootup. The options are **No** and Yes.

When Log is Full

Select Erase Immediately to immediately erase all errors in the SMBIOS event log when the event log is full. Select Do Nothing for the system to do nothing when the SMBIOS event log is full. The options are **Do Nothing** and Erase Immediately.

SMBIOS Event Log Standard Settings

Log System Boot Event

Select Enabled to log system boot events. The options are **Disabled** and Enabled.

MECI (Multiple Event Count Increment)

Enter the increment value for the multiple event counter. Enter a number between 1 to 255. The default setting is **1**.

METW (Multiple Event Count Time Window)

This item is used to determine how long (in minutes) the multiple event counter should wait before generating a new event log. Enter a number between 0 to 99. The default setting is **60**.



Note: Please reboot the system for the changes to take effect.

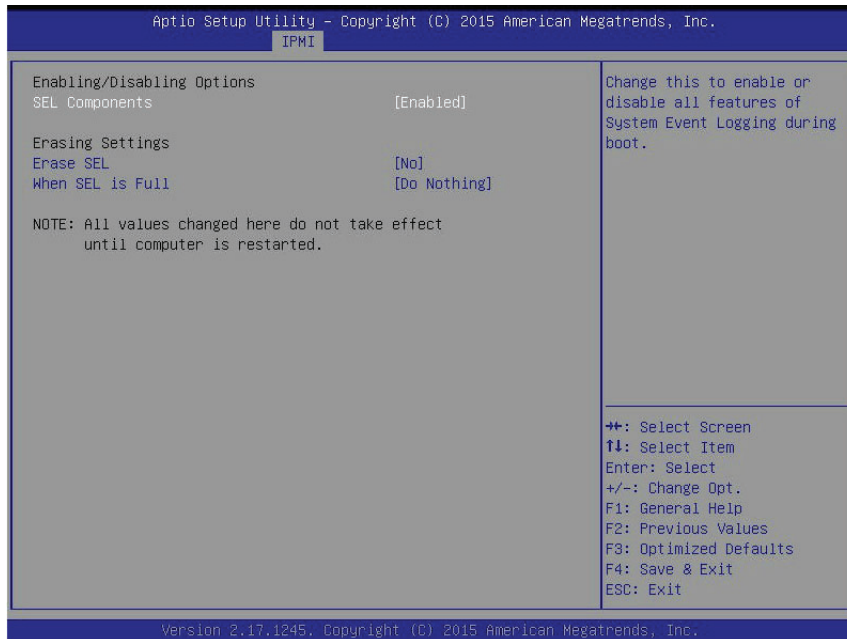
►View SMBIOS Event Log

This item allows the user to view the event in the SMBIOS event log. Select this item and press <Enter> to view the status of an event in the log. The following categories are displayed:

Date/Time/Error Code/Severity

4-5 IPMI

Use this feature to configure Intelligent Platform Management Interface (IPMI) settings.



IPMI Firmware Revision

This item indicates the IPMI firmware revision used in your system.

IPMI Status

This item indicates the status of the IPMI firmware installed in your system.

► System Event Log

Enabling/Disabling Options

SEL Components

Select Enabled to enable all system event logging support at bootup. The options are **Enabled** and Disabled.

Erasing Settings

Erase SEL

Select Yes, On next reset to erase all system event logs upon next system reboot. Select Yes, On every reset to erase all system event logs upon each system reboot. Select No to keep all system event logs after each system reboot. The options are **No**, Yes, On next reset, and Yes, On every reset.

When SEL is Full

This feature allows the user to determine what the AMI BIOS should do when the system event log is full. Select Erase Immediately to erase all events in the log when the system event log is full. The options are **Do Nothing** and Erase Immediately.



Note: After making changes on a setting, be sure to reboot the system for the changes to take effect.

►BMC Network Configuration

The following items will be displayed:

- IPMI LAN Selection
- IPMI Network Link Status

Update IPMI LAN Configuration

Select Yes for the system BIOS to automatically reset the following IPMI settings at the next system boot. The options are Yes and **No**.

Configuration Address Source (Available when the item above - Update IPMI LAN Configuration is set to Yes)

Use this item to select the IP address source for this computer. If Static is selected, you will need to know the IP address of this computer and enter it to the system manually in the field. If DHCP is selected, AMI BIOS will search for a DHCP (Dynamic Host Configuration Protocol) server attached to the network and request the next available IP address for this computer. The options are **DHCP** and Static.

Station IP Address

This item displays the Station IP address for this computer. This should be in decimal and in dotted quad form (i.e., 192.168.10.253).

Subnet Mask

This item displays the sub-network that this computer belongs to. The value of each three-digit number is separated by dots and it should not exceed 255.

Station MAC Address

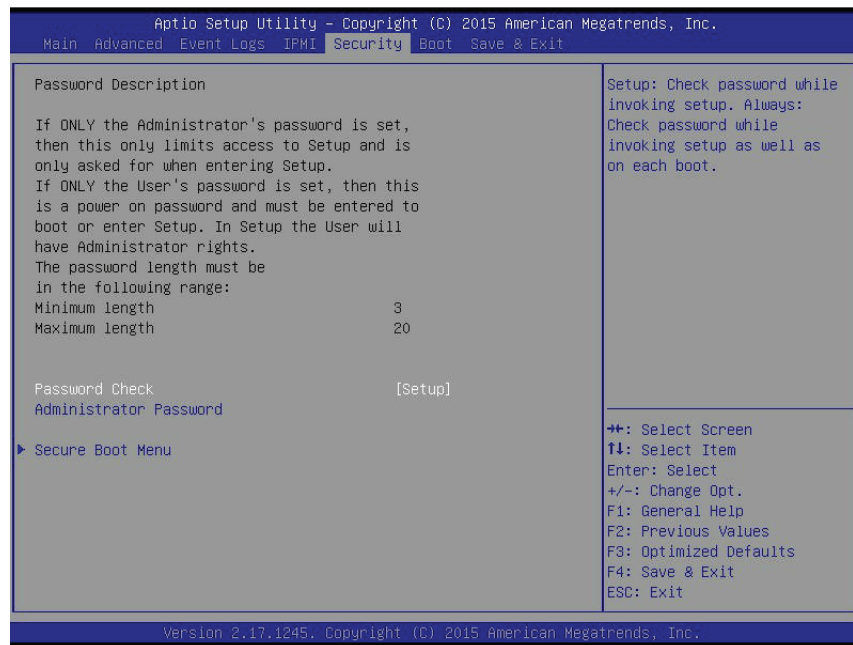
This item displays the Station MAC address for this computer. Mac addresses are 6 two-digit hexadecimal numbers.

Gateway IP Address

This item displays the Gateway IP address for this computer. This should be in decimal and in dotted quad form (i.e., 192.168.10.253).

4-6 Security Settings

This menu allows the user to configure the following security settings for the system.



Password Check

Select Setup for the system to prompt for a password when the user's entering the BIOS setup utility. Select Always for the system to prompt for a password at bootup and when the user's entering the BIOS Setup utility. The options are **Setup** and Always.

Administrator Password

Use this feature to set the administrator password which is required before the user entering the BIOS setup utility. The length of the password should be from 3 characters to 20 characters long.

User Password

Use this feature to set the user password which is required to enter the BIOS setup utility. The length of the password should be from 3 characters to 20 characters long.

► Secure Boot Menu

Secure Boot

Select Enable for secure boot support to ensure system security at bootup. The options are Enabled and **Disabled**.

Secure Boot Mode

This item allows the user to select the desired secure boot mode for the system. The options are Standard and **Custom**.

CSM Support

Select Enabled to support Compatibility Support Module (CSM) to enhance system security. The options are **Enabled** and Disabled.

►Key Management

Default Keys Provision

Select Enable to install all manufacturer defaults for the following system security settings. The options are **Disabled** and Enabled.

►Enroll All Factor Default Keys

Select Enable to install all manufacturer defaults for the following system security settings. The options are **Yes** and No.

►Save All Secure Boot Variables

This feature allows the user to save the secure boot settings specified by the user.

Enroll All Factory Default Keys

This feature allows the user to store security-related boot data in a file of the same named in the system root folder of your computer.

Platform Key (PK)

►Delete PK

Select <Yes> to confirm deletion of the Platform Key (PK) from the NVRAM (Non-Volatile RAM).

►Set New Key

Select <Yes> to load the manufacture_default platform keys for your system. Select No to load the default settings from other sources.

Key Exchange Key (KEK)

Select <Yes> to confirm KEK support to enhance system security.

► Delete KEK (Key Exchange Key)

Select <Yes> to confirm deletion of the KEK from the NVRAM (Non-Volatile RAM).

► Set New KEK (Key Exchange Key)

Select <Yes> to confirm that a new KEK will be set in the NVRAM (Non-Volatile RAM).

► Append KEK (Key Exchange Key)

Select <Yes> to load the new KEK from the manufacturer defaults. Select <No> to load the new KEK from other sources.

Authorized Signatures**► Delete DB (DataBase)**

Select <Yes> to confirm deletion of a database from the NVRAM (Non-Volatile RAM).

► Set New DB (DataBase)

Select <Yes> to confirm that a new database will be set in the NVRAM (Non-Volatile RAM).

► Append DB (DataBase)

Select <Yes> to load the new database from the manufacturer defaults. Select <No> to load the new database from other sources

Authorized TimeStamps**► Delete DBT (DataBase Timer)**

Select <Yes> to confirm deletion of the database timer from the NVRAM (Non-Volatile RAM).

► Set New DBT (DataBase Timer)

Select <Yes> to confirm that the new database timer will be set in the NVRAM (Non-Volatile RAM).

► Append DBT (DataBase Timer)

Select <Yes> to load the new database timer from the manufacturer defaults. Select <No> to load the new database timer from other sources

Forbidden Signatures

▶Delete DBX

Select <Yes> to confirm deletion of the DBX files from the Non-Volatile RAM (NVRAM).

▶Set New DBX

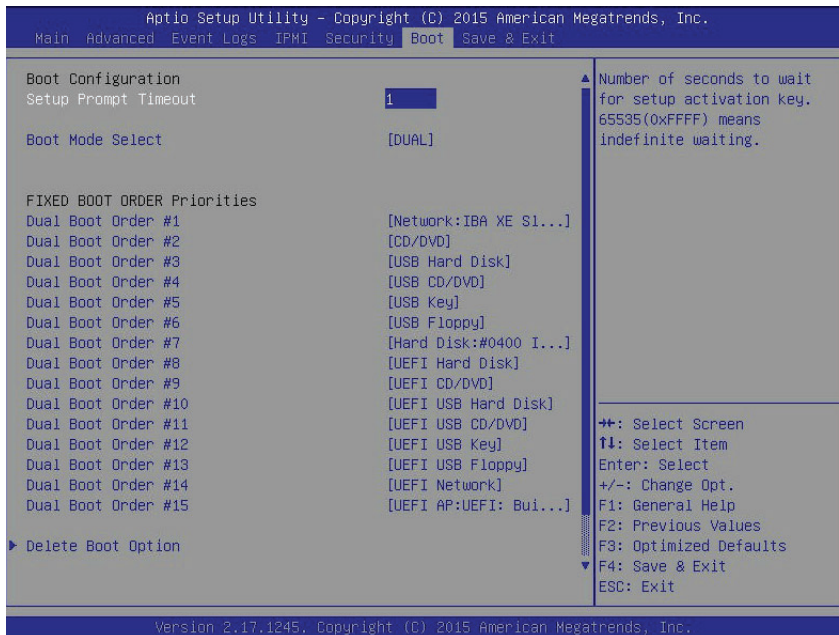
Select <Yes> to confirm that the new DBX files will be downloaded to the Non-Volatile RAM (NVRAM).

▶Append DBX (DataBase Timer)

Select <Yes> to load the new DBX files from the manufacturer defaults. Select <No> to load the new DBX files from other sources.

4-7 Boot Settings

Use this feature to configure Boot Settings:



Boot Configuration

Setup Prompt Timeout

Use this item to indicate how many seconds the system shall wait for the BIOS setup activation key to respond before the system starts to boot. The default setting is **1**.

Boot Mode Select

Use this item to select the type of device to be used for system boot. The options are Legacy, UEFI, and **Dual**.

Fixed Boot Order Priorities

This option prioritizes the order of bootable devices from which the system will boot. Press <Enter> on each entry from top to bottom to select devices.

- Dual Boot Order #1
- Dual Boot Order #2
- Dual Boot Order #3
- Dual Boot Order #4
- Dual Boot Order #5
- Dual Boot Order #6

- Dual Boot Order #7
- Dual Boot Order #8
- Dual Boot Order #9
- Dual Boot Order #10
- Dual Boot Order #11
- Dual Boot Order #12
- Dual Boot Order #13
- Dual Boot Order #14
- Dual Boot Order #15

► **Delete Boot Option**

Use this item to select a boot device to delete from the boot priority list.

Delete Boot Option

Select the target boot device to delete.

► **Hard Disk Drive BBS Priorities**

- Legacy Boot Order #1
- Legacy Boot Order #2

► **Network Drive BBS Priorities**

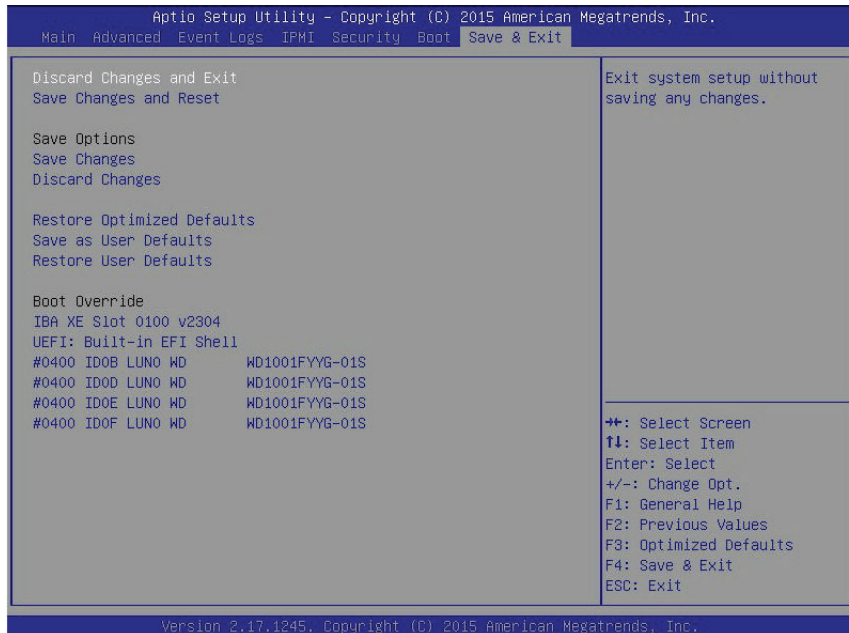
- Legacy Boot Order #1

► **UEFI Application Boot Priorities**

- UEFI Boot Order #1

4-8 Save & Exit

Select the Save & Exit tab from the BIOS setup screen to configure the settings below.



Discard Changes and Exit

Select this option to quit the BIOS setup without making any permanent changes to the system configuration, and reboot the computer. Select Discard Changes and Exit from the Exit menu and press <Enter>.

Save Changes and Reset

When you have completed the system configuration changes, select this option to leave the BIOS setup utility and reboot the computer for the new system configuration parameters to take effect. Select Save Changes and Exit from the Exit menu and press <Enter>.

Save Options

Save Changes

When you have completed the system configuration changes, select this option to save all changes made. This will not reset (reboot) the system.

Discard Changes

Select this option and press <Enter> to discard all the changes and return to the AMI BIOS setup utility.

Restore Optimized Defaults

To set this feature, select Restore Optimized Defaults from the Exit menu and press <Enter>. These are manufacturer default settings designed for maximum system performance but not for maximum stability.

Save as User Defaults

To set this feature, select Save as User Defaults from the Exit menu and press <Enter>. This enables the user to save any changes to the BIOS setup for future use.

Restore User Defaults

To set this feature, select Restore User Defaults from the Exit menu and press <Enter>. Use this feature to retrieve user-defined settings that were previously saved.

Boot Override

This feature allows the user to override the Boot priorities sequence in the Boot menu, and immediately boot the system with another device specified by the user. This is a one-time override.

Appendix A

Software Installation Instructions


A-1 Installing Software Programs

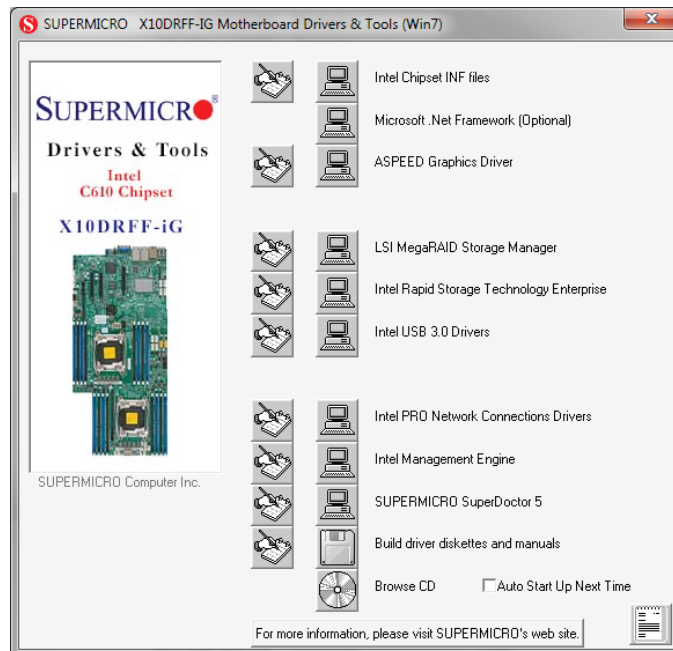
The Supermicro website that contains drivers and utilities for your system is located at <http://www.supermicro.com/wftp>. Some of these must be installed, such as the chipset driver.

After accessing the product drivers and utilities page, go into the CDR_Images directory and locate the ISO file for your motherboard. Download this file to create a DVD of the drivers and utilities it contains. (You may also use a utility to extract the ISO file if preferred.)

After creating a DVD with the ISO files, insert the disk into the DVD drive on your system and the display shown in the picture below should appear.

Another option is to go to the Supermicro website at <http://www.supermicro.com/products/>. Find the product page for your motherboard here, where you may download individual drivers and utilities to your hard drive or a USB flash drive and install from there.

 **Note:** Please refer to the documents posted on our website at <http://www.supermicro.com/support/manuals/> for additional instructions that may be applicable to your system.



Driver/Tool Installation Display Screen


Click the icons showing a hand writing on the paper to view the readme files for each item. Click a computer icon to the right of an item to install an item (from top to the bottom) one at a time. After installing each item, you must reboot the system before proceeding with the next item on the list. The bottom icon with a CD on it allows you to view the entire contents of the CD.

When making a storage driver diskette by booting into a driver CD, please set the SATA Configuration to "Compatible Mode" and configure SATA as IDE in the BIOS Setup. After making the driver diskette, be sure to change the SATA settings back to your original settings.

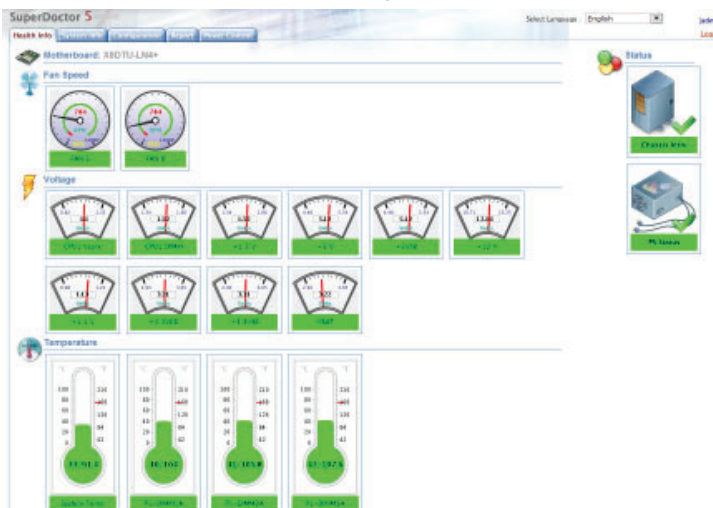
A-2 Installing SuperDoctor5


The Supermicro SuperDoctor 5 is a hardware monitoring program that functions in a command-line or web-based interface in Windows and Linux operating systems. The program monitors system health information such as CPU temperature, system voltages, system power consumption, fan speed, and provides alerts via email or Simple Network Management Protocol (SNMP).

SuperDoctor 5 comes in local and remote management versions and can be used with Nagios to maximize your system monitoring needs. With SuperDoctor 5 Management Server (SSM Server), you can remotely control power on/off and reset chassis intrusion for multiple systems with SuperDoctor 5 or IPMI. SD5 Management Server monitors HTTP and SMTP services to optimize the efficiency of your operation.

 **Note:** The default Username and Password for SuperDoctor 5 is ADMIN / ADMIN.

SuperDoctor 5 Interface Display Screen (Health Information)



 **Note:** The SuperDoctor 5 program and user's manual can be downloaded from the Supermicro website at http://www.supermicro.com/products/nfo/sms_sd5.cfm.

A-3 Logging into the BMC (Baseboard Management Controller)

Supermicro ships standard products with a unique password for the BMC user. This password can be found on a label on the motherboard.

When logging in to the BMC for the first time, please use the unique password provided by Supermicro to log in. You can change the unique password to a user name and password of your choice for subsequent logins.

For more information regarding BMC passwords, please visit our website at <http://www.supermicro.com/bmcpassword>.

Appendix B

UEFI BIOS Recovery Instructions

Warning: Do not upgrade the BIOS unless your system has a BIOS-related issue. Flashing the wrong BIOS can cause irreparable damage to the system. In no event shall Supermicro be liable for direct, indirect, special, incidental, or consequential damages arising from a BIOS update. If you need to update the BIOS, do not shut down or reset the system while the BIOS is updating to avoid possible boot failure.

B-1 An Overview to the UEFI BIOS

The Unified Extensible Firmware Interface (UEFI) provides a software-based interface between the operating system and the platform firmware in the pre-boot environment. The UEFI specification supports an architecture-independent mechanism for add-on card initialization to allow the UEFI OS loader, which is stored in the add-on card, to boot the system. The UEFI offers a clean, hands-off control to a computer system at bootup.

B-2 How to Recover the UEFI BIOS Image (-the Main BIOS Block)

A UEFI BIOS flash chip consists of a recovery BIOS block and a main BIOS block (a main BIOS image). The boot block contains critical BIOS codes, including memory detection and recovery codes for the user to flash a new BIOS image if the original main BIOS image is corrupted. When the system power is on, the boot block codes execute first. Once it is completed, the main BIOS code will continue with system initialization and bootup.



Note: Follow the BIOS recovery instructions below for BIOS recovery when the main BIOS boot crashes. However, when the BIOS boot block crashes, you will need to follow the procedures below for BIOS recovery.


B-3 To Recover the Main BIOS Block Using a USB-Attached Device

This feature allows the user to recover a BIOS image using a USB-attached device without additional utilities used. A USB flash device such as a USB Flash Drive, or a USB CD/DVD ROM/RW device can be used for this purpose. However, a USB Hard Disk drive cannot be used for BIOS recovery at this time.

The file system supported by UEFI is FAT (including FAT12, FAT16, and FAT32) installed on a bootable or non-bootable USB-attached device. However, the BIOS might need several minutes to locate the SUPER.ROM file if the media size becomes too large because it contains too many folders and files.

To perform UEFI BIOS recovery using a USB-attached device, follow the instructions below.


1. Using a different machine, copy the "Super.ROM" binary image file into the disc Root "\\" Directory of a USB device or a writeable CD/DVD.

 **Note:** If you cannot locate the "Super.ROM" file in your driver disk, visit our website at www.supermicro.com to download the BIOS image into a USB flash device and rename it "Super.ROM" for BIOS recovery use.

2. Insert the USB device that contains the new BIOS image ("Super.ROM") into your USB drive and power on the system
3. While powering on the system, please keep pressing <Ctrl> and <Home> simultaneously on your keyboard until the following screen (or a screen similar to the one below) displays.


Warning!! Please **stop** pressing the <Ctrl> and <Home> keys immediately when you see the screen (or a similar screen) below; otherwise, it will trigger a system reboot.

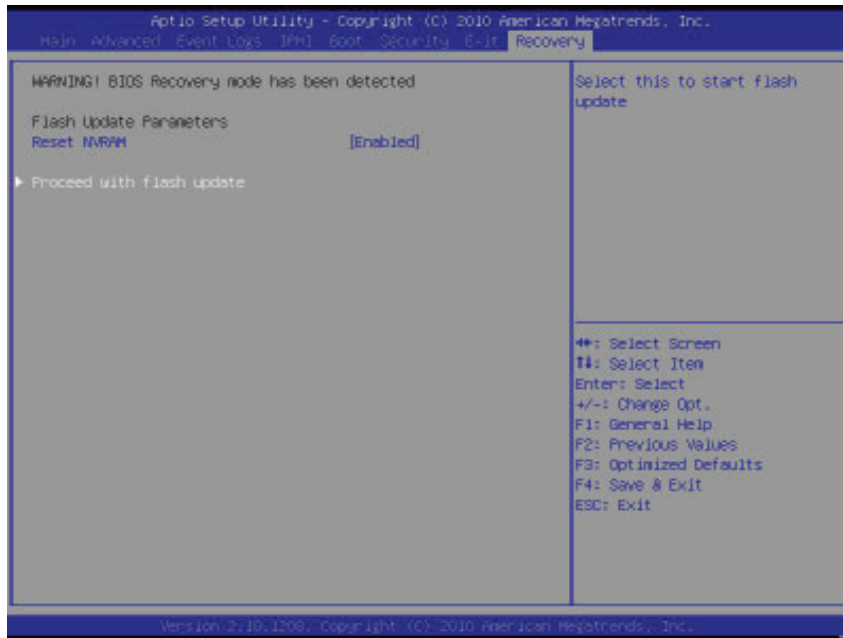


 **Note:** On the other hand, if the following screen displays, please load the "Super.ROM" file to the root folder and connect this folder to the system. (You can do so by inserting a USB device that contains the new "Super.ROM" image to your machine for BIOS recovery.)




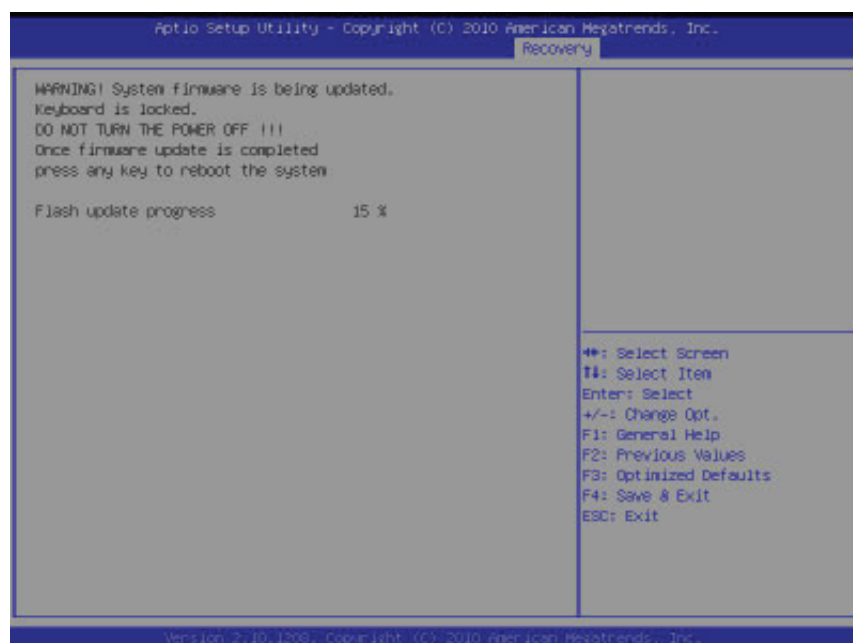
- After locating the new BIOS binary image, the system will enter the BIOS Recovery menu as shown below.

 **Note:** At this point, you may decide if you want to start with BIOS recovery. If you decide to proceed with BIOS recovery, follow the procedures below.

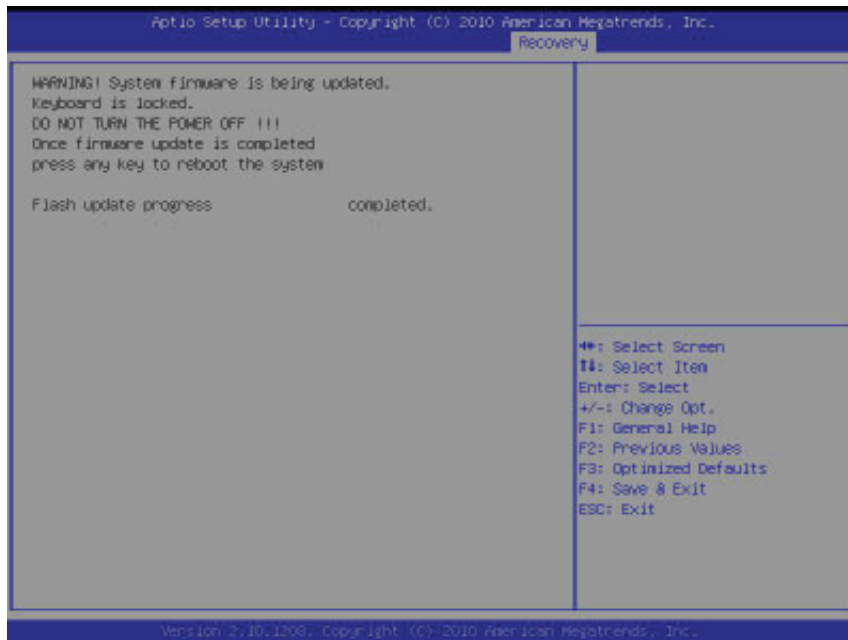


- When the screen as shown above displays, using the arrow key, select the item "Proceed with flash update" and press the <Enter> key. You will see the progress of BIOS recovery as shown in the screen below.

 **Note:** *Do not interrupt* the process of BIOS flashing until it is completed.



6. After the process of BIOS recovery is completed, press any key to reboot the system.



7. Using a different system, extract the BIOS package into a bootable USB flash drive.
8. When a DOS prompt appears, enter FLASH.BAT BIOSname.### at the prompt.



Note: Do not interrupt this process until BIOS flashing is completed.

9. After seeing the message that BIOS update is completed, unplug the AC power cable from the power supply to clear the CMOS, and then plug the AC power cable in the power supply again to power on the system.
10. Press continuously to enter the BIOS Setup utility.
11. Press <F3> to load default settings.
12. After loading default settings, press <F4> to save the settings and exit the BIOS Setup utility.